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Alan's soldering section on the EPE Web site has been reterenced the world over - it is even used by one soldering iron manuiacturer to add to their own information; this booklet has been developed from that material and should be equally highly regarded.

## VERSATILE EVENT COUNTER

Prompted by a reader's telephone request for a unit to help him coun! musical beats, this constructional article iirst shows how a very simple BASIC program on a PC can do the job. It then takes the idea a lot further, presenting a self-contained PIC-microcontrolled design that will monitor the timing of all sorts of events. The results are displayed on an alphanumeric liquid crystal display.

Event signals can be inpul via a built-fn microphone, via a jack-plugged connection to other electronic circuits, and via a panel mounted switch.

There are three monitoring modes:

- Mode 1 displays the time elapsed since monitoring began; the elapsed time at which the last event was detected; the total events counted; the average rate at which the events have been occurring, selectable for counts per second, counts per minute and counts per hour.
- Mode 2 is a frequency counter, principally for monitoring an external electronically generated irequency of $0 \mathrm{~V} /+5 \mathrm{~V}$ amplitude, up to about 20 kHz .
- Mode 3 counts the elapsed time between pairs of events and is of best use with events that are'longer than about one second apart.


## PIC16F87x MICROCONTROLLERS REVIEW

Thousands of you have become famifiar with the PIC family of microcontrollers, especially the PIC16x84 EEPROM versions, Arizona Microchip have now introduced a new range to the family, the PIC16F87x series, In many respects, these new devices can be regarded as greatly enhanced versions of the "84s.
Imporianlly, not only do they have greater capacity than the " 84 s , they offer more facilities that make them ideal candidates for use in many sophisticated designs for which the ' 845 could not readily provide complete control solutions.
Of particular importance are their several on-chip analogue-to-digital conventers (ADCs), and their communicationsoptions baseo upon internal USART (universal synchronous asynchronous receiver transmitter) protocols.

This review takes an overall look at the capabilities of these much-welcomed new devices.

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$1 \times 0$－imA PANEL PAETEA．FUA vision lace 70mm square Scaled 0－100，Order Rat 756
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3 xilh－FLEX SWiTCHES，wihn neon ontoll kighs，saves leaving things swidhed on．Oriner Ret？ 7
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VOL. 28 No. 3

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## WELCOME

Welcome to the first combined issue of $E P E$ and $E I I$. Over the last couple of weeks we have received a number of comments from readers ahout the "demise" of another electronics hobbyist magazine: one reader even sent us a copy from 1980 pointing out that it carried 12S pages and cost just 60p. I have not worked out the present equivalent of 60 p when intiation is taken into account. but a quick look at the issue told an interesting story.

Back in 1980 the mayazine carried over 70 pages of advenising trom about 100 different companies; in this issue we carry 24 pages of advertising from around 40 different companies. Yes, the hobby in general has declined quite dramatically in those 19 years. In the 'SOs there were six UK magazines aimed at the electronics hohbyist - not including the amateur radio titles - now there are just three.

## MOVING ON

The 1980 copy also tells another story which is significant: Tangerine were selling the Microton 65 "the most adwanced. mosi powertul. mos! expandable microcomputer available" for $£ 69$ plus VAT - it was 6502 based. had IK of RAM plus a cassette interface: Commodore had a page listing their stockists for the PET System " $£ 550$ for a self-contained unit" - it had 8K of RAM and ran at 1 MHz (our Technical Editor. John Becker. still has a 32 K version and still uses it occasionally "as a giant calculator. although not all the keys work anymore!" : : Science of Cambridge carried the Sinclair $\mathrm{ZX80}$ for $\mathbf{E} 79.95$ including VAT - 1 K of RAM with a 32 -character by 24-line TV display: Chromasonics and Comp Shop showed the UK 101 kit (a PE published design) for $\mathbf{E} 199+V A T$ - this boasted 4 K of RAM and you could buy a "colour add-on card" - an cxira 4 K of RAM was $£ 32+$ VAT. finally Powertan had the $\mu$ Comp 50 'as published in Wireless World' - this one cost $£ 225+$ VAT in kit form. had a maths co-processor (although they did hot call it that then) and 8K of "on board memory" - another SK cost $£ 78.50$.

Yes. this was the stan of personal computing in the UK and. although we did not realise it at the time, it would lead many potential electrontes hoblyyists away from the electronics magazines to become what are now referred to as computer nerds. Few people realised that within the next 20 years most LK homes would own a computer of wome sort.
It is, of course, interesting that those computer fanatics are now getting involved in electronics, where you can now use a I'C to program PIC chips to perfonm your specitic tashs. Of course, this leads other readers to complain that too many projects are computer based. Welt, they complained when we introduced i.c.s in preference to transistors - some still do - but in this hobby we feel we must move with the times, otherwise we would still be using valves. I should point out that in 99 per cent of cases it is not essential to have the use of a PC to build our PIC projects.


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# Constructional Project 

# SMOKE <br> ABSOREER 



## BILL MOONEY

## Significantly reduce your inhalation of soldering fumes.

WHEN working with tiny surface mount devices (SMDs). the constructor is drawn closer to the circuit in order to get a clear view of the soldering operation. Anyone who does soldering will have noticed the unwriten faw that smoke always moves towards your face, however much you try to avoid it. Close working with SMDs therefore involves a much higher risk of solder fumes being inhaled and potential bronchial problems.

Athough the smoke absorber described here was designed for use with SMD sol= dering. it can also be used beneficially when soldering "ordinary" components. It is very compact and can be placed close to the circuit board during population. It will remove the dense solder fumes from the immediate area and a built-in charcoal filter will provide a degree of filtration and absorption.

The Smoke Absarber is powered by a standard mains adapior and atter power is applied the operation is sutomatic. The fan
is triggered by the heat from the soldering iron and it switches off after about half a minute unless re-triggered. This is a suitable time delay as the average soldering action is usually within three seconds.

Automatic control is very convenient, as the soldering process tends to be intermittent and remembering to switch the unit on and off is quite a distraction. It is also safer since it reduces the overall current drain from the power pack and can therefore remain in stand by mode for long periods.

## HARMFUL CHEMICALS

The harmiul nature of solder fumes in production enviromments has been investigated in great depth by several organisations during recent years. The main component of common solder flux is a material known variously as rosin or colophony. This is derived from a few species of pine tree and is related to amber, which is a fossil material recovered from the sea or mined.

Rosin is a benign material that can usually be handted and worked without problems. It is found in a surprisingly wide range of industrial and domestic products. Common applicationts include paper. cosmetics and soaps. chewing gum, toolhpaste, vamishes, adhesives and, of course, on violin bows.

A number of people have contact aldergy to colophony. in paper for example, but it is very rare. However, our concem is the risk of inhatation. Like hardwood and coal. rosin is completely safe in normal usage. But in the form of a smoke or very finc particles. these materials can be harmful. especially if they reach our lungs.
The chemical component responsible for the adverse reaction, and indeed the main component of rosin, is abietic acid. This is a member of the terpine family of which turpentine (not the substitule) will be familiar to di.i.y. enthusiasts.
There are many other undesirable components in solder smoke, including lead derivatives and. although it is not acutely toxic. it is best to reduce its intake.

## CLOSE-LP WORK

Surface moum work increases the risk of inhalation of harmful vapours for several reasons. Close-in working means that any snoke breathed in will not have time to disperse and will therefore be more concentrated. There is a tendency to use higher levels of flux beciuse good solderability is so much more imporant in surface mount work.
It is also possible to use solder paste for hand working with SMDs. Solder pastes and creams give off copious quamities of smoke and fumes when heated to the reflow temperature. Reflow takes place just below $200^{\circ} \mathrm{C}$ when the solder appears out of the grey paste as a bright metal. This clean liquid metal flows into the joim. wetting both the componens contact and the printed circuir land to complete the soldering process. Solder paste fumes are mildly corrosive and should be avoided.

Commercial electronie production and servicing operations where full working day exposure is involved, take great care to remove fumes using cosily extraction and filtration equipment. If the amateur envisages a lot of work with solder paste. more elaborate air cleaning or extraction should be considered. Similarly, if working in a very confined spate for long periods a more efficient filtering system than this smoke absorber may be needed.


Fig. 1. Complete circuit diagram for the Smoke Absorber.

This unit will help disperse the fumes reducing the concentration in the immediate working area and a centain portion will be absorbed. Complete removal of finely dispersed material from air is a complex and expensive science. We should not be 100 concerned about intermittent exposure $t 0$ traces of rosin flux. It presents a very low risk and the faint therapeutic aroma of pine from the soldering process has pleasank relaxing associations within the cosy environment of the electronics workshop.

## CIRCUIT DETAILS

The full cireuit diagram for the Smoke Absorber is shown in Fig. I A 12 V 500 mA regulated mains adaptor is needed to power the circuit.

The circuit is required to switch on the fan for a period of about 30 seconds and then shut it down unless it is re-riggered. Since the soldering iron is to be used in close proximity to the smoke absorber, it is logical to use the infrared (i.r.) radiation from the hot iron to start the timing cycle. The elements in the circuit are therefore infraned sensor, amplifier/ifler. pulse shaper. timer and moor driver.
Detection of the heat from the soldering requires a long wave infrared detector. Semiconductor i.r. diodes and transistors are very unresponsive to radiation above about one micron wavelength. The useful heat radiation
from the iron covers the wavelengths longer than about five microns and, therefore, a different type of detector is required.

Fortunately, the passive infrared sensors used in security systems work in the range of five microns to about 20 microns and are ideal for the purpose of soldering iron heal detection. The device selected was the PIS201S which consists of two pyroelectric elements wired in opposition.

At a steady level of heat radiation. the output from this arrangement is zero. The fietd of view of the device is about 45 degrees and the elements are arranged so that the passage of the heat source across this aperture heats up one element before the other. This unbalances the system and a momentary voltage output develops. A built-in f.e.t. (field effect transistor) source follower reduces the loading on the sensor but an ouput of a few millivols is all we have to work with.

The PIS20IS is not the world's fastest device with a maximumi response frequency of a few Henz. The practical circuit, shown in Fig.I, is therefore required to make his low-level signal control the filter fan.

The oulput from the PIS201S (XI) develops across resistor R1 which is the load resistor for the internal source follower. Op.amp IC1 is configured as a low frequency amplifier with its gain rolling off above 20 Hz . The values of the coupling capacitors C . C3, C4, and the value of the feedback capacitor C 2 , optimises the gain in the 0.5 Hz to 5 Hz range.
Transistor TRI provides the final voltage amplification to a level suitable to trigger the type 555 timer. IC2. Whilst the trigger inpul in 1 C 2 pin 2 is held higli by resistor R7, the timer ouput on pin 3 remains low. To star the timer, a negativegoing signal is required to take pin 2 below one-third of the supply voltage.

Diode DI fully discharges C+ on posi-tive-going signals so that a well-defined trigger pulse approaching oV reaches the timer.


Fig.2. Component layout an the Smoke Absorber surface mount p.c.b. (twice life-size).

The timer will remain on for as long as the input on pin 2 is low. The lime cont stant of the $\mathrm{C} 4 / \mathrm{R} 7$ combination is therefore kept shont to give a clear liming period. With the component values suggested for R8 and C5. the timer remains on for about 30 seconds.

The output of the timer, on pin 3. is high during the timing period and it can be connected directly to the motor driver TR2. This is MOSFET device, type ZNV2106. in a surface mount SOT223 package. TR2 has a very low channel on-resistance and can easily handle the 120 mA required by the fan with minimal heating.

Switching the motor on and off results in a considerable ripple in the supply voliage. This must be removed in order to avoid feedback instability in this high gain circuit. A 6 V supply is adequate for the low power circuits and. with a stabilised mains power pack supplying 12 V , there is sufficient headroon to use a 6 V 2 Zener diode (D4) as an effective stabiliser.

The fan specified is a brushless dic. type. These devices are very quiet and electrically clean in operation.

## CONSTRUCTION

This is a surface mount project and it is based on readily=available SMDs. The printed circuit board (p.c.b.) component layout and track pattern are shown in Fig. 2 and Fig.3. respectively. This board is available from the EPE PCB Service. conde 223.

If you choose to make your own board. by any of the techniques as used for "ordinary" through-hole circuit boards, note that a clean surface is very imponant for SM work. If the p.c.b. is less than bright and shiny then a few moments spent cleaning it with a little mild household abrasive will greatly improve solderability.

After polishing, rinse off the copper surface with water or p.c.b. cleaning solvent. A light coat of rework flux from an aerosol will protect the clean surface: When this has dried the board is ready for assembly.

Although the type 0505 and smaller size chip resistors are popular in commercial production. the slightly larger 1206 sized devices are best for hand working of SM circuits, at least until some experience is gained. The overull size of the p.c.b. is determined by the size of the larger devices, in this case the tantalum capacitors. plus ICI and IC?

There are no tight spots in the layout and no particularly sensitive devices. All the resistors can be soldered in place followed by the capacitors and finally the ac live devices.

A nomal 12 W or 15 W soldering iron can be used for SMD placement. Select the finest tip available. Standard non-corrosive rosin-cored solder will suffice for com ponent placement, but finer 26 s.w.g. sil-ver-loaded solder wire is preferred by the author. A pair of non-magnetised weezers is essential for initial placement of the chips.

To solder 'a chip resistor in place. the most basic approach is 10 "fix one end first ${ }^{*}$. Place the selected chip on its p.c.b. lands and hold it in position by pressing it gently with the tip of the eweezers. Now touch the end of the solder wire with the iron tip to coltect a small drop of solder:
componthins

## Resistors

| R1 to R3 | 47k (3 0fl) | Se |
| :---: | :---: | :---: |
| R4, R5, R8 | 1M2 (3 off) |  |
| R6 | 4 k 7 |  |
| R7 | 68 k | Pag |
| R9 | 390ㅇ | Page |
| R10, R11 | 1k8 (20) |  |

R10, R11 1 k 8 (2 oil)
Rx zero ohm link (see text)
All resistors SMD 1206 package.
Capacitors
$\mathrm{Ci} \quad i \mu \mathrm{~F}$ tanialum, $16 \mathrm{~V}, \mathrm{SMD}$
C2 $82 n$ ceramic, SMD 1206 package
C310 C6 $22 \mu$ tantalum, 22V, SMD (4 off)

## Semiconductors

D1 BAS32 signal diode, SMD SOD80 package
D2 redl.e.d. 5 mm
D3 green l.e.d. 5 mm
D4 BZX84 Zener diode, 6.2V, SMD SOT23 package
BCF32 non Iransistor, SMD SOT23 package TR2 ZVN2:06 MOSFET SMD SOT223 package
IC1 LF351 op amp. SMD SO8 package
IC2 NE555 timer, SMD SO8 package
X1 PIS20is i.r. delector

## Miscellaneous

Printed circuit board, available from the EPE PCE Service, code 223: fan, 60 mm 12 V d.c., bushless; power socket, 2.5 mm chassis mounting, 24 mm fixing centres; Le.d. mounting clip. 5 mm , ( 2 off); plastic case, type PB1, 114 mm $\times 76 \mathrm{~mm} \times 38 \mathrm{~mm}$; speaker mesh. Sentinel, galvanised, 22g, $6 \mathrm{~mm} \times 6 \mathrm{~mm}$ holes. cut to 10 holes $\times 10$ holes: filter. chercoal filled; finger guard, 60 mm square, chromed steel ( 2 off); M3 nuts and bolts, 2 cm long (rear finger guard and fan mounting) (4 off); M4 nuts and boits, 6 mm long (fron! finger guard) (4 off); mains power adaptor, 12 V 500 mA , regulated; connecting wire; solder, etc.


Apply this to one end of the component to solder it in place. Speed is important here as the protective flux is rapidly evaporating from the molten solder drop. (Purists who observe the dictum "never
caity soldeì to the joint" can carefully renake the solder joint later if they wish!)

As soun as the flux evaporates. oxidation of the solder surface will begin. Some residue of flux is also required for proper tinning of the component lemminal and the copper pad. Pre-coating of the copper with flux is useful for this reason.

Having fixed one end. the second and can now be soldered in the normal twohanded manner where the solder and iron tip is applied to the joint at the same time.

A similar procedure can be adopied for ICl and 1 C 2 , where two opposing leads can be soldered first to fix an i.c. in place. In all cases, the am should be to apply minimal solder to the joint. Excess solder can be removed with solderwick. More refined methods of hand working with SMDs can be developed. but this method is satisfactory for most purposes.

Note that Zener diode D4 is an SOT23 package and the anode pins. I and 3. both contact the same pad. The signal diode DI is in an SOD 80 cylindrical package and therefore a litte tricky to solder in place. Component RX (Fig.2) is referted to as a "zero ohm jumper" in SM parlance and is a neat low-inductance way of jumping a p.c.b. track.

In this circuit. a $1 \Omega$ or $10 \Omega 1206$ chip resistor would work just as well. Even a wire link could be used if necessary.

## FAN BDX

The position of the tañ was determined after some experimentation. Uising the smoke generated by a soldering iron and rosin-cored solder it was found that the fan needs to be close to the soldering level if it is to grab most of the smoke. A height of about lcm off the work-surface was found to be the best compromise.

Apart from taking up less bench space, the smoke removal is nore efficien if the box stands on end with the fan near the bottom and the controls at the top. The arrangement of items in the author's box is shown in Fig. 4.

All holes must be drilled in the box before any units are added. The fan opening was made with a standard 5.7 cm diameter hole-sasy. This must be done slowly because the plastic melts easily.

Next, the four bolt holes for each of the finger guards are marked and drilled. It can be seen from Fig. 5 that two guards are required. The front guard is mounted on the box lid and it holds filter material in place. The second guard is mounted on the rear of the box and it shares the four fan mounting bolts.


Enlarged view of the assembled p.c.b.


Fig.4. Internal layout and interconnections of the Smoke Absorber components within its case.
The filter foam is sundwiched between the front finger guard and a wire mesh. The foam is under slight compression and the wire mesh is necessary to prevent it from bulging into the fan blades. If a different size of box is used the sandwich technique may not work as well and the mesh may aeed to share the from guard bolls in order to hold the foam in place. Also. it the box is too deep the electronics may be exposed to the airflow.


Fig.5. Side-on view of the fan assembly.


Internal layout of the Smoke Absorber


The filter consists of open-pored foam which is partly charcoal filled. This type of structure offers very little resistance to the passage of air. Charcoal is an excellent absorber of gaseous contaminants but in this application the flow of air is very fast and absorption may not always be complete.

As the filter material is not available in the size required. it must be cut to fit the smoke absorber. The filter sheet is easily cut with scissors to a circle just larger than the fan opening, about 6 cm in diameter, so that it is a tight fit.

The galvanised mesh is cut to a square just larger than the motor housing. One of the pillars of the box passes through a hole in the mesh and with the pressure of the filter it is adequately held.

A hole must be drilled for the PIR detector (XI) to make a secure fit and a small amount of adhesive from a glue gun wil! hold it in place. The sensor should be fixed so that the window will tee horizontal relative to the work surface.

The propulated p.c.b. is held in place with a little adhesive, so avoiding the need for holes in the board or the box. Two 5 mm mounting clips hold the l.e.d.s in place.

Finally. the interwiting can be contpleted. see Fig.4. Use the finest tlexible wire available for the i.r. sensor and l.e.d. connections. Suitable colour-coded wire (as used for multi-way computer cables)
makes a neater job than the rather heavy hook-up wire commonly available.
The wire in the prototype was taken from a short length of 25 -way screened cable. With an overall diameter of about $0 . S m m$, each wire contains $7 / 0.1 \mathrm{~mm}$ tine strands of tinned copper making it very flexible and ideal for small SM jobs.

Slightly heavier-duty wire is needed for the fin and for the d.c. supply socket. The circuit hoard pids are sufficienily large for these connections.

The motor will not function unless connected to the correct supply polarity. The red wire is connected to the positive supply rail and the blue to the driver transistor. The power connector specilied uses the centre pin as positive and the smoke absorber wiring should match this.

The fan pulls air in at the from opening. It should be mounted with the motor connections and label cowards the rear. This motor contains electronic driver circuitry and the supply polarity cannot be reversed.

Front panel lenering and decoration were composed using a computer drawing package and printed out as a single drawing. This hatel was then trimmed to size and holes cut for the l.e.d.s and the sensor. A standard paper adhesive keeps it in place. An inkjet printer is perfectly adequate for this application as the unit is unlikely to get wet.

## UP AND RUNNING

A visual check of both the on-board component positions and the off-toard wiring is worthwhile. In particular, check the polarity of the rantalum capacitors and make certain that the power supply socket is wired correctly.

On first switch-on. it would be wise to clieck the current drawn by the circuit. In sand-by mode it should draw about 20 mA . rising to about 120 mA in runmode. On tirst application of power the fan will go into run-mode lasting for about 30 seconds and the stand-by current can be checked after this timed period.

The low volage drop across transistor TR2 means that it should run cool even after long on-periods. The stabilised power supply is rated at 500 mA which is well above the requirements of the smoke absorber and only slight warming is expected. Some experimentation will help when it comes to operation of this unit.

To start with. place the filter opening
about 5 cm from a circuit under construction with the exit away from the face. When the fin stops after the 30 seconds timing period it is easily restarted if required by moving the hot iron lip past the sensor at a distance of about 2 cm . The heat from the tip of a finger close to the sensor window will also stan the run cycle. Remember it is heal movemen across the sensor which produces the trigger pulse to stan the timer.

The smoke is warm and rises rapidly aided by the rising hot air stream from the iron tip. This is a strong effect and the stream is difficult to redirect. The path taken by the smoke will the influenced by surrounding objects which will have a chimney effect causing the plume to cling to the tallest one.

Dratts. including the body convection of the operator will also influence he plume so that. overall. its final direction is fairly chisotic. In order to catch every wisp of smoke, the absorber will need to be quiet close to the soldering operation to overcome these effects. The air stream to the fan has only a negligible cooling effect on the iron tip.

A little experimentation soon results in success in catching most of the fumes. The smoke absorber will become an indispensable part of the soldering process. parlicularly for the SMD constructor.



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Imagine an electronics design system that lets you draw schematics onto the screen and then simulate them at the touch of a button. Now imagine pressing another button and seeing the schematic replaced with a PCB rats-nest. Pressing another button starts the autorouter, and finally you can click on File then Save As to create a complete set of CADCAM files.

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# lan Poole looks at why the "paperless" society has not happened yet and also reports on a display that uses 15 million transistors and 1.6 miles of thin film wiring. 

## Paper chase

THE IDEA of the paperless society is a wonderful idea. Whilst the rechnology is available to achieve this in theory. everyone knows that in practice things are rather different. Despite atl the technology that should enable less paper to be used. we actually use much more.

There are many reasons why the paperless office or society has not happened. It is often much easier 10 prist out a document to read at leisure. Also. the fact that we have better and faster printers means it has becone much easier to print documents.

Another reason is that printed sheets are much easier to read. There are a number of reasons for this. Reading a PC monitor causes more eyestrain because it is necessary to scroll the document up and down. In some cases it may be necessary to 700 m in and out to cateh the required detail.

Sometimes these fine details are not so easy to deciplier because of the limited resolution of the screen. Whatever the reason the end result is the same. The documents on the screen are not as easy or convenient to read.

If the paperless office is to succeed then it will be necessary to make the technology more compatible with people's real requirements. Only when it is more convenient to read a documem on the screen will people start to reduce the amount of paper that is produced.

In view of the green issues and cost savings of introducing a real paperless office it is likely that there will be a significant uptake of the solution. As a result several companies are now working towards this end.

## Roentgen

in a programme that has taken place at IBM Rescarch, an active lic.d. display with a resolution of 200 p.p.i. (pixels per inch) has been developed. This active matrix liquid crystal display (AMLCD) has a resoletion that is near the limit of the human eye.

This prototype display is named Roentgen after the famous German physicist, Wilhelm Konrad Roentgen (1845-1923). who discovered X-rays. The display is optimised to produce exceedingly shap images that are virtually indistinguishable from the equivalent paper copies. The jagged edges that diston curved or angled lines are vinually eliminated. making viewing far more pleasant and natural.

The sereen is a further development of a monitor that was known as "Moner". This was a 10.5 inch diagonal 150 p.p.i. Super XGi l.e.d. monitor. However. IBAl have
been researching into high quality displays for many years and started their work into active matrix displays in the mid-1980s.

The prototype "Roentgen" screen is 21.5 inches high and 16.5 inches wide. giving it a $16 \cdot 3$ inch diagonal viewing area. On top of this, the total depth of the whole display unit. including the base, is only 9.5 inches - a delinite improvement on the standard c.r.t. screen. The whole assembly weighs less than 20 lbs , making it less than a third of the weight of a comparable size c.r.t. monitor.

There are also significant power savings over the equivalent c.r.t. It consumes less than half that of an 18 inch c.r.t., and the output luminescence is high. With a 44 W back-light a brightness of $130 \mathrm{~cd} / \mathrm{m}^{2}$ is achieved, making it ideal under virtuatly all normal lighting conditions:

## Many pixels

Crammed into the display are over five million full colour pixets, giving it a full $2560 \times 2048$ line up. To achieve this it has been necessary to use over 15 million transistors. and a slaggering 1.6 miles of thin film wiring within the display.

The exceedingly high performance of the display carries some penalties with it. The screen has over four times the number of pixels of today's highest definition screens of the same size. This means that the amount of data that is required to drive it is increased. Consequently improvements are required in the computer that drives the display if the full benefits of the display are to be realised. and problenis resulting from the increased amounts of data are nut to be encountered.

Realising the possibility of these problems in advance. IBM reseurchers developed a scaleable graphics aduptor architecture. This is compatible with all the curent operating systems and it is capable of handling these types of high image content displays.

A further advantage is that the adaptor is based on off-the-shelf components. This makes it a relatively cheap card to manufacture, unlike one using specialised components that would add significant costs to the whole systent.

Whils: this should help in allowing people to view more data on screen. and thereby reducing the number of copies that are printed out there will also be ottier uses. In the immediate future, while their cost is relatively high. they are more likely to be used in applications that demand very high levels of definition.

In particular. they are likely to be included in the ever-increasing number of databases of digital images. These include digital libraries. architectural and
electronic blueprints, hisiorical archives, and scanned records; including those used by loospials and insurance companies.

It is also expected that with the exceeding!y high level of definition and crispness there will be a high demand for this type of display for graphic design and electronic publishing applications.

## Puture

Whilst there are many areas where these displays can be used. and it is expected that demand will be high. there are no plans to maket the Roentgen (AMI.CD) display at the present time. It is only a prototype used to prove the viability of the technology and the results of the work lave given IBM a very good insight into the development and fabrication of highly complex displays. As such it has been a great success.

However. it will need developing for manufacture and at the moment $1 B M$ are in discussions with a number of customers around the world to investigate its production. This should mean that very high detinition displays will be available in the foresecable future.

To make the paperless office a reality. other developments will be required to ensure that people are als at home with data displayed on a screen as they are with that on paper. There are many reasous for wanting a document to be printed out. The convenience of being able to look at it when required. without requiring a computer, the way it looks, and the way nany of us were brought up to look at paper at school rather than screens.

However. with more computers being fnstalled in schools and the younger generation being more used to them, it is possible they may use less paper. The best way to cut down on paper usage would be to make printers less convenient to use. or much slower. Unfortuntately. this is most unlikely to happen!


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# Neme ows News from the worlit of 

## CODE'S WALLOP?

## Copyright protection takes on a new twist for movie disc buffs Barry Fox reports.

HOurwood is getting tough with the computer industry. The Motion Piccures Association of America is plugging the loopholes which let a PC play unauthorised DVD movie discs. As a result consumers risk buying PC peripherals which will nor work at all.

The DVD Forum, which sets the standards for DVD, requires that all DVD movie discs are encrypted with the Content Scrambling System, to prevent digital copying. DVD discs and standalone players are also Regionally Coded, to let Hollywood stagger the release of videos to tie in with the staggered release of ciñema prints.

Regional coding divides the world into six zones, with North America designated Zone 1 and Europe Zone? Discs of one zone should not play in another.
Manufacturērs of DVD plaỵback equipment need a CSS licence. This requires that consumer DVD players leave the faciory pre-coded, so that discs can be coded for the zone in which they are sold.

Although some European firms have been modifying players so that they play dises of any zone; the UK's Federation Against Copyright Theft recently raided two British dealers and seized Region 1 dises and players which had been modified to play them. FACT can prosecute because the discs were not rated for content and certificated for sale in the UK.

## Locked-On

DVD movies will play on a PC, with display on a TV screen. A DVD-ROM drive and MPEG-2 decoder board cost under $\$ 250$ (rotal) and many new PCs already come fitted with a DVD-ROM drive. Because PCs are international products, the Regional Protection Committee of the DVD Forum initially agreed that DVD-ROM drives need not be regionally coded. Instead the board comes with software which lets the user set and re-set its code up to five times. After that it remains locked to whatever was the lifth setting.

But hackers offer free software on the Interner which lets users over-ride the coding control. The Regional Protection Conmmitter is now enforcing Phase? of its policy. From the end of 1999, all new DVD-ROM drives must be regionally coded. So a decoder board will only work when its code is set to match the drive. Some drive manufacturers are adopting RPC2 early. This is already causing confusion.

Signa Designs (of Fremont, Ca) makes the Realmagic Hollywood decoder board, which boasts an "extraordinary feature" that converts TV stindards so that North American NTSC movies play perfectly through a European PAL TV. The board conforms to RPC1 and lets the user set the regional code to taste, up to five times.

After that the setting remains locked in EEPROM memory. But if the board is used with an RPC2 ROM drive it can only play dises of the standard set by the drive. So a ROM drive bought in Europe may not work with a board set to play American dises.

Creative Labs (of Milpitas, Ca) sells the Dxr2 decoder board, and Encore DVD-ROM drive. Until October ' 98 the
drive was not coded and the decoder zone could be reset live times by the user, and over-ridden by unauthorised software from the Interner. Now, without any formal announcement and with no warming on the packaging, Creative has started to code both the drives and boards as they leave the factory, depending on the country where they will be sold. The coding is done by chips and cannot be altered.

## Panic Calls

Creadive's help line is now taking panic calls from customers who have bought drives in one country and find they will not work with boards bought in another. Movie buffs are hunting in shops for old stock.


## Low-power Radio Modules



DIP International has introduced new UHF transmitter and receiver radio devices specifically designed for low power radio applications. The MK KES modules are designed for use on $418 \mathrm{MHz}, 433.92 \mathrm{MHz}$ and the new proposed pan-European standard of 898.95 MHz .

The modules have industry-siandard pin-outs and have no adjustable components. for tuning, resulting in a highly shock-resistant, rugged product. Voltage requirements are 6 V to 9 V for the transmitters, and 5 V for the receivers.

For more information contact DIP International Lid, Sheraton House, Cassle Park, Cambridge CB3 OAX. Tel: 01223 462244. Fax: 01223 467316. E•mail: antonybignal!@ dipintemational.co.uk. Web: http:/hriwn.dipintemational.co.uk.

## YEDA '99

THE 19999 Young Electronic Designer Awards (YEDA) progrimme has been announced. Staped annually since 1985 and with the enthusiastic suppon of HRH The Duke of York, pairon since 1994. YEDA challenges young people between the ages of 12 and 25 at schools and universities, to design and build an electronic or communication device, or piece of soffware that answers an everyday need which they have identified.
YEDA judging is based on the same criteria as those used in industry in determining whether or not to take up a product: Is there a market need? Can it be made at the right price? Does it have unique selling features?

The purpose of the compctition is to give students practical experience not only in
developing their own ideas by applying modem technology, but in understanding the economics of manufacturing and marketing, so preparing them better for careers in indusiry. YEDA emphasises the exciting prospects offered by such careers and the imponant role young engineers can expect to play in the future prosperity of the country.

Prize money totalling over $£ 10.000$, together with trophies certificates of achievement and other avards are on offer to YEDA entrants and their schools.

For furher infomation contact The YEDA Trust, 60 Lower Streel, Pulborough West Sussex RH20 2BW. Tel: 01798874767. Fax: 01798873550 E-mail: postmaster@yeda.compulink co uk. Web: hup://www:yeda.org.uk.

## VIRTUAL REALITY

INTERESTINGLY relevant to our report in this issue of the VR Exhibition (IITEC) is a recently received press release from the DTI. In ii is stated that VR has become much more widespread in industry since the midnineties when it was used by only a handfut of companies.

Over 100 organisations from 10 market sectors were interviewed for the DTI's study. being asked for their perceptions and experiences of VR technology. how they use it, where they get their information from and what VR activity they were planning.

Findings from existing users of VR include:

* 63 per cent use VR for practical use rather than simply for evaluation or piloi testing
* 88 per cent describe their experience of VR as highly or moderately successful
* 25 per cent consider VR to be "essential technology". both operationally and competitively. to their company
* 79 per cent expect to make more use of VR over the next few years. spending same $£ 30$ million on over 300 projects.

The UK Virtual Reality Forum web site is at www.ukviforum.org.uk, and the Virtual Reality Awareness programme web site is on the Information Society fritiative web site at wwwisigowak.

## B.A.E.C.PLUG

NOW to give the B.A.E.C. another plug! The British Amatheur Electronics Cluh is a worthy organisation to whom we give publicity from time to time (and who are always pleased to warmly welcome new members).

In a recent newsletter (published or a quarterly basis), we were extremely pleased to see a praising review of Andy Flind's EPE Mood Changer (June 98). Charles Hill, the reviewer, confesses to be in his pre-dotage vears and plagued thy regularly waking ar four a.m. and failing to return to sleep.
To cur short an amusingly told taile, he built Andy's design and "ny wife and I went to bed in some trepidation. Would ir work - or wouldn't it? As ustual. I fell asleep easily, and then woke a few minutes after four oclock. After returning from the bathroom 1 got back info bed and that's all I can remember. Next morning my wife told me she had had a betrer night's sleep than usual . . I have never looked back since".

Also of inerest is that the latest newsletter gives deails of new nembers and that in answer to the question abour which mag they read. EPE is the most frequent answer.
To find our more abour B.A.E.C, contact the Secretary, Martyn Moses, 5 Park View, Coombe Manam, Cedar View, Aberdare CF44 61PE. Tel: 0168.5 877808.

E-mail: MPMOSES@compuserve.com.

## Yet More on Disk!

IT was Fujifilm technology that made Zip disks a reality, allowing around 100 MB of data storage space on a single disk. Fujifilm have gone yet another step further, introducing their High Density Floppy Disk (HiFD).
Still in the 3.5 inch format, it can store up to 200 MB , has a lightning quick transter rate (max $3: 6 \mathrm{MB} /$ second) and has full backward compatibility with current 2 HD and 2DD floppies. $\mathrm{It}_{\mathrm{s}}$ is inrended to be run on Sony's new HiFD Drive. The capacity is equivalept to 139 standard 1.44HD disks.

The suggested retail price is $£ 10.95$ for a single and $£ 19.95$ for a twin pack. For more information contact your local computer specialist, or Fuji Photo Film (UK) Ltd, 125 Finchley Road, London NW3 6 HY . Tel: 01715865900 . Fax: 0171722 4259. Web: www.fujifilm.co.uk.

## Radio Exams

BARNSLEY' \& District Amateur Radio Club have asked us to publicise that their club is now a registered exam centre for the RAE and NARE. They have several courses occurring throughout the year, the first being the Novice Course which stants on 15 March, with its exam on 7 Junc.

For further information contact Ernic Bailey G4LUE, is Hild Avenue, Cudworth, Barnsley, S. Yorks \$72 8RN. Tel: 01226 716339. Mobile: 0836 748958 (berween 6pm and Spm please). E-mail: badarcolewcominet. Web: www.badarc.memail.cont.

# AUTO CUPBOARD <br> LIGHT 

TERRY de VAUX-BALBIRNIE


It is almost impossible to leave
it switched on!

COMMERCIAL battery-operated cup: board lights are widely available in DIY stores and by mail order from electronic component suppliers. These lamps are useful as a simiple means of lighting up a cuptoard or other dark area.

They are also handy for garden slieds and other places where no mains supply exists. The fact that they are battery operated makes them particularly attractive for children's bedrooms because. unlike conventional mains lights they are completely safe.

## STRINGS ATTACHED

The most common type of cuphoard lamp (and the one used in the prototype unit) uses a 3 V supply comprising iwo $\mathrm{D}=$ size alkaline cells. This is sufficient to provide some 20 to 30 hours of useful operation. The on-off switch is of the string-operated variety having a pull-on pull-off action
This type of lamp suffers from one major drawback - it is too easily left swithed on! This is particularly true when it is used by children (due to their impatience) and the elderly (due to forgetfulness). By the time the lamp is next needed. the batteries are likely to be "flat". With a replacement cost of some $£ 2.50$ this must be avoided if possible.


Fig. 1. Complete circuit diagram for the Auto Cupboard Light. The power supply to the circuit is independent to that of the lamp.

## DOOR TRIGGERED

The Auto Cupboard Light is an add-on unit for a commercial cupboard lamp designed to provide it with automiatic timed operation. The lamp is sriggered by door movement which makes it very easy to operate and practically foolproof in use. Before proceeding, however, check that the application lends itself to the lamp being mounted on the door:
The new add-on circuil board is housed in a plastic box attached to the base of the lamp housing (see photo: graph). The box is also used to secure the unit to the inside of the cupboard door. The string-operated switch is dispensed with so that controf is taken over by the new circuit.
The time period during which the light operates is fully adjustable between some three seconds and about two minutes. However, this could be easily extended if required.

The lamp mounted on the Circuit board box. Note that the original cord-operated switch has been removed from the lamp.

Power to the circuit is independent from that of the lamp. This comprises four AAsize alkaline cells giving a nominal 6 V output.

White the lamp is off. there is a continuous current drain of some 100 pA (depend= ing somewhat on preset VR! selting). While on, it rises to about 60 mA and, in practice, the battery pack will last for many months.

## CIRCUIT DETAILS

The full circuit diagram for the Auto Cuphoard Light is shown in Fig.l. Switch S 1 is a miniature tide switch. This contains mercury or a small metal ball which bridges a pair of contacts when the orientation is right.
Various types of mercury and non-mercury swithes were tested in the prototype and they were all capable of giving good results. However. you should choose one with radialleads - that is, both wires coming out of one end rather than the axial lead type (where the wires appear one at each end). The contacts are then at the end having the wires.
Swith SI will be mounted perpendicular to the circuit board so that the end with the wires is slightly higher than the other one when the lamp is in position
on the door. The contacts will then be open.
With a small forward movement of the switch. the contacts will momentarily close then open again due to the inertia of the material inside. In other words, the mercury or ball tends to remain still for a shor time and the switch contacts catch up with it.
When the switch contacts "make", a low pulse is transfered, via capacitor Cl , to the trigger input (pin 2) of IC1. This activates the low power CMOS timer ICI, configured as a monostable. The output (pin 3) then goes high for a certain time then reverts to low.
The time period is related to the values of fixed resistor R3, preset porentiometer VRI and capacior C2. With the values specified. VRI can adjust the timing between three seconds and about two minules, as mentioned earlier.
The lower limit (three seconds) will not be useful in practice. However, it was arranged that way because it will le handy for setting-up later.
To extend the operating period, it would be simply a matter of increasing the value of capacitor C2 in proportion. Note that ICl pin 2 is maintained in a normally high condition by resistor $R 2$ and this prevents possible false triggering. Resistor R1 provides a discharge path for Cl and nuainains the upper contact of S1 in a normatly high state.
In fact, the circuit will often trigger in whichever direction the switch is moved since the material inside it will bounce/llow around inside the casing and close the contacts on occasions. If the switch contacts end up nomally-closed instead of normally-open, the circuit will probably still work because they ane bound to break then make again.
The high value specified for resistor R1 ensures that, if this happens. the increased standby current (i.e. that flowing through R1 and the switcl) is only a few microamps and nagligible compared with the quiescent current of the circuit as a whole.

## BACK TO BASE

While ICl pin 3 is high. current flows to the base (b) of transistor TRI via resistor R4. TRI collector (c) current then eneryises the coil of relay RL.A. which allows the lamp to swich on using its own 3 V supply and RLAI "make" (normally-open) contacts.
Diode DI bypasses the reverse highvoltage pulse which appears across the relay coil when it switches off. Without it. ICI and the transistor could be damaged.

## RELAY POINTS

The choice of relay is important. Although the bulb requires only 300 mA or so: the contacts should be rated at 2 A minimum. This is to take account of the current surge which occurs on switching on.
It comes about because the resistance of a cold filament is much less than when it is at operating temperature. If the contacts had a lower rating. they might stick rogether and cause the lamp to remain on continuously. Theory predicts that an even greater current flows instantaneously but, in practice. 2A rating is sufficient.
Use of a relay allows the full battery voltage to be developed across the bulb. Other forms of switching, such as using a transistor, would introduce a voltage drop


The circuit battery pack and printed circuit board mounted in position on the lid section of the box.
ond, with the small battery voltage used here any reduction would cause an unacceptable dimming of the bulb.

## CONSTRUCTION

The design for the Auto Cupboard Light is based on a single-sided p.c.b. (printed circuit board) and the topside component layout, together with a full-size copper foil master. is shown in Fig.? This board is available from the EPE PCB Service, code 222.
Begin construction by drilling the two board mounting holes in the positions indicated. Solder the relay, i.c. socket (but do not insent the i.c. yet), fixed resistors, preset poremiometer and capacitor Cl in place.
Next. add capacitors C2 and C3 also the transistor observing their orientation. Finish with the "tilt" switch, mounting it so that the end having the leads is about 3 mm higher than the plain one (when the p.e.b. is held vertically as shown in the photographs). This will give a reasonably correct sensitivity setting.

Solder 15 cm pieces of light-duy stranded wire, together with the battery connector leads, to the lead-off copper pads as indicated in Fig.2. Adjust the preses VR1 fully anticlock wise (as viewed from the top edge of the p.c.b.). This will give the mininum three second timing. which is suitable for testing.

## SHUT THAT DOOR

To make the lamp come on as the door is opened, the p.c.b. needs to be mounted on the top section of the box (see photographs). This will give the correct direction of movement. The box used in the prototype (see Shopralk) has a deep lid section making this work convenient.
A position should be chosen for the p.c.b. and battery pack. Mark and drill the p.c.b. fixing hotes but do not actually attach it yel.
Remove the plastic done of the lamp housing and take out the bulb and cells. Locate and remove the "built-in" switch to leave free ends. How this is done will depend to some extent on the lamp being


Fig.2. Auto Cupboard Light printed circuit board component layout and full size underside copper foil master pattern.

## COMPONENTS

|  |  |
| :---: | :---: |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
| Potentiometer |  |
| VR1 | 1M min. enclosed carbon preset, vertical |
| Capacit |  |
|  | 47 n polyester 5 mm pin spacing |
| C2 | $100 \mu$ radial elect. 10 V |
| C3 | $47 \mu$ radial ele |
| Semiconductors |  |
|  | 2TX300 npn transisto |
|  | ICM7555IPA low pow |
| Miscelfaneous |  |
| S1 miniature tilt switch $=$ see |  |
| RLA 1 | min. 6 V 100 ohm coil relay, with 2A normallyopen or changeover contacts. |
|  | 6V ( $4 \times$ AA cell) battery pack, with holder and PF3 type connector and leads |
| Printed circuit board avallable from EPEPCB Service, code 222; cupboard lamp, see Shoptalk page; ABS plastic case, size $143 \mathrm{~mm} \times 82 \mathrm{~mm} \times 44 \mathrm{~mm}$ external; 8 -pin di.l. socket. multistrand connecting vire; solder pins; solder etc. |  |
| Approx Cost <br> cuidance Only <br> lexcluding Batis. \& Lamp |  |
|  |  |
|  |  |

used. However. in the prolotype, it was a straightforvard job.
By inspecting the internal details of the lamp and taking account of the p.c.b. and battery pack positions, mark and drili two holes in free positions and drill these through the lamp base and the box. "These will be used to secure the lamp in position.
Mount the p.c.b. in position using countersunk screws (so that the lamp will be flat on the top). Attach the lamp unit

Layout of components on the completed p.c.b. Note that the metal cased fill switch, to the left of the i.c., should be carefully bent so that the wire end is slightly higher when the unit is mounted on a door.
to the box using small nuts and bolts. Drill a small hole for the wires leading from the p.c.b. to the lamp and solder them to the contact strips where the switch used to be.
Insert the AA cells ( $\mathrm{B} \mid$ ) into the batiery holder and attach it using adhesive Velero fixing pads or in some other way which will make it easy to remove for batterychanging purposes. Do not connect it up yet, however. Replace the cells and bulb in the lamp.
Insen ICl into its holder. Since this is a CMOS device. it could be damaged if there is any static charge on the body. To be safe, remove any charge by touching an earthed metal object (such as a water tap) before handling the pins. Mark the positions of the two keyhole slots for mounting the unit on the cupboard and cut these out.

## FINISHING OFF

Connect the battery holder. With the lamp held as it will be on the door, gently move the unit. The monostable will trigger and the lamp should come on if it has not already done so. If you keep the case
stationary, it should then go off after a few seconds.
Assemble the box using only two of the screws for the moment. Fix the unit to the door and wait for the lamp to go out (it will almost certainly come on again during the movement). Open the door normally - the lamp should operate again.
If it does not do this, or if it needs a particularly harsh movemeot to make it work. reduce the angle of the switch and re-try. A position should be found where it is neither too sensitive nor too difficult to operate.
Possibly. it will need a period of "trial and error" to get this adjustnent just right. Do not leave VRI at minimum adjustment because the standby current requirement is increased. So, set it to provide a suitable timing period.
Secure the remaining screws in the bof and attach the plastic dome on the lamp: Mount the unit on the door.
When the lamp is turned on, try to close the door before it goes off. If you do not, it will probably re-trigger when you close it and drain the batteries unnecessarily.


The original cord-operated switch has deen removed from the lamp and the wires from the p.c.b. are soldered to its former position.


The D-cells and bulb replaced in the finished Auto Cupboard Light unit ready for receiving the light diftuser dome

Whether you are an Amateur or an Engineer, this encyčopaedia is andincredible mine of information and will help you to save hundreds of hours of search.

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## Special Feature IITEC SHOW REPORT



## CLIVE "MAX" MAXFIELD

## A special report from the land of virtual reality at the Interservice/lndustry Training, Simulation and Education Conference in Orlando.

.
T'S rare that you see something you believe could make a significant impact on the world, but this happened to me at the beginning of December. 1998.

One of the more interesting conferences and trade shows of the year is I/ITSEC (Imerservicel/ndustry Training. Simulaion, and Education Conference), which is held in Orlando. Florida. USA. This conference is unusual in that it is held immediately after Thanksgiving weekend, but it was explained to me that this gives the American Generals an excuse to bring their families to Walt Disney World, so that's OK.

The reason I/ITSEC is so interesting is that it offers a showcase for the state-of-the-an in computer-based visual simulation and virtual reality. Governments spend luge amounts of money on training. and in today's restricted economy they want to get the "biggest bang for their buck ${ }^{-}$

For example, by the time you take into account wear and tear, fuel, cost of munitions. and envirommental impact. it can cost as much as $\$ 100,000$ US dollars every time you start up a tank (and this doesnit liake account of the danger to personnel
that is inherent in live-fire exercises)! In fact, the National Training Systems Association estimates that US government investment in modeling and simulation (M\&S) is currently ruming at about $\$ 3$ billion US dollars per year, which equates to some honking big and mega-cool night and tank simulators. let me tell you!

## MODELLING <br> THE MORLD

The company that almost blew my socks off and takes my personal "most amazing idea" award is Geometrix Inc, San Jose. California (www.geometrixinc.com). The first product from this smatl ( 12 person) company is 3 Scan. whicli provides a way of quickly creating models for use in 3D visualization. simulation, and animation environments. The idea is that you put the object to be scanned on a computer-controlled turnable and scan it with a single video camera (Fig.1).

The camera I saw them using was $\hat{t}$ $\$ 120$ "cheap-and-cheertul" version. you could get significantly better results with a more expensive unit. Once youve instructed the computer how many images you wam to use. it automatically rotates the
tumtable and captures the appropriaie data: for example. 20 images equals 18 degrees between images.

The key points here are standard video camera (i.e. no lasers or anything like that). and just one camera, which means that you don't require multiple camems for stereoscopic viewing to calculate depth.

Once the scanning process is finished. the 3 Scan program examines the first frame o locate and identify key teatures in the image (the algorithm that does this is pretty sophisticated in its own right). The program then determines how far each of these features moves from frame to frame. Also some features become obscured and others become visible as the object rotates. so the program adds new features and discards old ones as it proceeds.

The program uses parallax to detemine the relative positions of each of these key features (which ones are closer and which ones are further away). (In this context parallax is the apparent displacement of each of the features from one frame to another.)

The end result is that 3Scan processes the raw video data and generates a very tasty 3D model with appropriate textures


Fig.1. The 3Scan set-up with simple turntable and single


Fig.2. The model generated by 3Scan.


Fig.3. Original Softscene picture with features indicated.


Fig.4. Showing the image displacement vectors.

Fig. 5 (right) Reconstructed camera path with "point cloud"
(surface characteristics). see Fig 2. These models are essentially a mesh of polygons (triangles) with textures applied to them. This nieans that the models wouldn't really be suitable for individual animation, but they would make great general-purpose models for inclusion in 3D scenes.

## BUT THEN / SAW

I thought that 3 Scan was pretty cool, but then the guys from Geonetrix introduced me to Softscene. which is where things start to get really amazing. With SofiScene, the clever rascals have extended the 3 Scan concept to allow someone with a single hand-held camera to. Say, walk around a buikling filming it.

As for 3 Scan. Soilscene uses advanced image processing algorithms to detect and extract prominent visual features in the scene (Fig.3). In this image, green crosshairs indicate features that have been detected in the current frame.
SoliScene tracks the prominent features from frame to frame, re-invoking the feature extraction algorithm whenever gudditional features are required. In Fig. 4 ,
 the building. The image in Fig: 5 shows the fully reconstructed camera path altong with a "point cloud" representing the final set of tracked feafures in 3D space. The green and red camera icons represem the first and last frames, respectively (camera icons are shown 12 frames apart in this image).

In fact some people (like the creators of Jurassic Park. for example) are really interested in knowing the path of the camera for the purposes of combining live video with animated characters. Using the SoftScene process, they could just give the program a video clip and it could tell them the path of their camera (this is essemially
cyan linies are used to indicate the image displacement vectors of the selected features (the green crosshairs indicate where the features are in the current frame). SoftScene then employs advanced suruc-ture-from-motion algorithms based on analyzing motion parallax to spatially locate the tracked features.

One very clever aspect of all of this is that the person holding the camera obviously isn't going to walk around the building following an exacily circular path. So one of the things the SoftScene program has to do is to calculate the path of the camera in order to compensate for the operator moving closer to and father from a product in its own right.).
But retuming to our building, to complete the process SoftScene uses advanced topology and computational geometry algorithms to construct a polygonal mesh from the data contained in the 3D point cloud (Fig.6). Texcures are then automatically extracted from the video imagery and applied to the surfaces of the model, resulting in the model shown in Fig. 7.

We should note that this was an early beta of the Softscene program Geomerrix are planning on formally unveiling the full product at the Siğgraph conference in mid-1999. Also. Geometrix


Fig.6. Polygonal mesh constructed from the data in the "point cloud".


Fig.7. The 3D model generated by SoftScene from the original video.
have recently been awarded $\$ 950.000$ in development funding to attach a GPS receiver to the camera and to record GPS data on the audic track of the video, which will allow them to generate exiremely accurate models.

## TREMENDDUSLY EXCITING

I think that the Softscene technology is tremendously exciting. It seems to me that there are almost limitless applications in all walks of life that would benefit from the ability to create models using this technique. For example, imagine a police officer at the scene of an accident walking round with a video camera filming everything. Later. that video could be converted into an accurate 3D model that you can "walk through". This would make it much easier to evaluate whether people could see what they said they saw from the positionts they said they were in. and so forth.

Another potentially interesting market is television studios, which are increasingly making use of "virtual sets". These com-puter-generated sets offer many advan= tages, such as the fact that you don't have to set them up. take them down, or store them. and also that you can quickly and easily change things like colours and textures. The problem is that creating 3D computer models for these virual sets can be extremely time-consuming. SofiScene technology would make creating the models for these vintual sets much casier.
Geometrix emphasize that their technol ogy is not currently targeted towards the commercial (home) market, but I can also see inany potential applications here. For example: imagine filming the inside of your house with your video camera and using Softscene to create an accurate 3D model of your home. This would allow you to experiment with altemative decors in a virtual environment - the possibilities really are endless!

## EYE EYE . . .

Several companies were demonstrating eye-tracking systems. including SMI (SensoMotoric Instruments from Germany, www.smi.com) and ASL (Applied Science Laboratories. Bedford. Massachusetts). The most sophisticated version is attached to a head-mounted assembly, with sensors that track the position of each eye (and the size of your pupils and suchlike). This data can then be displayed on a computer screen superimposed on an image of what you were looking at at the time. This lets you model the path the observers' eyes took. combined with other data such as how long the eyes rested at each point.

Applications for this sor of technology range from tlight simulators (for example to see what instruments the pilot looked at and in what order during a simulated emergency) to web marketing (to see how users read web pages - what they look at and what they miss or ignore).

Although the head-mount unit was a lit= tle obrrusive, it did have the advantage that it was position-sensitive, which allows the system to compensate for movements of the head. Also, this would be far less obirusive if it were mounted directly into a
fighter pilots hetmet, for example. Of course, applications of the web-marketing ilk don't require this level of sophistication, and simpler units are available that can be mounted on your desk or on top of the computer screen to track your eye motions from that perspective.

## / SEE ND SHIPS

Another technology that scored high on my "cool-meter" is the concept of virtual binoculars. which were being displayed by companies such as Virtual Research Systems Inc. Santa Clara. California (www.virtualresearch.com) and u.Vision. McLean, Virginia (wwonvis.com).

These litule rascals look and feel like a standard pair of binoculars - for example: you can adjust the focus of each eye, widen or narrow the spacing between the cyes. and so forth. The difference being that the images seen by each eye are generated by a computer connected to the binoculars and displayed on small l.c.d. screens inside the binoculars (Fig.8).

One clever aspect of this is that the binoculars trach their position in 3D space, so as you turn your head (and body) the scene changes appropriately. For example, when I looked through them. I could see a plane coming towards me and passing overhead, and by moving my body I could track the plane. zoom in on it , and so forth.

This sort of device can be very useful for such tasks as training forward observers to recognize planes and vehicles
im a batlefield situation without the cost of accually fielding the real units.

## IF AT FIRST . . .

Whenever I hear the term "simulators" i think of flight simulators (e.g. planes and helicopters) or vehicle trainers (e.g. tanks and suchlike). so 1 was somewhat surprised when I meandered into the bwoth of Jason Associates Corporation. Draper. Utah (www.jason-sed.com) to find a smorgasbord of crane simulators.
The iden is that you sit in a seat from a real crane (well. a seat made by the sanie company that makes the seats for the real. cranes) with real controls, facing a large screen showing the load on the end of the hoist. and then you practice, and practice. and . . See Fig. 9.
Of particular interest was a dock-side simulator that modelled the effects of the swell of the sea. the motion of the ship. the effects of the wind on the load. and so forth (in the more sophisticated units the seat fhas motion and the controls have tactile


Fig.9. The Viriual crane simulator from Jason Associates Corp.
feedback). It ohviously makes a lot of sense to practice this sont of thing in a virtual world before you are let loose on a real crane.

Jason Associates started off modeling cranes for military applications, but now they're moving into the commercial area. As the representative I was talking to said: "Have you any idea how many cranes there are in the world?" I must admit that I hadnit previously spent a lot of time pondering this poser, but it certainly makes one pause for thought.

## VISIONDOME

If you place your hands to either side of your head, and restrict yourself to about a 60 degree field of view, the resulting image is approximately the same as you see on a computer screen. Some applications. however, require a greater field of view to fully immerse you into the simulation.

One very nice solution demonstrated at I/ITSEC was the VisionDome from Altimate Realities Coporation, Durham; North Carolina (www.virthat-reality. com.), see Fig.10. These litte beauties range from 5 metres wide by 3 metres high (for two to five viewers) up to models thiat are 8 melres in diameter and can accommodate over 45 viewers.
The nice thing about the VisionDome is that it provides a fully immersive multiuser virtual reality environment without requiring goggles, glasses, helmets, or other restrictive devices. This makes the VisionDome an ideal choice for nulti-user simulations. training, design. engineering. and so forth. For example, the virtual cranc simulations discussed above would be even more realistic if they were to use a VisionDone as their display device.

As I was to discover, there are three very clever portions to this system:

The dome itself
The projector/lens
The software

## THE DOME

The reason I say the dome is clever, is that the way it is constructed makes it extremely portable and easy to erect. First of all you raise an aluminum frame, then you clad the outside of the frame with a thick black covering and the inside with the white lining material.

The cunning part of all this is that if you were to iry to attach the white lining to the struts of the frame, you would end up with distortions on the surface of the dome. Instead, the white liming is only attached around the edges of the frame, then a sniall pump is used to create a slight negative pressure by sucking out the air from between the black cladding (on the outside of the frame) and the white cladding (on the inside of the frame). This results in the inner lining being stretched taught and forming a perfect hemisphere.

## PROJECTOR AND LENS

The next interesting point is the projection system. Instead of having red, green, and blue "guns", the VisionDome employs a single unit projector, which means that there's no problems with alignment. But the really clever part of the projection unit is the lens. which somehow manages to
project a perfectly focused imige across a 180 degree field of view.

Furthermore. this lens has an infinite focal length. This means that you can hold a piece of paper a few inches from the leris and see that portion of the image in focus. Then as you move the paper further and further away the inlage stays in focus without any correlation problems. I don't have a clue how this works, but the end result is that the image is in perfeet focus at every point inside the dome.

## THE SOFTWARE

Last but not least is the soltware. In order to appreciate what this does, you first have to understand a few basic concepts about 3D graphics. First of all. a 3D graphics application performs a process called "tesselation". in which all of the objects in the 3 D world are converted into collections of simple polygons (usually triangles). Second, the vasi najority of today's highperformance 3D graphics applications use a programming interface called OprenGLO, which allows the application to pass requests to graphics accelerator boards in a well-defined format.

The problem is that OpenGl was conceived with flat-screen displays in mind. In the case of the VisionDome, if you want to display a straight line. then that line has to be curved so that it appears straight on the curved screen (if you see what I mean). In
order to handle this. Altemate Realities software takes the OpenGl. calls and retesselates everything on the fly (for each frame) so that it can corect for the curvature of the screen.

The overall effect is one of total immersion - I now know what it feels like to fly through a 3D molecule, and that's not something you expect to hear yourself saying too often!

## 3D GRAPHICS

If you viant to know more about 3D graphics, including the meaning bethind terms like tesselation, OpenGL, textures, and suchrike, then we would recommend the easy-to-understand introductory book. Graphics Supercomputing on Windows NT.

This book commences by introduting 2 bitmapped and vector graphics, and then moves on 10 discuss 3D graphics concepts. These introductions are easy 10 understand and set the scene for what is to come. As you will discover, the main portion of the book is occupied by an extensive colour illustrated glossary, which covers everything from the different schemes for representing colour and translucency to lighting, shading, texturing, and rendering.

You can downioad a sample chapler of this book (the chap:er on 3D Graphics) by visiting the EPE Online Web site at wiw.epemag.com and strolling into the Library. If you like what you see, you can purchase the full printed book direcily from the EPE Online Store.

Fig. 10. The VisionDome really immierses you in the scene (Courtesy Alternative Realities Corp.)


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## - LETTER OF THE MONTH

## APPLE IMAC

Dear EPE.
I was delighted to see two items in the January 1999 issue relating to the Apple iAlac computer, but I think that you were very unfair with your comments in both pieces. The $/ n$ novations article on page 15 is very misleading on several points: it implies that suitable peripherals are difficult to find - this is simply nom the case. The imac's LiSB ports are standard PC conmetors. A whole range of low cost deviees induding scanners. printers and serial adapters were available to buy in the UK when the ivise was launched in September, and many new produels have come onto the narket since then, including extemal disk drives.
The iniw does have an expansion slot, although its use is limited but sink the imac comes with an ATI Rage Pro Turbo graphies controller and 6MB of video RAM. SRS sound. 100 Mps Ehemet and 56 K modem buile in, it is hard to imagine what a home user would need to add. The cost of RAM for the iAlice is no different to any other modern PC and the price varies greatly - at the monrent ( $\mathrm{Dec} \cdot 98$ ) most iMac suppliers axe including tree 96MB RAM upgrades. Many people have criticised Apple for not including a floppy drive. but this does make sense if you consider that most people buy an addititonal zip or supendisk drive for their PC anyway, leaving the floppy redundant.

I do not understand why you have decided to quite the stan-up time as your only measure of how fast the iMac is. Since when has this been a useful measure of a computer's capability? In use. the iMac is blisteringly fasi - the combination of the super fass Power PC processor with it highly efficien Cache memory and accelerated video handware mean the iNac is only marginally slower than Apple's top of the range systems costing three times as much - Iruly amazing for a home computer.
The Net Wort anticle on page 69 does provide a far more rounded piece on the iMac. until Alan Winstanley chooses not to recommend the machine on grounds that I feel are unjustified. It is true that Apple Mass are rarely seen in the high strees - but this means that there is a thriving mail order community instead. I see no reason why a "technically inexperienced" iMac owner should have dife ficulty buying mail order. I have yet to gain any benefit from sales staff at Dixons or PC-World when buying computer equipment. It seems far better to talk to a real expert in a mail order company over the telephonte.

With regand to the issuc of IBM PC softwan companibility. this is no longer a serous problem for Apple users. Vinually all of the major software tilles (Microsoft Word and Excel. Adobe Photoshop, etc) are available for both Mac and PC. and they use identical lile formats - files saved on the Mac can be opened on the PC and vice-versa.
Easy Open file translation software, often bundled with Apple computers performs autoinatic translation of documents when the correct application isn't available - far more flexibte
than straight compatibility. I am not exaggerating by saying that in many cases, Apple computers are more compatible with PCs than PCs are. I am regulariy using my Macintosh computer to unzip. translate and convent ${ }^{1} \mathrm{C}$ files for my colleagues to use on their PCs.
For software titles not avaitable for the Mac. a number of excellent and very realistically priced software emulators are available that will run any Windows application. The astonishing speed of the iMac means it is quite capable enough to run Windows 98 at Pentiam speeds. 1 currently use Virual PC 10 run Windows 95 and the Microchip MPLAB soltware and program PIC mictocontrollers withoul any problems on my Mac. a model far less well specified than the imac.
The issue of IBM compatibility is not nearly as serious as you suggest. Apple's Macintosh computers (including the iMac) run MacOS. eurrently at version 8.5, which is almosi universally aeclaimed as the best personal computer operating system available, it is brilliantly designed from the ground up to provide fast, easy coniputing. The MacOS and applications that rum on it really are a joy' to use - a far cry from the problems of Windows 3.1. 95 and 98. Apple computer hardware is also exceedingly well builk - there are many ten-year old Macintosh systems in use today.
If you want an indication of just how good Apple Computers and the Alacintosh MacoS operating system is. consider this: despite the fact that Nixe users ane in a minority among Windows users, the fact that Moce hardware and software often isn"t available on the high-stret. and the fact that Apple computers have always proviously been more expensive than Windows systems. Apple is still one of the largest compurer companies in the wordd, second only to companies like Dell and Compaq in terms of numbers of computers sold. but always on top and winning awards with the fastest maxtines. and rewest and most innovative teclunologies. It is fantastic that Apple is able to produce the iMac, a fully specified computer at a home computer price

1 find it sad that such a brilliant piece of engintering is knocked back largely on the basis that it inn't directly compatible with the majority of PCs. There are millions of phople in the world who know that there are problems with Windows 95, millions having problems with Wmulows 98 - so why an people so keen to stick with something so universally known to perform so proriy? With the iAlac, Apple has at las: produced a very enedible, affordable alternative to Windows. and this should be applauded.

PS. I am a produst designer/electronic engineer who uses bods Macs and 1BM-PCs every day. and 1 don't work for Apple!

Roger Coleman. via the Net
Thanks jor an extremely interesting añd dif. ferent view point of the inac. Editarially we try to be impartial and the vieus exprexsed by contributors are cntirely their ow:n.

## WEBBED TRACKS

## Dear EPE.

Now that the technology to publish electronically thas been proven with the adveni of EPE Online. it would be of great heip for those producing pec.b.s. at home to the abre to print difectly from a downloaded image of a p.e.b. track layout onto a iransparency.
I also mie thar EPE does not have the hangups which some of the comperitive electronits magazines have with respect to the prolection of source code. and this is gratefully acknowledged. Whth the increasing number of hobby electronics projects being based on some form ol microcomimiler or other. protection of the code would seriously impace the constructor's ability to understand the operaliont of the unit.

Martin Slipley. via the Net
There are a number of problems asswioled with downhouding p.c.b. images from the Web Although most of the p.cb. truck images have been produced by authors who use p.eb. CAD pactages. the great variety of such pactages awilahle prevents us from making direst usc of anthors" electronicalls generated images (exceps in vary rare cases). Generally speaking we scan double-size paper primtouls of the peeb. tracks inta our system as Corel Draw images.

Whitst sec have full control over the size of the images that are subsequmtly output as hrose you sec on the ordinary printed pages of the magazine. We have no control over the same images when they are electronically put on the Web for downloading as part of EPE Online. Consegutntys. Web asers canmot be assured that the p.e.b. innages they print are the correct size. we are, however, working on this problem.

Regarding source codes. we have lung main' tained that readers have as much right to these as they have to the circuits and p.eb. lavouts. If is part of our conditions of article acceptance that the somrce eodes are avoilable for the use of all readerswilhoul restricion.

## WHAT'S IN A NAME?

Dear El'E.
If is sad to see the end of Elacironies Tudars International as a separate entity, but on the subject of what the comhined magacine stould be called (Editurial Feb '99): Ifelt the original publishters made a mistake when they chose to shorten it to ET/ some years ago. I could stand for almost anything - English Tourist Information, maybe. if electronics nugazines are trying to attract new readers, what better than to have the word Electronits beanting out amongst the many publications on the newsagents shelves?

The ward Pracrical should also be dropped, it was a commendable tribute to an old rival. but Eserguay Prutsical Electranics never did roll oll the tongue:

Then we have a possible confict - Everyday or Today - perhaps nol. International is quite relevant, as you are available worldswide.

So here is my title suggestion - Everydoy Elecironics International (NOT EE/ please!).
K. G. Aftchell. L. f ghton, Fisel

Thank jor the observations - we'll stay silent on this subject for the monten, just letting readers have lieitr say - Carry On Commenting?

## COMPRESSORS

## Dear EPE.

Having used limiters and compressors professionally in the (w) and 70. as well as building many gain control circuits as a hobby. I cannoi totally agree with Robert l'enfold's antalysis of tha causes of disturbing changes in volume un. TV.

Ary programe (and I daw no distinction between a two hour drama production or a 30 second commercial) will be produced with consistent sound levels. Possible exceptions to this are News bulletins where material will arrive during transmisslon and thus cannot sometimes be checked for sound level as thoroughly as would be desimble. Although sone viewers may be upset by the sound levels, it is impossible to cater for the quality delivered by all TV sets: There is a world of difference between the sound of a decent set with a forward facing lloudspeaker and a small "portable" set with a sideways facing loudspeaker pointing at heavy curtains which soak up most of the frequencies that provide intelligibility.

Within a programue. the long established technigue of gradually reducing the level over some 10 to 20 seconds prior to a climax will give impact without over-modulation, but it would be impossible to do this before commercial breaks without lengthy rehearsal and in today's climate it is unlikely that any broadcaster wotuld have the resources to provide the staff to do this.
The problem is that commercial breaks as well as the trails and promos aired between programunes on BBC1 and BBC2 have been produced as standalone "programmes" and considerable time will have been spent to ensure that everything looks and sounds right. I have come across before the belief that the sound levels of programmes are reduced to nake the commercials stand oun. This is a very doubiful premise since advertisers are aware of their barge audience and are unlikely to wish to alienate them. nor dues it make sense for the broadcaster to do this since a reduction in level of say GdB with respect to the tevel of the commercial is the same as having the sound transmitter on half power excepl for a few minutes in every weaty which will impair the signal-to-noise ratio of analogue transmissions and possibly make quamizing noise apparent on digital iransmissions.
So why do some commercials sound so loud? It is because they have been compressed! There are many combinations of attack and release times. compression ratios and thresholds that can be used making it mort of an art than a scitnce. A favourite technique of mine that did not always work was to cut the bass: compress quite heavily and then boost the bass by the same amount that it had been cut: if it didn"t work it sounded dreadful thel there was no way of prodicling this. This was in the days before manufacturers routinely provided EQ insert points in the side chain).
Compression is not necessarily used to chsure viewers attention will he grabbed but mainly to get the message across in an impeccable style given the content and the short duralion, which means there is to time to inplement the techniques used in say druma productions and thus the use of compressors is the only way so achieve the desined resuit.
In some respects this is no different to a Top 40 single. The producer of this is nol concemed what radio station it is played on: it is simply a standalone product that satisfies the proxiker and the artist and if they get it right it will. sell. Thirty years ago, for example. broadcasters empluyed sound stain to ensure that saund levels were seamless. 1 suspect that foday's solution is to simply have a peak limiter in cireut or if there is an operator in charge then that person is simply looking at a meter and not listening to the output.

That said. I'm certian that Robert Penfold's circuit will do what he says it does and beller than many circuits 1 have seen and tried, except
that I wouldnit call it a Compressor, which is generally taken to mean a device that reduces the dynamic range and thas incteases the average sound level and makes things sound louder. A more accurate name. given the altack and release times. would be an Automatic Volume Control.
Where I will disagree is with the suggestion to build two boards for sterco. This will not be satistactory. Suppose a signal in the left channel is high enough to cause gain reduction. The level in the LH channel will be reduced and a ceanral image will be pulled to the right as no gain reduction has occurred in the RH clannel.

Depending on which channel causes gain reduction. a central image will swing ahous the centre in at very disturbing way. What must be done to ensure stability of the central imuge is to ensure identical gain reduction in both channels regardess of which channel causes the reduction.

A simple way of doing this is to combine the outpuss of IC3 with a virual earth mixer and feed this into a single side chain with the ourput of the side chain feeding both VCAs. As there is another rechannel MOSFET in a 4007 and a Th072-costs the same as a TL07I, a stereo version could be beift for only a little more than a mono version.

Barry Tayler. viat the Nel

## Rohert replies:

1 found Bursys lever rery interesiling bul do not thint. that his explonation of things is that much differem to my own. I suggested that the apparent volume could be (and prohably is) mate greater by using a high average lewd. and this is something that is uchieved usms compression.
Some of the over-foud adierlisements sound to me' as though there has been some doctoring of the frequency response. tut I obviously have no way of proting this. Whatever the couse. there are cersuinly large numbers of people who have problems with the sound for one reasan or another. Hence this project, which ivus requested by readers.

I normally find that sephrate processing of stereo channels gives the hest results. but in this case I think Burry is probahly right, and that controlling the thio channels in unison wauld give slighly lether results. maintaining correct sterco balance.

Robert Penfold

## D.I.Y. ONLY

Dear EPE.
Could you help with your PIC Tutorial as 1 now seem to be a hit stuck. 1 am having a problem trying to understand some of the exercises. Could you please send me the answers to all the exereises so that I can eompare my answers with them.

Roy Chaplin, vià the Net
It's now offen I refuse to help. but I am doing so on this onte - sorng to disappoins you'. It is not in the interews of thase studving the PIC Tutorial that they should tre given answers to the extrcises. The exercises are not of the sane nature as. for exumple. problens in mahematical fert hooks, where offen there is only ont correct answer:

The intention of the cerercises is to get readers fhinking for themselves alvoul how to deal wifl the programming situtions presented. In many instances there are several ways in which programming solutions san be acheved. There are sufficient dixcussions and excomples within the ithorials for readers to apply intelligence and lagical thinking about how the programming commands that are arvilable con be used to create a program routinu that achieves the suggested ohjective After ull. there are nes al lal of comunsuds whose function you hare to learn.
Readers who ure capable of applying con: cemrution on a logical prohb'm should non really find the exercises difficult. For the to provide answiers would nos reully sel people thinking for themselves. In a reat-life sittration.
ven urn tre pille to keef on ativing others foons to sohve prograinning iroblems thu you nerd in overcome.

In wour hobby activinites for cxample. it;'s you who have imvented the design for which you want a FIC (or ohler procesesor) to provide control. If wou heep asking others to write whur programs. Where's the plensure or serase of achieventent for jnu in having thought up the idea?" " will have ceased to be your creatioa.
A mare severe situttion will accur if you are wrilng a proyram as parl of your job requirement. If prograniners cannos provide programming solutions, they will cease so be of value to their emphovers - need I say more?
As a general sip with a programming problen. if you cannm solse it immediucly. make it step by step. experimenting at each step if necessary, and jusi keep) on until pou do solve it. It could he that the approach you first throught was the correct one may furn oul to be based on a wrang assumption. Tre alfernotive nuays of achieving the desired end resuth. It's the way 1 learned to program - 1 assume it is logical to think that others involved in programming have a similar autilude.
But at the end if the doss. it is worth recognis: ing that not evervoute is capable of secing the world in grognamming serms. And there is no shame in bieng such a person. We all have our talens. and there are mony things which some prople can do successfully that others cannot. the latter having their own strong points unassullable by the former.
However, if when believe you are logical enough in wour thinting. sou probahly are capuble of programming - so leep at is and you'll get there?

An I right in expecting to receive critical het: Miers as at result of this reply? Ifes. I himis we at EPE set out la educate and encourdge people in efecronnies - but how far is hand-holding juse rified or desirable?) fell at me if dou wisht

## IEE REGULATIONS

Dcar EPE,
I am afraid that Bob Medilitan has not contsulted his IEE Regulations recently (Recudou Jan 94). In the 16 th Edition there are three earthing systems shown (TN-S. TT and TN.C-S) and it states that the classification of the system will depend upon the disitribution syatern and not upon the customer's instalkation.

As a rutined electrician (my eareer covers Contracting, Installation and Maintenance at the National Conl Board). I recommend that readers use mains testers to cheek their supply integrity - I have used a commercial versione for many years and found them useful for quich checks. and have never found them hazardous.
1.. G. Sutton:

Burion-on-Trent, Staffs
The above is just part of a much tenghtier lether from Alr Sulton. Who also kindly sent illustroted edracts from the IEE Regas from which it is olvious that the sitution is far more' complicated than the discassion has hitherto restaled. It is a suhject las we have suid on sither occasions) that we shall return to in greater deph.

## WICKETLY CHUFFED

## Dear EPE

Thanks very nuch for making ny letter Letuer of the Month (Feb "99). I am very "chufled" and I get a prize! Ya hoo!
And hest of British lack in the one day cricket. I might be an Aussic but I still think Wame and Waugh should be tossed out permanemity.

## Carle Wilde

## Acacia Ridge, Australia

In defenence to our many orerseas reuthers we are officially impartial io internutional comflicts:' (Carl faved these conmments in is on 7 fan O2 in response to us telling him of his letter's states:)

Clive "Max" Maxfield and Alvin Brown

WFecome to the fifth instalment of our really unique and exciting electronics and computing project. This series of anicles, as many of you are confiming, will lre of interest to anyone who wants to know how computers perform their magic. because it uses a unique mix of hardware and software to explain how computers work in finn and interesting way.


#### Abstract

We now enter the second phase of the tate about our phaniasmagorical little rascal. getting ready to drive the world (or at leasit: our buggy) buzzing with excitement - yes, folks the PhizayBol robotic wheelie is fast approaching. You too cat drive is. without needing to know too much about electronics (although youp do


 need to know a bit about PCs). .OOD grief - how time flies! li seems like only yesterday we were starting this PhizzyB series, and now we re proised to hit the ground running with a simple robot called the PhizzyBot.

In his Phizzy B constructional article elsewhere in this issue, that wanton warrior of electronics. Alan Winstanley. describes how to construct a simple controller device that will allow the PhizzyB to control two d.c. motors. These motors will, in turn. provide the PhizzyB with the ability 10 roam around the room. thereby turning it into the PhizzyBoi:

## WHAT'S IN STGRE

For our part in this tutorial. we will start to consider some simple programs to first test the motors and later to control them more meaningrully. Please note that although this article concentrates on the real Phizzy B. all of the programs deseribed herein can be run on the PhizzyB. Simulator if you so desire.

Also note that this is where we stan to use everything that we ve done thus far (including the simple l.e.d. device from Part 2. the subroutine concepts we discussed in Part 3. the intemupt routine concepts and the internapt-driven switch dévice from Pars 4, and so forth).

The new programs we create here will form the basis for next month's anicle. in which Alan adds some collision detection eapability to the PlizzyBot in the form of microswitches, while we will program the PhizzyBot to mundle around the room, taking evasive action whenever it bumps into anything. Later we'll be augmenting the PhizzyBot with some optical sensors. but all of that is in the future. For the nonce, we'll concentrate on testing and controlling the motors.

From Alan's article we know that we will be using the least-significant (LS) nibble (four bits) of one of our external output ports to drive the motor controller device (this leaves the most-significant (MS) nibble free to control more motors or other devices in the future). The bits used to control the motors are shown in Table 1 .
In this context, the terms "Left" and "Righn" refer to your perspective if you

Table 1. The bits used to control the motors.

| LEFT |  | RIGHT |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| 0 OP1 | OPO | OP3 |  | OP2 |  |
| 0 | 0 | - | 0 | 0 | - |
| 0 | 1 | Reverse | 0 | 1 | Reverse |
| 1 | 0 | Fonward | 1 | 0 | Foward |
| 1 | 1 | - | 1 | 1 | - |

were to slirink yourself down to a height of six inches ( 15 cm ) and position yourself atop of the PhizzyBot looking towards its front end. If both motors are instructed to drive forwards. the entire PhizzyBot will trundle forwards (Fig.la). and if both motors are driven in reverse. the Phizzy Bot will go backwards (Fig. Ib).

If the left-hand motor is driving forward while the right-hand motor is driving in reverse, then the Phizzy Bot will rotate in a clockwise direction (Fig.lc). Similarly, if the right-hand moor is driving forward while the left-hand motor is driving in reverse. then the PhizzyBot will rotate in an anticlock wise direction (Fig.Id).

Almost last, if the left-hand motor drives forward and the right hand motor is turned off, the PhizzyBot will turn to the right (Fig. Ie). Similarly, if the righthand motor drives forward and the left-hand motor is lumed off. the PhizzyBor will turn to the left (Fig. In).


Fig.1. Using the motors to control the PhizzyBot's direction.

## Listing 1

\＃易 Start of Constant Declarations

| SKITCHES： | ．Enu | \＄5012 | 4 | Switch input device |
| :---: | :---: | :---: | :---: | :---: |
| MGRAPH： | ．EQU | \＄E030 | \＃ | Motor baxgraph display |
| TGRAPH： | －EQU | \＄F031 | 4 | Timer bargraph display |
| MCOITTROL ： | ．EQU | \＄F032 | $\underline{3}$ | Hotor controller boazd |
| DELCONST： | ．ECu | 20 | H | Deiay constant value |

f\＃Start of Hain Program Initialization
ORG $\$ 4000$ Set program origin
SLDSE S4EFE Noad gtack pointer
\＃第草 End of Main Program Initialization
\＃\＃\＃Start of Main Rroyram Eody
\＃\＃\＃End of Main Ryogram Body
烠 Start of Interrupt Service Routines
\＃\＃\＃End of Intersupt Service Routines

部解 Start of Subroistines
\＃\＃\＃Erat of Subroutine＇s
\＃\＃Start of Eenp Locarions and Date Values
CHTELES：．BYTE 异 Elag（ $0=s$ top count）
Trave：．BYTE 费 Mis timer temp value
COMMHD：．BYEE \＄0A，\＄05，\＄02，\＄08，\＄06，\＄09，\＄00，\＄00
\＃\＃\＃End of Tern Locacions and End of Program

## EXPEAIMENT 2

More sophistication
The previous experiment served to prove that we can control the motors at a rudimentary level，but we really need to be a little more sophisticated．What we would like to do is 10 assign particular tasks to the different switches as shown in Table 2

For example．from Table 2 you will see that we want switch 54 to correspond to the command＂Spin Clockwise＂．In order to do this．we need the left－hand motor to drive forwards $(\mathrm{OPI}=1$ and $O P 0=0)$ and the right－hand motor to drive back－ wards（OP3 $=0$ and OP2 $=1$ ）．

The hexadecimal digits associated with each of these commands are also shown in Table 2．If you now cast your athention to the data values associated with the label COMMAND at the end of our program． you＇ll see that these values correspond to those shown in Table 2 （Ah Ha！Much like my mother＇s custard ．．．the plot thickens）．

Save your modified program under the new name of ddexp2．asm．and then modify the GETCOMM routine so that it

## GETCOMM：LDA［SWITCHES］ XOR SFF RTI

The first thing GETCOMM does is to read a value from the inpur port connected to the switches．From Part 4．we know that a closed switch（whichever one we＇ve pressed）will present a logic 0 value to the port，while any open switches will present logic 1 values．But this is the opposite way round to the way we wish to see this data， so we next use an XOR \＄FF insintition to invert all of the 0 s in the accumulator to ls．and vice versa．
Finally，we execute an RTI（return from imerrupt）instruction，which exits this routine and retums us to the main body of the program（with the value we read from the switches safely squirreled away in the accumulator）．
Now insert the following statements be－ liveen the＂start and end of main program body＂commenis：

## BLDIV <br> GETCOMM SETIM <br> MAINLOOP：HALT STA［MGRAPH］ STA［MCONTROL］ JMP［MAINLOOP］

The first of these insinuctions loads the interrupt vector with the stant address of our GETCOMM interrupt service routine． This is followed by a SETTIM（set interrupt mask）instruction，which instructs the CPU to respond to future interrupts．（Interrupts． interrupt service routines．and the interrupt mask were introduced in Part 4．）
Next we use a HALT instruction．which will cause the CPU to stop everything and wait for an interrupt（or a reset）to occur： When an interrupt does occur，the CPU will jump to the GETCOMM routine read a value from the switches，and return to the main trody of the program．
As soon as we do return，we store the value in the accumulator to the 8－bil l．e．d： bargraph display at address \＄F030．and also to the motor controller connected to the output port at address SF 032 ．then we

## Listing 2

Al＝＝Start of＂Get Comand＂Routine

| getccme | 3：．0x | \＄0000 | 4 | Load index register |
| :---: | :---: | :---: | :---: | :---: |
|  | LDA | ［SWITCHES］ | \＃ | Read vaiue on switches |
|  | XOR | SE？ | 4 | Invert os to is etc． |
| GCLIOOP： | RORC |  | \％ | Rotate right 1 bit |
|  | JC | ［GCRETURA］ | \＃ | Jump if carcy fiag $=$ |
|  | Ifick |  | \＃ | Else increment $X$ reg |
|  | TMP | ［GCLCOP］ | \＃ | Jump back and repeat |
| GCRETURM： | LDA | ［COMmand，XI | \＃ | Get command byyte． |
|  | RTI |  | \＃ | Exit routine |

jump back to the beginning of MAIN． LOOP and wait for a new interrupt to occur．

Assemble this program．download the resulting ddexpl．ram file to the PhizzyB， and set the program running．Now click switch S0 on the interrupt－driven switch device and observe the bit 0 l．e．d．on the SF：030 bargrapl display light up．Also ob－ serve that the left－hand motor starts to turn in its＂reverse＂direction．（Refer back to Table 1 at this time to remind yourself how the output bits relate to the motors．）

Now click switch Sl and observe bil 1 on the bargraph display light up and the left－hand motor start tuming in its＂for－ ward＂direetion．Next click switelt S2 and observe bit 2 on the bargraph light up and the righthand motor start turning in its ＂reverse＂direction．Now clich switch S3 and observe bit 3 on the bargraph ligltt up and the righthand motor start turning in its ＂forward＂direction．

Finally．click any of the switches S4 through $\mathrm{S}_{7}$ to stop both motors．Then reset the PhizzyB and proceed to the next experiment．
now looks like the one shown in Listing 2. As you see．our new GETCOMM routine is a litte more sophisticated than our ear－ lier version．First we use a BLDX instruc－ tion to load the index register with 0 ．Then we use an LDA to read a value from the switches and an XOR to invert that value into the form we prefer．
Now we enter a loop slarting ai GCLOOP in which we rotate the accumulator one bit to the right，which causes the bit that＂falls off the end＂to be copied into the Carty flag．If we see a I in the Carry flag we jump to the GCRETURN label．otherwise we incre－ ment the index register and jump back to GCLOOP．
Thus，when we actually reach the GCRETURN label．the value in the index register corresponds to the number of the switch that we pressed（ithat is．for switch So the index register contains 0 ，for switch SI the index register contains 1．and so forth）．
The cunning parn of all this occurs at GCRETURN，in which we use an LDA to load the accumulator using the indexed

Table 2：Associating commands with the switches．

|  |  |  | Output Bits |  | Hex |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Switch <br> 0 | Action Go fonward | OP3 | OP2 OP1 | OPO | Value |
| 1 | Go backwards． | 0 | 10 | 1 | 5 |
| 2 | Turn right | 0 | 0.1 | 0 | 2 |
| 3 | Turn left | 7 | 00 | 0 | 8 |
| 4 | Spin clockwise | 0 | 11 | 0 | 6 |
| 5 | Spin anti－clockwise | 1 | $0 \quad 0$ | 1 | 9 |
| 6 | Stop | 0 | $0 \quad 0$ | 0 | 0 |
| 7 | Stop | 0 | 00 | 0 | 0 |

Listing 3

addressing mode. That is the statement LDA [COMMIAND, X] instructs the CPU to "lond the accumulator with the data in the location whose address is given by adding the address of the label COMMAND with the contents of the index register". (Phew!)
Remember that the PhizzyB's addressing modes are discussed in Appendix A of The Official Reboputer Micropracessot Databook (and also that this Appendix is provided free with your PhizzyB Simulator -see the simulators online help for more details).
The end result is that if you click switch SO. this instruction will load the aecumulator with the data value SOA (stored at the address of COMMAND + index register $=0$ ). If you click switch S1. we'll load the accumulator with the data value 505 (stored at the address of COMMAND + index register $=1$ ). and so lorth.

Assemble this program, downioad the resulting ddexp2.ram file to the PhizzyB: and set the program running. Now click swith SO on the interrupt-driven switch device and observe bits 3 and 1 on the \$F030 bargraph display light up. Also observe that both the left and right motors stant to go in their forward direction.

Refer back to Table 2 to remind yourself how the switches relate to the different commands, then experiment by pressing switches So through S5 to confirm that everything works as expected.

Finally click switch S6 or 57 to stop both motors. then reset the PhrzzyB and proceed to the next experiment.

## EXPEAIMENT 3

## Creating a timing routine

One thing were going to require in the future is the ability 10 perform actions for specific amounts of time. For example. *go forward for 5 seconds, turn rights pause for 3.5 seconds, go forward for 2 seconds", and so fortin.
Before we leap into action, it will be advantageous to our future selves if we spend a little time thinking about the "usage model" for this routine (that is: how are we going to call it from oller parts of the program).
For example, it would be a pain if we created a timing loop that had to be executed some weird number of times, such as "214 cycles round the loop equals one second". In this case, wher-
ever we wanted a specific delay, we'd have to whip out a pencil and start performing calculations (e.g. a delay of 2.5 seconds $=2.5 \times 214=535$ cycles - arrgssh).

A more preferable solution for our purposes would be to be able to instruct the routine to delay for a specific number of $1 / 10$ ihs of a second. With this in mind, save your previous program as ddexp.3.asm and then enter the livo subroutines shown in Listing 3 between the "start and end of subroutines" comments.
First let's consider the ONETENTH subroutine, which is the one we're going to use to create a delay of $1 / 10$ th of a second. We commence this subroutine by loading the accumulator with the value represented by the constant label DELCONST (delay constant), which we originally dechared in the Delay Constants section it the beginning of the program.

Next we stan a low in which we decrement the value in the accumulator and then test it with a JNZ (junnp if not zero) instruction. This means that we keep on looping around until the accumtulator contains zero, at which point we exit from the subroutine. So all we have to do is to assign an appropriate value to DELCONST. such that this routine does indeed take $1 / 10$ th of a second to count the accumulator down to zero.

Now tum your attention to the main TIMER subroutine, whose delay value (sprecified as an integer number of $1 / 10$ ths of a second) is already present in the accumulator when we call the routine (we'll see how this happens in a monent).

The first thing we do when we enter this routine is to save the value in the accumulator to our temporary location TVALUE (which we reserved at the end of the program). Next we store the same value to the CNTFLAG location, on the basis that we're going to use any non-zero value in this location to indicate that the count should proceed.

Now we enter a loop that commences at label TLOOPA. and the first thing we do is to call our ONETENTH subroutine (this is an example of "nested subroutines ${ }^{\circ}$, in which one routine calls another).

As soon as we relurn from ONETENTH, we load the accumulator with the value in CNTFL.AG. If the value of this flag has been mysteriously set to zero we will exit the TIMER routine.
oherwise the count will continue. Nef month we will investigate how we can use an imertupt to modify the CNTFLAG value and terminate the count. The reason we ve included this test here is that adding instructions at a later date will modify (i.e. mess up) any delays generaied by our routines.

Next we re-load the coum value from our TVALLE temporary location. decrement it, store it away again, and also store it to TGRAPH. which is the address of the output port driving the extemal 8 -bit l.e.d. display. Thus, this display will be updared with a new value every $1 / 10$ th of a second.

Next we test the count value to see if it is zero. If so we'll exit this routine and return to the main program, otherwise we'll juntp back to the beginning of the loop at TLOOPA and do the whole thing again.

Now we need to create a very simple interrupt service soutine. Scroll through the program to the "start of interrupt service routines'" comment and insert the following stateruent:

## WAITIRQ: RTI

As we said, this really is a very simple routine. In faet all it does (when it's called) is to return. but that's enough for our purposes as we'll sce.
Last but not least, we need to modify the instructions between the "start and end of nain program body" comments such that they now read as follows:

| BLDIV | WAITIRQ |
| :--- | :--- |
|  | SETIM |
| MAINLOOP: | HALT |
|  | LDA 250 |
|  | JSR [TIMER] |
|  | JMP[MAINLOOP] |

This is very similar to what we did before. In this case, the BLDIV instruction loads the interrupt vector with the stant address of our new WAITIRQ intertupt service routinc. This is followed by a SETIM (set interrupt mask) instruction ${ }_{+}$ which instructs the CPL to respond to future interrupts.

Next we use a HALT instruction, which will cause the CPU to stop everyihing and wait for an interrupt (or a resel) to occur. When an interrupt does occur, the CPU will jump to the WAITIRQ routine. which, as we just discussed. does nothing except retum.
As soon as we do return, we load, the accumulator with a value of 250 (which we understand to mean $250 \times 1 / 10$ th of a second $=25$ seconds). Then we call our timer routine which we hope will count down the appropriate delay. then ive jump back to the begilning of NAINLOOP and wait for a new interrupt to occur.

## TIMER CALIBRATING

Assemble this program to generate the cortesponding ddexp 3 ram file, download the progran to the PhizzyB, and set it ruming.

Now place your wristwatch on the table next to your PhizzyB, wait for the second hand to reach the top, and click the Phiz= zyB's IRQ (interrupt request) bution. Observe that the 8 -bit l.e.d. bargraph display connected to the output port at address
\$F0.31 stants to coumt down from binary 11111010 to 00000000 (from 250 to 0 in decimal). Note the elapsed time in seconds as soon as all of the l.e.d.s are extinguished:

Press the IRQ bution again to re-time the delay. Repeat this several times and average the delay values to try to compensate for human error. This average should tum out to be approximately 38 seconds. which is a bit longer than the 25 seconds we require.

Reset the PhizzyB, return to the assembler and scroll to the DEI.CONST label at the top of the program. and modify the value assigned to this label to 5 . Re-assemble the program and re-run the above tests. This time the resulting average delay should be around 15 seconds, which is somewhat shorter than the 25 seconds we're looking for.

Experiment with different DEL.CONST values until you find the one that gives you a total delay as close to 25 seconds as possible (a DELCONST value of 12 should te as close as makes no difference - dang. perhaps we should have mentioned that before, but then you wouldn't have had so much fum).

Once you've finished calibrating the timer, reset the PhizzyB and procced to the final experiment.

## EXPERIMENT 4

Honing your motor skills
Now we're really humning along, because we've already got everything we need to proceed (no more routines to create). In fact all we've got to do is to "gluc" our existing routines together in a

| Listing 4 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| U\#\# SEart of Main Erogram Eody |  |  |  |  |
|  | BLDIV | GETCOM4 |  | Load interrupt vector |
| MAIMECOP: | SETI: |  | \% | Enable tneerruprs |
|  | HALT |  | \% | Wait Eor an interrupt |
|  | ClRIt |  | \% | Disable interrupzs |
|  | STA |  | \# | Stoze command to LEDS |
|  | STA | [MCOHTROL] | \# | store to motor controller |
|  | LDA | 50 | \# | Load ACC withe 5 secs |
|  | JSR | [TIMER] | $\pm$ | Call timer zoutine |
|  | Lig | \$00 | ! | Lrad Acc mich 0 |
|  | STR | [MORAEH) | \# | Store "motors off" to lisds |
|  | STA | [HCOHTROL] | \# | Store to motez controller |
|  | JME | [MEINLOD?] | * | Hait for a new command |
| 䋨出 End Cif | Main | Program Body |  |  |

stighly different way. So save your program as ddexptasm and then modify the main body of the program as shown in Listing 4.
As we said, we ve seen all of this before in one way or another, so perhaps the best thing to do is to rm the program and see what happens. Assemble the new program to gencrate the corresponding ddexpt.ran file, download this program to the Phizzyb. and set it rumning.
Now click switch $\mathbf{S 0}$ and observe that the 8 -bit l.e.d. at output port $\$$ F030 displays a value of 00001010 and both of the motors start up in their "forward" direction (just like they did in Experiment 2). Also observe that the 8 -bil l.e.d. at output pori SF031 starts to count down from 00110010 to 00000000 (from 50 to 0 in decimal). which equates to a five second delay. As soon as the timer counts down to zero. our new "main body" turns both of the motors off.
Similarly. clicking any of the switches So to $\mathrm{S5}$ will result in the appropriate motors (as specified in Table 2) running
for five seconds before stopping. Pretty cool eh? Next month we'll extend these routines to provide the PhizeyBot with some rudimentary intelligence. but feel frec to experiment on your own until then.

## FURTHER EXPERIMENTS

On the off-chance that you're desperate for something to do, you could try exchanging the addresses associated with the MGRAPH and TGRAPH labels (such that the timer is now displayed on the l.e.d.s as address SF030 while the state of the motors is displayed on the l.e.d.s as address \$F031).

Next. unplug the l.e.d. device from the outpin port at address $S F=031$. replace it with the liquid crystal display (I.c.d.) module from Part 3. and modify the program so as to display appropriate messayes on the l.c.d.. along the lines of "Tuming Left". "Going Forward", and suchlike. (This should keep you busy until we meet
again in Part 6.)

# PhizzyB COMPETITION 1 - The Results 

## AND THE QUESTIDN WAS ... ?

Consider the simple program shown in Listing 1. As we see, this program just loops around reading values from the s-bit switch (connected to the inpur pon at address SFO10) and writing these values directly to the 8 -bit l.e.d. (connected to the output porl at address SFO 0 ).

Using the table provided in Appendix A of The Official Bebopurer Microprocessor Datalook, we can determine that this program uses nine bytes of the PhizzyB's memory (note that this Appendix is provided with the PhizzyB Simulator check the simulator's online help for more details).
Remember that a swich $=$ down $=$ off $=$ logic 0. while a swith $=u \dot{=}=$ on $=$ logic 1. Also an unlit l.e.d. $=$ oft $=$ logic 0 , while a lit l.e.d. $=0$ on $=$ logic 1. Thus, using this program. a pattern of 00110101 on the switches will, not surprisingly, result in a pattem of 00110101 on the l.e.d.s.
As you may recall, the competition described in PhizzyB Par 3 tuterial (EPE Jan ${ }^{99}$ ) required you to perform a very similar

task to that performed by the program slown in Listing I. However, for the purposes of the competition, we requested that you "flip" the value from the switches. such that the state of the most-significant (MS) left-most switch is displayed on the least-significam (LS) right-mosi l.c.d., and vice versa. and so forth for the other bits. For example, a patiern of 00110101 on the switches should result in a pattern of 10101100 on the l.e.d.s.
Furthermore, the judging criterion for this competition was that your program should use the smallest possible number of bytes in the PhizzyB's memory.

## FIRST-PASS SOLUTION

Before we look at the winning entry; it will be advantageous to peruse and ponder some athernative solutions created by the authors to see how one might sel ahout tackling a problem such as this.

In this particular case, the core strategy appeared to be relatively obvious. We know that using a SHL (shift left) instricion to shift the contents of the accumulator one bit to the left will cause the MS bit (the bit that "falls off the end") to be copied into the carry flag. Similarly, we

Table 1.

etc.

## Lísting 2


know that using an RORC (rotate right through carry) instruction to rotate the accumulator one bit to the right will copy the contents of the carry flag into the MS bit of the accumulator.

This means that if we loop around shifting the original value to the left and rotating the new value to the right, we ll end up "flipping" the old value into the new value. For example. consider the sequence shown in Table $1(\mathrm{C}=$ carry flag. $\mathrm{X}=$ don't know/care).
Of course there are many other ways in which we could achieve the same resulh. but this technique seems to offer one of the more efficient solutions. so let's consider a first-pass implementation of this approach as shown in Listing ?.
At the beginning of the program (OLOOP $=$ "outer loop") we use a BLDX (big load index register) instruction to load the index register with the number of times we intend to go around the II.OOP (inner loop). Next we load a value from the switches and store it to al iemporary location called OL.DVAL.
Now we enter our inner loop, in which we shift the old value to the left and rotate the new value to the right. We do this eight times using the index register as a counter and decrementing the index register each time we go around the loop. As soon as the index register contains zero we know that iwe've finished. so we store the new value to the 8 -bit le.d.s and jump back to the beginning of the program to read a new value from the swithes and stant all over againi.

As we see, our first-pass solution requires 35 byles. Hmmm, can we do any better than this?

## USING THE STAEK

As we know, the task is to create a solution that uses the smallest number of byfes. One point to remember is that loads and stores using the absolute addressing mode each require three byles. This can be quite expensive as a percentage of the total program. so it's worth trying to cut down on these instructions as nuch as possible. For example. consider the program shown in Listing 3 (the differences to our previous attempt are highlighted in bold).

In this case we ve decided to make use of the stack, because we can use PUSHA (push accumulator onto the stack) and

POPA (pop accumulator from the stack) instructions to store values on and retrieve values from the stack. and these instructions each only require a single bye.
Thus. we can discard our OLDVAL temporary location, which saves us one byte. Unfortunately. we need to add a BLIDSP (big load stack pointer) instruction. which costs us three bytes. However, once we've done this we can replace two STA (store accumulator) instructions ( $2 \times$ $3=6$ bytes) with two PLSHA instructions ( $2 \times 1=2$ bytes), which saves us four bytes. Also, we can replace an I.DA (load accumulator) instruction (three bytes) with a POPA instruction (one byte). which saves us a further two bytes.
So the end resulh is that we have to add three bytes (from the BI.DSP), but we save seven bytes from our tinagling with the other instructions, which provides us with a total saving of Tour bytes. resulting in a program that is only 31 bytes long. Pretily good so far, but can anyone do better?

## AND THE WINNER IS

The winning solution was presented by Don MeBrien who hails from Ireland. As you'll see. Don quickly spotted the "Shift-Left-Rotate-Right" strategy for solving this problem (ahhough he used a "Rotate-Left-Rotate-Right" variation). However. Don enhanced his solution with a rather cunning lechnique, which involves a simple example of self-modifying code (the concept of self-modifying code is introduced in our book Bebop BITES Back - An Unconventional Guide to Computers).

Before we look at Don's solution, consider what you would expect the following snippet of code to do:

## GROK: LDA \%00000000 STA [OUIPORT]

You probably wouldn' be aniazed to discover that running this program segment would cause a binary value of 00000000 to be written to the output port: But now consider the following:

## LDA G11111111 <br> STA [GROK + I]

GROK: LDA \%00000000
STA [OUTPORT]



Table 3

| Data bytes | Even parity | Odd parity |
| :--- | :---: | :---: |
| 00000000 | 0 | 1 |
| 00000001 | 1 | 0 |
| 00110011 | 0 | 1 |
| 00110111 | 1 | 0 |
| 11111110 | 1 | 0 |
| 11111111 | 0 | 1 |

an extra (parity) bit can be appended to each transmitted byte of data.

The idea is that the transmitter counts all of the Is in the original byte, and uses this number to determine the value of the ninth bil. In the case of an "Even Parity" scheme, the ninth (parity) bit would be set so as to maintain an even number of Is. Altematively, when using an "Odd Parity" approach. the ninth (parity) bis would be set so as to maintain an odd number of is. For example, consider tife data byles and their resulting parity bits shown in Table 3.

In this case, the value written to the outpui port would actually be 11111111. The reason for this is quite simple. but it does requite a lithe "lateral thinking" to get the hang of it. Noie the latel GROK. The address the assembler associates with GROK is the address of the I.DA opcode. so GROK +1 will be the address of the operand (data) byte associated with this opcode.

When we lirst assemble this program, the assembler will cause a binary value of $000000(0)$ to the loaded into the memory location at address GROK +1 . However. when we actually run the program, the first STA instruction will ovenwrite the contents of location GROK + 1 with a value of 11111111 . The second LDA will subsequently load this 11111111 value into the accumulator, and the second SIA will store this value to the output port.

Now consider Don's solution as shown in Listing 4 (the lines highlighted in bold are the ones in which Don uses his selfmoditying code).

As we see. this program has no subroutines. no temporary data locations. and no stack. In fact this really is a rather curning approach. First. Don saves a byte for each temporary location he doesn'i use. But more imporanily, the I.DA instruction al the label LOOP (and the next L.DA three instructions lower down) can now use the immediate addressing mode (two hytes) as opposed to the absolute addressing mode (thre bytes).

The end result is that Don's selfmodifying solution requires only 31 bytes. which equals the authors' best attempts above.

All credit - plus a year's free subscription to the printed edition of EPE - goes 10 Don for a very interesting solution.

## CLEVER DICK!

But you could flay us with wet noodies if you think that we re going to let "Clever Dich" Don beat us (or at least match us) on his first attenpt!

So let's consider what happens if we apply Don's seli-modifying code concept to our last solution from Listing 3. Firsi of all we can lose our NEWVAL femporary location. which saves us one byle. Next we can replace our L.DA [NEWVAL]

(absolute addressing mode) instniction with an LDA $\$ 00$ (immediate addressing mode) version, which saves us a second byte. This modified program is shown in Listing 5 (the modilications are highlighted in bold).
Thus, the authors final version of this progranl requires only 29 bytes. "Ha! Take that Red (Don) Baron! ". And of course this is absolutely the smitlest solution to this prohlem that anyone could ever achiese in the known universe .. . miless you can offer a better solution.
A PIC device can do is in sir bivesl' Edt

## COMPETITION 2

When one conveys digital data from one portion of an electronic system to another or from one system to another, noise or other "glitches" may occasionally tlip a bit from a 0 to al or vice versa.
For example, suppose that we were to transmit a byte of information containing the value 00110111 from one computer to another. bus that something occurred to llip the least-significant (LS) bit from a 1 to as 0 . such that the receiving computer was presented with the value 00110110 .
This could obviously cause problems. so we need some way to defect fand ideally correct) errors in our data. One of the simplest forms of error detecting codes is that of parity. For example.

The trantsmitting unit iransmits each byte along with its associated parity bit. The receiving unit generates its own parity bit for each byte and compares this to the transmitted parity bit. If the iwo parity bits differ. the receiver knows that an error has occurred and can respond appropriately (for example. it could request that this byie be re-iransmitted).
Competition 2 is to write a program that loops around reading a value from the 8 -bit swith device (connected to the input port at address SFOIO), generating the odd-parity bit associated with this value. and displaying this parity bit using the LS bit of the $S$-bit l.e.d. display (connected to the output port at address SF030).

The judgement criteria for this conipetition is that the progrant should read the value from the switches and write the resulting parity bil to the l.e.d. display using the least number of PhizzyB clock cycles (a table of PhizzyB instructions versus clowk eycles is provided in the EPE Ontine Library at www.epemag.com).

E-mail submissions to editor@epemag.cont. The winner will receive a year's free subscription to the printed edition of EPE. The closing date is whenever we decide to "close the doors" and the decision of the judges is final!

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BEST all-round PROGRAM
EWW CAD Review Round Up September 1998


# Alan Winstanley <br> Polling and interrupts set us on course for our super wheelie - the phabulous PhizzyBot! 

THis month's practical constructional article describes the assembly and operation of a simple motorized huggy which can the guided and controlled by a suitably programmed PhizzyB. After several seconds of searching for a moniker which could be attached to Max and Alvin's latest creation, the name PitizzyBos was contrived, and unfortunately it appears to have stuck. So, ladies and gentlemen, start your engines, as they used to say in the golden days of grand prix motor racing.
The PhizzyBot is a two-wheel drive motor platform which can be configured to demonstrate a number of very absorbing practical assignments. In due course we will be actively investigating collision detection/obstacle avoidance as well as simple photo-sensitive applications (e.g. the Phizzy Bot's inquisitive atiraction to artificial light sources).
Such experiments incorporate all of the principles of computer programming which we have described in earlier pants of the PhizzyB tutorial series, as well as introducing further programming commands and lechniques. Oh, and since the PhizzyBot can "feel" its way around by using a system of input detection switches, we will be able to make further use of the simple switch input devices assembled in previous months as well!
This article describes the construction of a simple motor controller which connects directly to a PhizzyB output port. A PhizzyB can then be programmed to read the state at an input port (e.g. using
external microswitches that are activated in a "collision") and a control signal is then sent to the PhizzyBot motor controller board by the Phizzy's output port.

## SIMPLE AND SAFE

The PhizzyBot platform is simple and safe to construct but is remarkably efíestive. It consists of two d.c. gear motors which are switched by a Iransistor-driven relay controller. The motors are battery powered, and later on the associated PhizzyB can be powered by a 9 V rechargeable battery (for example) so that a completely independent stand-alone buggy can be produced, with a PhizzyB fitted on top of the motor platiorm.
The PhizzyBot can then advance, reverse or rotate in either direction under the control of a PhizzyB. By incorporating other types of input circuitry the PhizzyBot can be made to respond to other stimuli - such as a light source (i.e. an attraction to, or repulsion from - hence the possitility of a MothBot and RoachBof). or it could he made to navigate around a maze. We'll be describing more applications and ideas for the PhizzyBot in the concluding parts of this series.
In order to assemble a PhizzyBot, readens will require a hardware PhizzyB single board computer together with the full version CD-ROM of the PhizzyB Simulator, which runs on a Windows $95 / 98$ NT personal computer. The PC is used to assemthe the prograns which are then transmitted via the PC's serial port to the PhizzyB mounted on the PhizzyBut:


The phabulous PhizzyBot!

As usual, we'll give all the necessary con: structional details to enable even a novice to build the PhizzyBot with every probability that it will work finst time. In order to avoid disappointment, our advice is to read through all the following details and to take your time and not to rush the construction,

## PHIZZYBOT CIRCUIT

The circuit diagram for the PhizzyBot is given in Fig.1, which divides into two sections. The motor driver circuit utilises forr transistor switches, TR1 to TR4, each of which controls the coil of a corresponding single-pole changeover relay (RLA to RLD respectively).
Such relays have a single contact (the "pole") which moves between two conticts labelled normally open (n.o.) and normal ly-closed (n.c.). Hence, when the relay coil is powered, it will connect together the pole and the normally-open contact. This occurs whenever the base terminal of the driver transistor is taken to +0.6 V (indicaling a logic "high" on its input resistor - R1 to R4 as appropriate): the transistor switches on and this completes the circuit to the relay coil.

Note that each relay coil also has a reverse-biased diode connected across its coil, in order to snub out the often sulsstaniial reverse voltage (back e.m.f.) which appears when the coil is switched off again.

## MOTOR DIRECTIONS

Two small d.c. motors, M1 and M2, are used for the left and right "engines" which are controlled by the relay contacts. The circuit diagram shows the contacts in their normal (unpowered) states. For the pupposes of our anticles, the descriptions of "left" and "righ" are taken from a PhizzyBot dirver's poinn of view - as if he'd been shmank and sat on the moving PhizzyB.
By reversing the d.c. voltage across either motor, the direction of the PhizzyBot can be controlled. Using motor M1 as an example, we see that this is switched by the relays RLA and RLB whose contacts RLA1 and RLB1 are connected to hoth terminals of motor M1.
The normally-closed contacts ensure that the motor is connected to the 0 V rail when neither relay is powered. This means that the motor will not operate because no voltage is present across it. Similarly, if both relays coils are switched on. then their normally open contacts will now close. Both terminals of M1 will then be connected to the PhizzyBot's +6 V supply rail - which again means that the motor will not function.

However, if one of the relay contacts is "high" at +6 V and the other is at 0 V , then


Fig. 7 Complete circuit diagram for the PhizzyBot motor control interiace.
the motor will be powered. If the contacts are in dissimilar states then the PhizzyBot will move, and the polarity of the applied voltage determines the direction of rotation of the motor's shaft.

It will be seen that the direction of both motors can be controlled very simply ty using a 4-bil code. Each transistor's base terminal connects to the output pins OPO to OP3 of a Phizzy B output port. By turning a transistor on or off, one terminal of the corresponding d.c. motor will be switched accordingly, and thus we can steer the Phizzy Bot in the desired direction. This month's accompanying tutorial article describes how the PhizzyBot can be made to advance, reverse, sicer or rotate in either direction.

The current consumption of each motor is quite substantial - approximately 500 ml A each as measured. Thus the PlizzyBot's motor circuit is powered by an independent 6 V d.c. power supply which, on the prototype, was derived from four AA-size nickel cadmium ( NiCad ) cells ( B 1 to B 4 ), switched by S 1 .

Note that the OV rail of the motor driver circuit is also connected to the 0 V bus of the PhizzyB (wia pin 12 of the Phizzyl3 output port CONI). This is to provide a common OV rail between the Phizzy B and the PhizzyBot. Let's now consider the assembly of the PhizzyBot.

## CONSTRUCTION

The constructional method falls into three sections: the transistor driver circuit, the relay hoard and lastly the motorised platform which will carry the rest of the electronics, including a fully-assembled PhizzyB board! Assembly can commence with the transistor board, see Fig.?.
This circuit is built onto a type "A" T/O board which is cut from one quarter of a full $1 / O$ board. Drill two 3 mm clearance holes for the board mounting hardware (e.g. insulated stand-offs). A good tip: use the enpity board to then mark out a simple template for the drilling centres which will he needed later when the board is fitted to the platform. The circuit board stand-offs will then align perfectly!

Comnience assembly by inserting the transistors which must he orientated as shown (the pinout diagram gives the base view, looking "down" the pins); being semiconductors: they might be damaged by


Fig.2. The trarisistors are mounted on their own board,
excessive heat but, provided that the solder joints are each completed in roughly a second or so, there will not be any problem.

Continue with the resistors and tinned copper wire jumpers. A 20-way box header is used for CON1, which is orientated as shown (look for the central slot along one side, and the arrowhead for pin 1 as usual). A solitary pin header can be inserted for the connection to the 0 V terminal of the
battery pack B1 to B4. It will be seen that a +6 V connection is not needed for this board.
At this point, four 6in ( 152 mm ) long fly ing leads made from general purpose hook-up wire can also be soldered, the other ends of which will eventually connect to the separate relay board: Four ?in ( 50 cm ) link wires are also needed to connect the resistors to their corresponding


Fully assembleditransistor böard, complete with connectorr.


Fig.3. The four relays are also mounted on their own board and linked to the transistor board and mobors as indicated.

OP solder pads next to CON1- however, do not connect to the OP pins of CON1 at this stage.
It will simplify checking and testing later on considerably if a simple colour code is adopted for all eight leads fitted (two leads of each colour). Leave the four short leads "up in the air" for now, because by connecting any of them to +6 V , the associated transistor and relay can be turned on "manually" so that motors and relays can be tested.
Well test the completed circuitry this way at the end, so put the transistor board to one side for now.

## RELAY STATION

The next stage is the assembly of the small relay board which was buif onto a type "B" Phizzy B I/O hoard. Drill two 3mm clearance holes in the board for mounting. then proceed to the interviring as shown in Fig.3. Space is a little tight but the recommended relays will all fit comfortably onto one board, so commence by soldering the four relays into place, observing their orientation (sec inset diagram).
The four diodes (D1 to D4) can follow next, and correct polarity is essential or they will be damaged when the circuit is powered. Check for a stripe which denotes the cathode terminal. The diodes are fed through the board from ahove and their leads should then be cut to length and "doubled over" on the underside to conneet to the relay coils. There are thus four solder joints per diode. Take care not to heat these semiconductor devices excessively with the soldering iron.
Insert the four tinned copper wire jumper leads from above. Four more jumper wires are suggested on the underside of the board to connect each relays n.o. terminals to the +6 V bus. Use insulated wire or PVC sleeving for these links.

The n.c. contacts have to be wired to 0 V , and the prototype made use of a pair of pin headers soldered into place as shown. Four insulated wires were soldered to the n.c. solder pad for each relay. then all four wires were soldered together on the pin headers. The electrolytic capacitor CI can he soldered on the top side and correct polarity is very important. A single pin header can the used for the +6 V connection needed to the battery pack.

We next tum our attention to the PhizzyBot platform in order to install the two motors, after which they can be interwired


## Fully ässembled relay board.

to the completed relay board. We will then be ready to test the motor driver circuitry. and at the same time the connections to OP0-OP3 will be finalised to ensure that both PhizzyBot motors respond correctly to some simple control commands (see "Final testing" later).

## PHIZZYBOT CHASSIS

The PlizzyBot chassis was constructed
and "right"). An area approximately 5 in $\times \operatorname{lin}(127 \mathrm{~mm} \times 25 \mathrm{~mm})$ protrudes from the front to allow leading-edge sensors to he fitted later e.g. for use as a white line follower. Dimensions are not too eritical.

Since the top of the PhizzyBot platform carries a hardware PhizzyB single board computer, the underside of the platform is. used to mount the two motors, the battery from 1/16in ( 1.6 mm ) Plasticard which is available from model shops. It is somewhat flexible but this is not a problem because the PhizzyB circuit board will add rigidity. It is considered feasible that thin gauge aluminium sheet could perhaps be used as a stronger alternative, but the plasticard was cheap and lightweight and was very easy to work with and forms.a good hasis for prototyping.

Approximate dimen= sions of the prototype are shown in Fig. 4. The underside view of the platform is shown (so that it is clear which motor is "leff"


Fig.4. Dimensions of the PhizzyBot platform.
pack and the transistor and relay boards. A short ribhon cable will then interconneet the transistor board and the PhizzyB - see photos.

Recommended gear motors are stated in the components list. but altemative types could be utilised. The suggested ones are mulit-ratio 1.5 V to 4.5 V d.c. motors with self-itssembly gears.

They should be assembled as per the instructions included, selecting an initial speed of $74 \mathrm{r} . \mathrm{p} . \mathrm{m}$. at 4.5 V supply. Therefore, four black gears are used, and three or four spacer washers (supplied) are used to take up the remaining free space on the drive axle. The gearmotors are tolted into position - remembering that the two arles must align along a common axis, see Fig.4. Astute readers will soon realise that if the PhizzyBot is 10 move forward. the two motors must therefore rotate in opposite directions-but let's worry abouf that later!

Linfortunately, during assembly the author found it impossible to close the plastic gearboxes together fully hecause the length of the two internal shafts prevented everybing seating reliably. Some care was needed not to force everything together or the plastic gearbox housing could be easily damaged.

In the end. a solution was achieved by careiully grinding a fraction of a millimetre from the two shorter shafts using a Dremel sanding disk. This completely cured the problem and allowed the gear housings to be snapped together successfully. Readers will soon be able to determine if similar remedial action is needed.

Several drops of light oil are essential on the gears. (Apart from that. your PhizzyBot does not need a $12(000$ mile service interval!)

## PHIZZYBOT IS WHEELY COOL!

The gearmotors are surprisingly powerfut and propel the complete PhizryBot-with ease but the efficiency of the completed Phizzyl3ot depends entirely on the type of wheels used. After some interesting trials the author eventually settled for 56 mm diameter push on rubber tyres which are very "gnippy" and easily transport the


Close-up detall of a motor and its wheel
PhizzyB along any carpeted or smooth surface.

The tyres must be of sufficient diameter to enable the motors and circuit boards to clear the ground. Doly cut the motor drive shafts to length when suitable tyres have heen tested; and the recommended tyres simply push on with a tight fit and are perfect for the job. Each completed gearmotor can be tested using a d.c. battery up to 4.5 V or so.

The rear wheel is just a siabilising jockey wheel, and a model aircraft freewheel with rubber tyre. This ean be fitted centrally along the rear edge. Happily, this performs perfectly in that when the PhizzyBot reverses; the whed


The "jockey" wheel möunted in position. swings round underneath the platform to become the "leading wheel. It swings back out and trails the platform when the PhizzyBot moves forward again.

Simply cut a slot in the chassis and screw the mounting bracket into place. then bend the whed's wire arm to ensure that the PhizzyBot is roughly level. A short piece of PVC sleeving can be fitted tighty over the wire end to prevent the arm falling through. All of the parts fitted to the underside should then be clear of the ground.
Continue consiruction by completing the interwiring between the relay and transistor boaid. plus the iwo motors themselves. Keep the motor wires as short as possible, and they can be soldered directly io the relay thand. The motors should have a polarity marking and there will probably be a" + " sign moulded next to that terminal.

In order to allow for the fact that one motor must rotate in the opposite direction to the other for them to both transport the Phiz?y Bot in the same direction. note the polarity markings of the motor interwiring as shown in Fig.3. A simple test (sce later) will soon confirm that the set-up iscorrect.

The undersicie view of the motor platform, showing the complete Phizysot hardware in position.

At this stage. do not solder those four short wires OPO-OP3 on the transistor board to CON1 just yet, but you can hook over the long flying leads to the relay board. Nylon tie wraps and stick-on bases are useful for tidying up the wiring. or dabs of hot-melt glue can be used to tack down the wiring.

You can now plan for fitting the transisfor and relay boards underneath the platform. The author recommends using M3 (3nm thread) standoffs, 10 mm high. Some care is needed when positioning the circuit boards (see photos). The main consideration is that CONI on the transistor board must align approximately with the output por SF032 on the Plizyls abow it. A short 20-way IDC ribbon cable then connects the two neatly.
It will then be seen that there is sufficien clearance underneath the platform to accommodate CONI and the riblon cable plug, but do not fit the ribton cable at this stage until final testing is complete.

The PhizzyB can then the carefully fitted on the top, using M3 standoffs once again. The motor driver boards can be screwed into place when alt soldering is completed. Henceforth, you will find it easier if you use, for example, an upturned bowl or pot to rest the upturned PhizzyBot during assembly (take care to avoid damaging the PhizzyB's back-up battery when the unit is turned upside down).
Turning our attention to the power source needed for the completed PhizzyBor, at a later stage it will be feasible to power a Phizzy B from an on-board 9V battery. This will allow the PhizzyBot to operate as a completely independert stand-alone buggy.
The author used an ordinary 9 V Ni -Cad battery for testing: a simple power lead was made from a battery snap-on clip and a d.c. power plug, which enabled a 9 V rechargeable battery to plug directly into the PhizzyB board in lieu of an external power supply. For now you should use the external mains adaptor as usual, with the Phizzy B raised up on the bench so that it freewhecls.

As previously noted. the author used a set of four "AA" Ni-Cad cells to power the motor circuit. Expect 30 to 40 minutes before recharging is necessary. A low-profile battery holder was stuck down under the platform using double-sided sticky pads. A single toggle switch was used for Sl, fitted onto the platform centrally.

## FINAL TESTING

The final aspect is to test the transistor and relay board, in order to ensure that the motors operate in accordance with the foltowing truth table:

LEFT

| LEFT |  |  | RIG1HT |  |
| :--- | :--- | :--- | :--- | :--- |
| OP1 OP0 |  | OP3 OP2 |  |  |
| 0 | 0 | - | 0 | 0 |
| 0 | Reverse | 0 | 1 | Reverse |
| 0 | 1 | Reverse | 0 | 0 |
| 1 | 0 | Forward | 1 | 0 |
| 1 | 1 | - | 1 | 1 |$=$

A " 1 " in the table indicates a logic high. which means that when a particular OP lime is high, the corresponding relay will switch on and the relevant motor will operate. By applying a little reverse engineering, we can make life easy for ourselves and force the PhizzyBot to behave as required simply by testing the four inputs OPO-OP3 on the constructed platform, see what happens and then solder the four OP flying leads to the appropriate connections at CÔN1.


## Resistors

| Resistors <br> R1 to R4 2k2 (4 oif) All $1 / 4 \mathrm{~W} \cdot 5 \%$ carbon film |  | See 3月0ロ |
| :---: | :---: | :---: |
|  |  |  |
| Capacitor C1 | $900 \mu$ axial elect. 16 V | page |
|  |  |  |
| Semiconductors |  |  |
| TR1 to TR4 BC548C or any general |  |  |
|  | purpose non <br> transistor (4 off) |  |
| D1 to D4 | 1N4148 signal diode |  |

## Misçellaneous

| St | s.p.s.t. miniature soggle switch |
| :---: | :---: |
| 81 to B4 | "AA" Ni-Cad cells (4 Off) |
| RLA to | s.p.c.o. min. relay, 5 V |
| RLD | $56 \Omega 1$ A, p.c.b. |
|  | mounting, Fujitsu |
|  | FBR211 series (4 off) |
| M1, M2 | 5.5 V to 4.5 V d.c. |
|  | gearmotor (Como |
|  | 430G) (2 oif) |
| CON4 | 20-way IDC box heade |

Phizzy $1 / O$ board, " $A$ " type and " $B$ " type (1 each); $56 \mathrm{~mm} \times 16 \mathrm{~mm}$ solid rubber wheels (2 ofi); model aircratt tailwheel; Plasticard, $\sin \times \sin \times 1 / 15$ in $(228 \mathrm{~mm} \times 203 \mathrm{~mm} \times 1.6 \mathrm{~mm})$ gauge or to suit (see lext); low profile $4 \times A A$ battery holder; battery snap connector: $\mathrm{M} 3 \times 10 \mathrm{~mm}$ mounting pillars ( 8 off); connecting wire; sōlder. etc.
The 20 -way IDC lead and interrupt board required for this part have been constructed previously. (Fluffy dice and go-faster stripes are optional!)


Do this as follows: raise the PhizzyBot off the bench. Ensure CON1 is disconnected from the PhizzyB, then snap on the battery pack and swith on the +6 V supply to the driver boards. None of the motors should turn. and none of the relays should be heard to click. Temporarily look the flying leads from the four resisiors R1 to R4 in turn to +6 V (this equals a logic 1). This should switch on the associated relay and the corresponding motor should then operate.

Try all four leads and see what happens. Prove that each relay powers up, and check that both motors operate in both directions properly. It can require some mental gymnastics to work out what is left, right, forwards and backwards! Remember that we take the PhizzyBut "driver's eye" view in this respect.
From the simple tests you can confirm which OP signal wire performs which function. If all is well and you have followed the diagrams then you will be able to prove the truth table given above, so go ahead and complete the interwiring to CONI on the transistor board. guided by Fig. 2 which gives the wiring details. All that remains is to fit the hoards to their mounting pillars and tidy up the interwiring. then your PhizzyBot is ready for action!

## DRIVING TEST

We are pretty sure you can't wait to road testyour PhizzyBot, so here is a simple test routine which will confirm for sure that your PhizzylBot is behaving itself. Hook up the motor driver board to outpui pori SF032 using a ribbon cable, and use the \&bit interrupt switch device constructed in Part 4. connected to the SF012 input port with a second rilbon cable. Enter and save the routine in Listing I as motortestasuy then download it to your Phizzy B mounted atop the PhizzyBot platform. This is based on the original interrupt program from Part 4. If you then click and release:

Switch S0 - motor 1 (left) backwards Switch S1 - motor 1 forwards
Swith S2 - motor 2 (right) backwards Switch S3 - motor $\underline{2}$ forwards


The PhizzyBot platform with the PhizzyB computer boârd mounted in position, plus the temporary connection of the swithed control board:


Clicking suitches S4 to S7 (see Fig. 4 of Jast month's constructional article) will stop both motors. Thus the PhizziBot can be made to zig-zag around the floor! Further tesis can be made using a full 10 feet ( 3 metres) length (no less!) of ribion cable on the interrupt lateh board and happily the prototype shoutd respond to every command. trundling around the floor and sounding like a food blender on steroids.

A variation of the above routine is used in the main tutorial article, in which Max and Atvin descrite how to program some fundamental control conmands for the

PhizzyBot. You will pröbably find it best simply to ratise the motor platform off the bench top for the purposes of this month's tutorial, and it will be adequate, therefore. to use the mains adaptor for the PhizzyB's power source it this time.

## HELP IS AT HAND

If you have any problems related to the constructional aspects of the series, you can contact the author by E-mail at alan@epemag.demon.co.uk or by writing to the Editorial address. Dont forget that Max and Alvin love to hear from you too. by E-mail to max@maxmon.com.

And if you're new to the PhizzyB (where have you been that you missed paris 1 to 4 ? - if you are an ETI reader we extend a very warm welcome to you!) we can supply back issues of the previous parts - see this month's Back lssues page)!

## NEXT MONTH

Next month, we'll be adding sensory switches to the PhizzyBot and will introduce more computer programming techniques. using the principles of collision detection as a further example of the Phizyl's versatility as a computer teaching aid and control system.

## PhizzyB "LITE"

Undoubtedly some of you reading about our phabulous Phizzyeot will be longing to bunto it but have noi built the PhizzyB itsell - weill, you con't have to build the whole shebang in orde to run our wheely, you can omit a number of Phizzyb parts. Here's a list of what you can omit (you can alvays adod them at a later date if you wish)

* IC5 (32K RAM) and its di.l. socket
* 107 (memory battery back-up controller
- HC10 and IC102 (mo display drivers) and their d.i.I. sockets
EBAR101 (status register lo-biled bargraph)
* DUAL1 to DUAL6 (7-segment displays)
- CONi (50-way boxed header connecior)
In this case we befleve that a Phizzye "Lie" would cosi not much more than hali that of a fully-populated FhizzyB.

Please refer to the full comporents list on page 812, November 1998 issue for more detalls.

# SHOP INTALK with David Barrington 

## Smoke Absorber

Extracting a source for the componenis for the Smoke Absorber project should not prove too difificult. The main problems could be the infra-red detector, low voltage fan, filter and finger quard.

Regarding the surface mount devices (SMDs), these are now widely stocked by advertisers bui you will probably have to buy in minimum quantities. You could try Electromaif ( $=01536$ 204555) or Maplin (limited range).

The PIS201S passive infre-red detector, with an internal i.et amp. was purchased from a Maplin store, code AJ77J. The 12 V brushless d.c. ian also came from them (code YZ38P, 60 mm version), together with the charcoal filled filter pad (code BN91Y) and the 60 mm square finger guard (code JU23A: two required). The filter pad will cost around £10 and comes in a much larger size than needed and will have to be cut to size.

The Sentinel galvanised wire mesh should be stocked by garden centres. Once gain, like the filter pads, it will be necessary to pur chase a piece larger than required and cut it down to size.

The plastic project box came from Maplin, code LFO1B, and the surface mount ZVN2106G MOSFET from Electromail, code 274992. The small surface mount printed circuit board is available from the EPE/ETI PCB Service, cods 223 (see page 220).

Finally, those readers having access to the Internet and wisthing to know more about surface mount may like to "visit" the author, Bill Mooney, on the wab at: www.billssmd.mcmail.com.

## Time and Date Generator

A few points need to be raised regarding components for the Time and Date Generator project. The only listing we have spotted for the El4581 video sync separator i.c has been Maplin, code AJ62S. The rest of the "semiconductor" devices should be generally available.

The PIC16C84-10/P should be the 10 meg version. If you do not. wish 10 program your own PIC chip, a ready-programmed 'PIC16C84-10 microcontroller is available from Magenta Electronics (s 01283565435 or http://www.magenta2000.co.uk) for the inclusive price of $\$ 5.90$ each. Overseas readers acd \&i for postage.

If you do intend to do your own programming, the software listing is available from the Editorial Offices on a 3.5 in . PC-compatible disk, see EPEJETI PCS Service page 220 . There is a nominal admin charge of $£ 2.75$ each (UK), the actual sotware is Free. For overseas readers the charge is $£ 3.35$ surface mail and $£ 4.35$ airmail. If you are an Intemet user, it can be downloaded Free from our FTP site: ftp://ftp.epemag.wimbome.co.uk/pub/PICS/timedate.

You can replace the 2 -way jumper links with straightionerd bare wire links. If you wish to use the "bridging" jumpers, these can be ordered from Maplin, codes UL70M (pin jumpers) and JW59P (pin strip). The interiocking 2 -way 5 mm, p.c.b. mounting, right-angle terminal block should be widely avaiable. The printed circuit board is avallable from the EPEIETI PCB Service, code 221.

## PhizzyBot

Casting a quizzical eye over the parts needed to produce PhizzyBot, our motorised buggy some of the major items: appear, at firs:
sight, as though they could give some readers headaches locating a supplier. The relay and motors are likely to be the main culpnts.

The low voltage, 1.5 V to 4.5 V d.c., Como 430 G gearmotor is currently being advertised by Magenta Electronics ( $\mathbf{3} 01283565435$ or http://www.magenta2000.co.uk) and is listed as MGS (small); there is a postal charge. Model shops may stock similar types.

Turning to the relay. This is from the Fujitsu FBR211 series and is currently held by Fameli ( $\boldsymbol{\pi} 01132636311$ or http://www.farnell. com), code $103-063$.

If you cannot find suitable solid model rubber wheels or an aircrat tailwheel at your local model craftshop, we have found that Maplin (http://www.maplin.co.uk) can supply them. When ordering quote code WCY4C (4 pack) for the wheels and code LJ61R for the taiwheal assembly.

You will also need the 4 -section PhizzyB $/ / O$ printed circuit board. This is obtainable from the EPEIEII PCB Service, code 216.

## Auto Cupboard Light

If you are unable to find a suitable cord-operated type cupboard lamp for the Auto Cupboard Light at your local electrical store, then we suggest you use the one listed by Maplin code KR34M.

The choice of miniature 6V d.c. relay ior this project is important, as apart from it being physically identical, if it is to fit on the small printed circuit board, it must also have contacts rated at 2A minimum to handle any switch-on current surge. The one in the model also came from the above mentioned source, code FM91Y.

The author calls-up a mercury filled tith switch from Maplin, code FE11M. However, due to the highly toxic nature of mercury, we would suggest readers go for the non-mercury version, code DPSOE. Both types are oblanable from Maplin.

A deep-seated lid section is required for the circuit board control case and the recommended one used in the model is the ASS plastic BM12 type listed by the above, code CC82D. No doubt some of our excellent componenis advertisers will be able to come up with something similar. The printed circuit board is available from the EPEIETI PCB Service, code 222 (sce page 220).

## Wireless Monitoring System-2

Having investigated any possible buying bugs for the Wireless Monitoring System in last month's Shoptalk, we only have some good nows to pass on this month. The f.m. versions of the transmitter and receiver modules are also stocked by the companies mentioned last month.

We understand that Radio-Tech (m 01992576107 or htpp://www.radio-tech.co.uk) are presently rurining a special ofier on Radiometrix modules. They are offering a iree additional transmitter module with every order placed for a pair oi f.m. modules We suggest you contact them to see if it applies to this month's f.mi adaptor circuits, quote reference EPE-RM 1 .

Those readers unable to program their own PICs can purchase ready-programmed PIC16C71s for the Transmitter and Receiver from Magenta Electronics ( $\mathcal{\sim} 01283565435$ or http://www magenta2000.co.uk) for the inclusive price of 55.90 each (overseas add £ 1 for p\&p). There are two versions of the Transmitter software: one for the Tilt sensor, and one for the Temperature sensor. Please indicate version(s) required.

The f.m. adaptor p.c.b.s come with the main a.m. printed circult boards. See the EPE/ET/ PCE Service page 220 for prices:

MOST panel mounting components are very easy to fix in position, ancl in the majority of cases a single fixing hole of modest diameter is all that is required. Inevitably there are a few components that are more difficult, and this is usually due to their large size and the large mounting holes that they consequently require.

In this month's article we will consider some of the numerous ways of dealing with large panel cutouts. There is a wide range of tools available for carving large pieces out of metal and plastic panels, but provided you are in no great hurry, some simple and inexpensive tools will suffice.

Moving coil panel meters remain quite popular for use in home constructor projects, and they are a good example of a component that has unusual mounting requirements. The standard mounting arrangement for a normal moving coil panel meter having a front plate of about 60 mm by 45 mm is shown in Fig. 1.

The four small mounting holes are for the threaded rods buitt into the meter itseff. The meter should be supplied with matching washers and fixing nuts. There is no difficulty in marking the positions of these as they are simply at the corners of a square having 32 mm sides.

## Getting the Needle

The large ( 38 mm dia.) cutout ac* commodates the body of the meter. and its centre is at the middle of the square. This cutout has to be made quite accurately, since the meter will not fit if it is fractionally undersize, and it will tend to engulf the four smaller holes if it is made slightly too large.

There is an obvious problem in making a hole of this size, which is larger than normal methods of drilling can accommodate. The cheap way of attacking any large cutout, whether regular or irregular in shape, is to use a minialure round file. For obvious reasons, these are also known as "needle" files.

One or two of these files or even a complete set of assorted shapes and
sizes is something that you should buy sooner rather than later. While files do not represent a quick way of making large cut-outs, they do at least provide you with a means of making virtually any required hole no matter how large or irregular in shape.

The best needle files for making cutouts are "Abrafiles". These can sometimes be a bit difficult to track down, but they will probably be available from at least one of your local DIY superstores.


Fig.1. Mounting holes for a panel meter look easy enough, but a 38 mm dia. hole can be awkward to produce.

Their main claim to fame is (literally) their flexibility, and these files can be bent to reach inaccessible places. This ability is probably of no great importance in the current context, and the bigger asset is their coarseness, which enables them to cut relatively quickly. You can obtain an Abrafile that fits into a special frame that looks rather like a coping saw.

Cutting quickly and accurately with one of these is very easy, and the ability of the blade to cut in any direction makes life much easier than using a fretsaw or coping saw. On the other hand, the frame prevents the tool from making holes towards the middle of large panels, whereas simple needle files can make curs just about anywhere in any panel.

## Don't Fret

If you have a fretsaw or a coping saw it is wortin giving it a try when making large cutouts. There are blädes


The four main pants of a chassis punch (above). The cutting blade (second right) is forced through the panel and into the die (second left). This produces very neat and accurate results.
(Left) The frame version of the Abrafile (iop) and the standard version (middle) provide an inexpensive means of handling practically any cutout. The miniature round file (botiom) is equally versatile, but can be guite slow.
it until the component fits into place properly.

## Ring Around

When using needle files that have fine blades it can take a very long time to make large cutouts. There is a simple but effective way of speeding things up, and this is to drill a ring of 3 mm to 3.5 mm diameter holes just inside the perimeter of the required cutout.

The holes should be about 4 mm or 5 mm apart so that there is a small gap between them, but the gap should be kept quite small. The needle file is then used to join up the holes and complete the cutout.

Most of the material is removed by the drilling, which does not take long despite the large number of holes involved. This leaves relatively little to do with the needle file and greatly speeds things up. The resultant cutout will probably have some rough edges, but it will not take long to file it out to a hole of the correct size having a neat finish.
anything more than thin aluminium panels. For larger holes a "hole-saw" is a better option.

This is basically just a saw blade in the form of a ring, plus a mandrel that enables it to be mounted in a hand drill or power drill. The mandrel includes a drill bit that produces a guide hole of a few millimetres in diameter. The blade then comes into action and produces the main hole.

The blades are quite thick, which means that quite a lot of material is removed when cutting the hole. However, with a power drill on a slow setting it does not take long to produce holes having diameters of around 30 mm to 60 mm . Provided the saw is not seriously worn, the holes should be quite "clean" and neat.

There is an obvious limitation with hole-saws and chassis punches in that each one can only produce one size of hole. Buying several tools of either type individually can be quite expensive, and it is worth considering one of the sets of popular sizes that are available. Although the tools
low speeds, and do not even work properly using most hand drills.

They are just about usable with a heavy-duty hand drill set to its slower speed, but only when used to make holes of up to about 40 mm in diameter. A brace gives the best results and enables large holes to be handled with ease.

A hole cutter is surprisingly fast even when cutting very large holes, but it does not produce neai results. A substantial amount of filing is usually required in order to "clean" the hole, and the hole therefore tends to grow. Consequently, it is necessary to cut a hole one or two millimeires smaller than the required size to allow for this "inflation".

## Cheating

Where possible, you may prefer to avoid large cutouts. In most cases this is not possible, but with miniature loudspeakers there is an alternative to the "text book" approach.

Normally the panel is drilled with a hole that is slightly smaller than the


(Above) Hole-saws produce neat results and are quite fast. They work best in a power drill set to a slow speed. (Left) A reamer can turn a small hole into a larger one, up to about 30 mm . The tank cutter (right) can handle holes having diameiers from around 25 mm to 120 mm .

## Quick Cuts

Where a large round mounting hole is required, such as for a panel meter or a loudspeaker, there are quick ways of making the hole. A chassis punch is a tool that makes the "cleanest" holes, and it is also very quick. It consists of four main pieces, see photograph.

A small guide hole is drilled in the panel, and a bolt with the circular cutting blade attached is fitted into this. A nut and a short metal tube are then fitted onto the bolt on the other side of the panel. The bolt is tightened with ihe aid of an Allen key, forcing the cutting blade through the panel and into the tube. In doing so it punches a round hole in the panel.

In fact there are chassis punches that produce rectangular holes, including some that are designed to produce accurate holes for push-fit switches. Unfortunately, the relatively high cost of chassis punches makes them impractical for one-off or very occasional use, and you will probably only use the round variety.

## In The Hole

A chassis punch is fine for holes of up to about 30 mm in diameter, but larger sizes are difficult to obtain and it can be difficult to use them with
in these sets áre not usually of the highest quality, they seem to be adequate for dealing with the aluminium and plastic panels enz countered in project construction.

Unless you have proper workshop facilities and high quality tools it is advisable not to try making large cutouts in steel panels. Tools that cut through softer materials with no difficulty will usually need a lot of lubrication in order to cut through steel, and may break under the strain.

There is an inexpensive alternative to chassis punches and holesaws in the form of hole cutters, or "tank" cutters, as they, are also known. Tools of this type are available from most DIY superstores.

A hole cutter is a bit like a holesaw, but with only one tooth on the end of an adjustable arm. By adjusiing the effective length of the arm it is possible to produce holes from typically about 25 mm up to around 120 mm in diametér:

These tools are very quick and effective, but a few points need to be borne in mind when using them: First and foremost, they are only intended for use in a hand brace, and should not be used in power drills. They are designed to operate at very
overall diameter of the loudspeaker, and some speaker cloth or fret is then glued in place behind the cutout. It is very unusual for miniature loudspeakers to have any provision for screw mounfing, and they invariably have to be glued in place on the speaker material.

Any good quality general-purpose adhesive should do this well enough, but glue-guns are now remarkably cheap and offer the quickest means of handiling jobs of this type. Avoid smearing adhesive onto the diaphragm of a speaker as this could seriously impair its performance.

The alternative to the conventional approach is to drill a matrix of small holes in the panel to form a simple speaker grille. The loudspeaker is then glued in place onto the panel, behind the matrix of holes.

Making a neat job of this is trickier than you might think, and it is a job that needs to be given due care and attention. It is best to drill small guide holes first and then enlarge these to the required size. An advantage of this approach is that it should leave the panel quite stiff and strong.

Making large cutouts in small panels can cause serious weaknesses and is best avoided.

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## Regular Clinic

## CIRCUIT SURGERY

# ALAN WINSTANLEY 

## This month's roundup of readers' queries and comments follows up with more information on electret microphones, a quick capacitor check with ohmmeters and some pointers for using variable regulators.

The question of using variable voltage regulators to produce simple power supplies crops up from time to time. Here's a query from G. A. Wilsher of Teddingion, Middlesex.

## Regulators sum it up

I have been waiting patienily for a "decent" Bench Power Supply unit to appear when I came across the variable regulator LM338 which will produce an output from 1.2 V to 32 V at 5 A . and this would give me exactly what I'm looking for. Although 1 could manage such a design on striphoard I would appreciare some guidance.

I happen to have a Iransfonner with bridge rectifiers and smoothing capacilors which produces 50 V d.c. The LMM338 specification states that the difference benween input and oupht voltages should not exceed 35 V . What are the implications of this? Second. I can see that two resistors are required to set the voltage. How does one calculate their values?

Three-terminal regulators are simple and convenient to use "on paper"* but there are one or two drawbacks to consider. especially at the higher voltages/currents you are hoping to provide.

I'm not sure that stripboard would be a suitable medium on which to construct a reliable high current design. You might consider assembling with old-fashioned tag strips or "turret board", using heavy gauge solid core wires for interwiring, which should be kept as shon as possible. A simple point-to-point design will cope admirably wilh higher currents as well as having a higher mechanical strength than anything you could make with stripboard.

The outpur ratings of your transformer are not quite clear. so here are a few points to tear in mind. Transformer manufacturers normally rate secondary voltages and currents assuming only a simple resistive load is used at the maximum permissible current of the transformer. This has iwo implications: first. due to its regulation the secondary voltage will rise at lower currents, so allowances must be
made for this (when specifying the rectifier and smoothing capacitor voltage ratings).

Second. although makers specify ratings assuming a resistive load, in a full-wave rectified power supply the load is more complex than that - we are placing rectifiers and a very large smoothing capacitor across the secondary winding. The effect is that we find currents flowing in the secondary circuit higher than those found in an ordinary resistive circuit.

As a good rule of thumb. the transformer secondary current figure should be derated by about 40 per cent in sucl cases. A 5 A stansformer (for example) should only be expected to safely supply about 3 A total load current in a full-wave power supply. see Fig. 1.

Funthermore, that 5A secondary current is only valid when a simple resistive load is used as shown. Ideally your own transformer must be capable of providing 8.3A on full load. but to be realistic you are limited by what you can buy off the shelf or happen to have available anyway.

## Regulations

Looking at the regulator itself. a guaranteed way of destroying such a device is to exceed the input-output voltage. so well done for highlighting this parameter! Incidentally my National Semiconductor data book specifies 40 V as the maxinum input-output differential.
This inpus-output voltage rating is the maximum difference in volage which is


Fig. 1. Transformers mist be derated when full-wave bridge rectifiers and. smoolh. ing capacitors are used. Makers' ratings usually assume a resistive load.

The term root-mean-square (r.m.s.) is applied to alternating voltage and current values, including the ratings of transfomers. Root-mean-square values are useful as a way of comparing a.c. voltages and currens against d.e. equivalents.

An alternating voltage of say 12 V r.m.s. placed across a fixed resistor will produce the same heating effect (power dissipation) as would a voltage of 12 V d.c. (Multiplying the r.m.s. value by $1-414$, the square root of 2. tells us the peak value of the allemating voltage.)

In the example of Fig. 1. the transformer shown will have a manufacturer's secondary voltage rating of 12 V r.m.s. at 5 A output. The voltage will tend to increase when the load curent is reduced, and a value of nearer 16 V or so off-load would not be surprising.


Fig.2. A simple pre-regulator may help avoid excessive input-output vollage appearing across variable regulators. D1 and D2 are the Zener diodes.
permitted between the input and the output pins of the regulator. A bench power supply is likely to be stthjected to some rough treatmen, and it is quite easy to overlook the implications of this rating.

If the input voltage is known to be, say, 30 V d.c. maximum then if the power supply output was shorted to 0V. the entire voltage will appear actoss the device. No harm will be done al 30 V and the device will current limit.

If for some reason the input voltage rises to say 50 V or more then clearly the input-output voltage would be exceeded and you risk destroying the device. Hence you ought not to directly use your transformer to produce a variable design with the LM338. One work-around is to use a form of "pre-regulator" which clamps the input voltage to a safe fixed value.

A simple Zener diode with an extemal pass transistor might be suitable (see Fig. 2) provided the tolerances are not tow wide. You can combine Zener diodes and also "jack up" the voltage with ordinary for-ward-biased rectifiers using sone trial and error it necessary. The output voltage seen at the transistor emitter ( $c$ ) is one diode drop lower than that appearing at the base (b). Remember to select a transistor with a highi enough collector current ming.

## Doing your Sums

Your second question relates to the calculation of the resistor values. The formula is just a derivation of that for a potential divider. A typical three lerminal variable regulator is shown in Fig. 3.

A reference voltage $V_{\text {ref }}$ is developed across the resistor R1 and by applying ordinary potential divider theory we can say:

$$
\begin{aligned}
& V_{\text {ref }}=\frac{R 1}{(R 1+R 2)} \times V_{\text {our }} \\
& \text { so } V_{\text {oin }}=\frac{V_{\text {Rer }}(R 1+R 2)}{R 1} \\
& \text { or } V_{\text {oif }}=V_{\text {ref }}(1+R 2 / R 1)
\end{aligned}
$$

A value of 1.25 V is a nominal value for the reference voltage. A value of 240 ohms is typically used ( 220 ohms will be line) for RI, which allows about 5mA to flow through the resistor chain. A further $50 \mu \mathrm{~A}$ ( $\mathrm{I}_{2 j \mathrm{j}}$ ) flows out of the adjustment pin to OV . but for most purposes it is not often necessary to consider this any further.

One final aspect of your proposed power supply is that you should allow for a suitable heatsink on the LM33S. and it is here where the plan may fall down. Whilst


Fig.3. Calculating the output voltage from a variable voltage regulator. A typical value for $V_{\text {ret }}$ is $1-25 \mathrm{~V}$.

The device is protected from therrial over= load. it does mean that if you ane to enjoy naximum current outputs then some substantial heatsinking will te required. otherwise the device will thermally-limit in extrene ciases.

Unfortumaty. it is not possible to calculate the heatsink's thermal resistance until the power dissipation of the regulator is known. It is precisely because of thermal efliciency problems with such designs that switched-mode power supplies are preferred.

## Electret microphones

Lust month I described the composition of a typical electret microphone capsule. and went off in search of the mysterious transistor contained within. My thanks to Barry Taylor who adds furtlyer interesting comments and describes the electret effeet in more detail:

I would guess that the transistor labelled K596 is probably a $25 K 596$ as 1 have frequently come across devices from the Far East that omit the first wo characters (esp. on TO92 packages). $\AA$ indicates an t-channel f.e.f. or MOSFET. You say that Motorolu suggest a $2 N 5484$ but according 10 my sources this is not a MOSFET but an n-channel j.f.e.?. I regret that / can'। find any listing of a $25 K 596$ though.

You make no mention of the meaning of the word "electrer". which could imply that the varittion in copacitance directly produces the af. output. This is not strictly true even though it is easily demonstrated that tapping a length of screened cable consnected to a high input impedance amplifeir will produce a noise on the output. Electret material. which is a fluoracarbon polywer can be given an electrostatic charge which lasts almost for ever.

If an electret material is used as the diaplraym (wihh one side metallised of course) in a microphone rogether with a rigid back plate then a capacilor is formed with constant charge Q. Sound pressure will cause the diaphragm to vibrate and thus the capacitance to vary. Since the charge is constant and as $\overline{\mathrm{V}}=\overline{\mathrm{Q}} / \mathrm{C}$ the result is a volkage proportional to the somd pressure.

The fe.s. simply acts as an impedunce converter and this must be (as you found only one resistor) configured as a common= source amplifier which, given a suitable j.f.e.t. doesn't need a source resistor if the input signal is small. So. I nould guess that the resistor you destroved was a gate to source resistor with a value of possibly tens. of megohms. I'd be interested if anyone can confirm this surmise of mine ahout the f.e.t.

Professional condenser microphones generally do not use electret materials. The diaphragm is a melallized polymer film polarised by 48 V d.c. via a $/ G$ ohm resistor which results in a yirmally constant charge. The polarising voltage is fed to the microphone via a "phantom" power supply" from the mixer which ulso powers the built-in impedance comerter.

It is true than the " 2 N " or " 2 S " is commonly omited from the nomenclatute printed on the device. My first thought was also that the mysterious transistor was a 2 N or 2SK type but further research drew a hlank (as you found yourself). A device type KSK596 is listed by Mororola but there was no data available. I rechecked the National Semiconductor web sites as des-
cribed last month. and this bounced over 10 the Fairchild web site.

I finally obtained the data shee whicti states "N-Channel RF amplifier" so I'm none the wiser. In retrospect I would concur that it's probably a junction f.e.t. but hopefully l can be forgiven my near miss!

Back to my groaning bookshelves and my Dictionary of Electronics detines "electret" as the electrical analogue of a permanent magnet. It is a dielectric body with separate electric poles and a stable existence. The metallic disk 1 alluded to is a synthetic dielectric film. metallised on one face.

1 agree that there is a dearth of intormation about electrets, which is what prompted the anticle to start with, but a quick look at several microphone manufacurers' web sites did teveal interesting infommation including more on "phantom" poyer supplies. Try wow.sennheiser.com and www.shurecom. I also found an enthusiastic web page on www.mpage.demon.co.ukfelectret.htm. Thark you for filling us in with the extra information!

## Quick cap check

I would like to know how to chect: a capacitor both out of board and in-board. to determine whether the capacitor is leaking or orhenvise faully.

So asks Siva Pathmanathan (by Email). It is straightforward to check larger capacitors just by using an ordinary multimeter set to a high resistance range. A moving coil meter is possibly better for this experiment.

By clipping the test leads to an electrolytic capacitor, the resistance reading will briefly be seen to fall as the capacitor will charge up through the meter's intemal batery. The meter's black lead is in fact positive (the reverse is true on digital multimeters. which have a much higher impedance), so by touching this to the capacitor's positive lead, the needle "blip" will be seen.

With lower value capacitors. probably the best you can hope for is to clieck for a shonted capacitor, which will show as a low resistance.

Checking a capacitor in-situ isn't retiable because other components in the same circuit may affect the readings. In any case. definitely do not chech while power is applied to the circuit. and remember to discharge the capacitor to start bith, using a low-value resimor lor larger electrolytic capacitors if necessary.

## CIRCUIT THERAPY

Circuin Surgery is your column. If you have any quenes or comments, please write to: Alan Winstanley, Circuir Surgery, Wimbome Publishing Lia, Allen House. East Eorough, Wimbome, Dorset. BH21 1PF, United Kingdom. E-mal alan@epemag.demon.co.uk. Please indi-
cate it your query is not for pubication. A personal reply cannot akways be guaranteed but we will try to publish representative answers in this column.

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| :---: | :---: |
| Frequency accuracy | : $+/-5 \%$ of full scale |
| Output waveiorms | : Sine, square, triangle, puise, sawtooth |
| Output impedance | : 50 ohm $+1-5 \%$ |
| 0 dB amplitudé | : 2 Vpp - 20Vpp no load |
| -20dB amplitude | $200 \mathrm{mVpp}-2 \mathrm{Vpp}$ no load |
| -40dB amplitude | :20mVpp-200mVpp no load |
| TLL output | Capable of driving 20 TTL loads |
| Sinewave distortion | <<1\% (at <200kHz) |
| Square rise \& fall | : <20ns |
| Power requirements | $\because 240 \mathrm{Vac} 50 / 60 \mathrm{~Hz}$ (Optional 120Vac) |
| Size \& weight | : $203 \times 195 \times 75 \mathrm{~mm}, 1100 \mathrm{~g}$ |

[^1]FLECTRONIC CIRCUITS \& COMPONENTS + THE PARTS GALLERY by Mike Tooley

## ELECTRONIC CIRCUITS \& COMPONENTS

Electronic こircuts \& Components provides an introduction to the principles and application of the most common types of electronic components and shows how they are used to form complete circuits The virtual laboratories, worked examples and pre-designed circuils allow students to leam, experiment and check their understarding as they proceed through the sections on the CD-ROM, Sections on the disk include:
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Virtual laboraiory - iraffic lighis


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## FUNDAMENTALS

Fundamentals introduces the basies of digital electronics including binary artd hexadecimal numbeting systems, ASCII, basic logic gates and their operztion. monostable action and circuite, and bistables - including dK and D-type fip-flops

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## designing your circuit

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I found that 1 cou'd deign a circuit without nyy teocher's helo. Andit vinikedil Everything was to hand - which chlps to use - and which pins did whit."

Andrew fresion (COSz stustint)

# Interested in progranming plc microcometollers? PICtutor by yohn Becker 



Deluxe PICtutor Hardware

Dēveloped from our lamous. and incredibly popular EPE PIC Tutorial series by John Becker (EPE March to May 98) this CD.ROM, together with the PICfutor experimental and development board, will teach you how to use PIC microcontrollers with special emphasis on the PICx84 devices. The board will also act as a development test bed and programmer for future projects as your programming skills develop.

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The Deluxe Development Kit is supplied with a plug-top power supply (the Export Version has a battery holder), all switches for both PIC ports plus I.c.d. and 4 -digit 7 -segment l.e.d. displays. It allows users to program and control all functions and both ports of the PIC and to follow the 39 futorials on the CD.ROM.

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# TIME AND DATE GENERATOR 

 TONY HARTCapture real time events on your security momitor screen or add them to your home videos with this PIC time machine.

WITH the availability of cheap video cameras more and more people are adding surveillance cameras to the exterior and interior of their homes. Cameras connected to a video recorder will record all sorts of amusing and sometimes nefarious activities. and usually it is useful to know the precise time that these events occur.

Designed for home use, this project adds lime and date information to a multicamera home video security system. The Time and Date Generator inserts a steady and casily readable time and/or date caption onto any composite video signal. The time and date information being displayed at the bottom of the screen.
It also has the optional facility to display a camera number ( 1 to 8 ) provided from an external camera selector by inputting a 3 -bit camera address. This will allow the user to know which one of eight cameras is currently active.

Although originally intended for adding time and date information to security cameras. it is equally useful for adding time and date to home videos. for those who have not got this capability built into their video camera.
This design is based on a PIC16C84 which pertorms the real time clock function and display character generation.

## CIRCUIT OVERVIEW

A system block diagram of the Time and Date Generator is shown in Fig.1. The full circuit diagram is given in Fig. 3 .
The circuit is based around the PIC16C84 microcontroller IC3 (from here-on referred to as the PIC). The PIC has three main functions. namely to maintain the real time clock, to generate all the character pixel data that is necessary to display up to 32 characters at the bottom of the screen and finally to control when the character pixels are displayed.

Each character is composed of an eight pixel by seven screen line matrix. For this number of characters the PIC is not fas!
enough to directly superimpose the required display data onto the composite video signal coming from the video source. Instead, the PIC loads the character display data on a line-by-line basis into an external RAM chip ICS in the period of time immediately after the frame sync.
One line is written per frame, so that every seven frames the RAM is fully updated. At the end of this period the RAM contains all the data required to produce a 32 -character by 7 -line display on a TV/monitor screen.
Between line 256 and 291 the PIC releases control of the RAM chip (IC5) to a fast gated oscillator. IC2a, which clocks the previously stored lines out of the RAM. each bit modulating the input composite video signal on the fly. The gated oscillator is repeatedly synchronised to stan a few microseconds after each line sync. therefore enabling the outpur display to appear steady on the monitor screen.

## REAL TIME CLDĖK (RTC)

The PIC soltware implements a real time clock (RTC) function. The real time cloch data is stored innegisters SEC: MIN,

HRS, DAY. MTH, YRS and YRSH. The DAY register is incremented to the value determined by the MTH and YRS registers according to the verse:
$\because 30$ days hath September. April, June and November. All the rest have 31 except Tebnuary alone which has 28. except 29 during a leap year."

A leap year is normally any year divisible by four except if the year ends in 00 when it also has to be divisible by 400 to be a leap year. So. for example, the year 1900 was not a leap year. but the year 2000 will be.

The RTC only operates when the PIC is powered. either from the main 12 V source or from a batlery back=up.

## RTC ACCURACY

The RTC relies on the PIC's crysial controlled clock for timing. For this application the PIC's 8-bit timer TMRO is set to operate in conjunction with the 8 -bit prescaler to form a 16 -bit counter which is incremented each instruction cycle. This is every $0.4 \mu \mathrm{~s}$ with the 10 MHz erystal specified (XI).
The PIC maintains the real time clock by counting the TMRO overflows. When incremented every insiruction cycle TMRO


Fig. 1. System block diagram for the Time and Date Generator.

## SPECIFICATION

- Adds time or date or both to a composite video signal (NTSC, PAL and SECAM video signals).
- Selectable character height of 5.10 .15 or 20 lines
- Selectable line display from line 256 to line 291 at the bottom of the screen.
- Inverse or normal video display.
- Day and month display are swapable for those who prefer the American standard
- Display extemal camera number from 1 to 8:via external 3-bit input
- Display symbol with camera number via exiernal 1-bit input.
- Leap year correction.
- Baitery back-up if required
- Accurate Real Time Clock with a software trmmer.
- Year 2000 compliant.


## DISPLAY EXAMPLES

Examples of the display are as follows :-

23:59:33 23:02/1998
23:59:33 02/23/1998
23:59:33 $23 / 02 / 1998 \quad$ C-5
23:59:33 23/02/1998 C/5
23.59 .33

23/02/2001
time date displayed day/month swapped camera 5 displayed camera 5 displayed symbol change time only date only
will overflow every $0.4 \times 256 \times 256 \mathrm{~ms}$ or $26-214 \mathrm{~ms}$. At each overflow. a counter called CLOCKI is incremented and every 38 CLOCKIs the clock's second counter (SEC) is incremented.

However, one second is not exactly 38 CLOCK 1 periods, it is actually equal to 1 sec/ 26.214 mms or $38 \cdot 147$ overflows. So incrementing SEC every is CLOCK! periods will produce a fairly inaccurate real time clock as the inaceuracy will be $0.147 \times$ CLOCK 1 or 3.8 ms error every second, which translates to 3.32 seconds per day error - not very good.

## IMPROVED ACCURACY

Luckily. the computing power of the PIC allows some easy manipulation to improve the accuracy. Consequently every seven seconds an extra CLOCKl is added so that 39 overflows are counted. So every seventh SEC count there are $38 \times 7+1$ or 267 overflows. This translates to $267 \times 26.2144 \mathrm{~ms}=6.9992448$ sec. This reduces the inaccuracy from 332 sec/day to about 9.3 sed/day. which is a big improvement.

To improve the accuracy even more. every 35 of the seven second periods another CLOCK is added. So every $7 \times 35$ or every 245 seconds the PIC actually counts $35 \times 6.9992448+0.0262144$ seconds $=244.99978$ secs. This reduces the inaccuracy from 9.3 see/day to about $0 \cdot 1$ sec day.

These numbers assume that the 10.000 MHz crystal oscillator providing all the timing is running at exactly zero parts per million ( $\mathrm{p} p \mathrm{~m}$ ) ertor. This is highly unlikely and with tolerance. temperature and ageing aftects the crystal frequency will be more likely to be within $\pm 100 \mathrm{ppm}$ unless a trimmer is incorporated

An error of 100 ppm translates to an error of about $\$ .6$ seconds per day. So unadjusted. the real time clock is going to keep time 10 better than 10 seconds per day or say live minutes per month. providing the erystal oscillator is running at $10 \mathrm{MHz} \pm 100 \mathrm{ppm}$. A method of trimming has been included in the sofiware to trim the real time clock by up to plus or minus nine secondsper day.

## TIME STDRE

Every time any of the clack counters, c:g minules, hours, day, month or year changes. its value is stored in the EEPROM of the PIC, so that should the power supply fail the time will be reset to what it was before the power failure. except that the seconds count will be reset to zero. There is a slight problem here, in that the data EEPROM of the PIC has a limited lifetime of typically one million write cycles.

For the hours counter, one million writes relates to 114 years of use. But for the minutes counter it relates to only 1.9 years of use which means that the minutes memory location in EEPROM will become unreliable in just under two years time.

To overcome this the minutes value is stored in the EEPROM address calculated from the year ANDed with IF hex and is therefore distributed over 32 EEPROM focations, which increases the EEPROM lifetime from 1.9 years to about 60 years. (Let me know in 60) years if my calculation is comect.)

In practice it is recommended that is rechargeable hack-up battery is used so thiat the power never fails.


Monitor screen shots of the Time and Date Generator module showing two forms of displaying the time and date readout. Note the rechargeable back-up battery.

## DISPLAY CHARACTER GENERATOR

The character format for the numbers 0 to 9 is shown in Fig.2. Characters are conposed of a simple 8 -dot by 7 -line matrix (lines L0 to L6 ). Each numeral is six dots or;pixels wide with a blank dot either side, producing two spaces between characters.

In normal video the top line (LO) and a bottom line (L6) are always blank so this effectively produces an 8 -dot wide by 5 line high matrix which is quite readable. If the inverse video option is enabled the topand bottom lines appear white, so producing the effect of an 8 -dot $\times 7$-line matrix.

The software allows each line of dots to be output once, iwice, three or four times providing actual character height of $5,10.15$ or 20 lines. The user can therefore select the most easily readable format for their particular screen.
Because the PIC is too slow to insen the time/date pixels directly onto the composite video signal, the character data is written by the PIC to the RAM chip (IC5) during the first 256 lines after the frame sync at a rate of one line per frame. The RAM chip used is actually a $2048 \times 8$-bit device, but only one bit of each 8 -bit byte is used here. Each of the five lines of the display plus the spacing lines at the top and bottom of the display (seven lines in total) are therefore clocked into the RAM during a seven frame period.

The bollom 8 -bits of the RAM address A0-A7 come from a 4040 binary counter (IC4) and the top 3 -hits (A8,A9,A10) are driven directly from PORTA of the PIC (IC3). The PIC can control the RAM address by initially clearing and then clocking the counter via PORTB pins RB5 and RB3 respectively.

The PIC can therefore write up to 256bits of data or pixels to up to eight blocks of RAM. wiere each block contains one line of hits to display. This allows up to 32 characiers to be displayed across the screen. although the normal maximum used here is 28 characters.

However. during configuration up to six extra characters are required to be displayed and to allow this each character is displayed with only one space between characters. This bunching up allows up to 36 characters to be displayed. but with slighty less legibility.

## PIXEL LOOK-UP TABLE

The PIC is responsible for compiling and generating the character pixels to store in the RAM. The following is an example that shows how each pixel of the character "2" is formed and Table 1 slows añ extract from the actual software.
On entry to the wrramblik routine the value to be displayed, in this case " 2 ", is in the high nibble of the $W$ register. This is the same as the value to be displayed multi= plied byl6 $(2 \times 16=32$ decimal $=20$ hex $)$. The low nibble is cleared with the andis OFOM instruction and then the pixelsare olrtained from the look up table in segment.

The IV register is stored in memoty location OFFSET which is then divided by 2 by the rr斤 OFFSET instruction. This is now the equivalent of the value to the displayed multiplied by 8 and gives the offisel to any number in the look up table.

10

|  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 000000 | 00 | 000000 | 000000 | $\infty$ | $\infty$ | 000000 | 000000 | 000000 | 000000 | 000000 |
| 12 | $\infty \quad \infty$ | $\infty$ | 00 | $\infty$ | 00 | 00 | 00 | $\infty$ | $\infty$ | $\infty$ | $\infty$ |
| 13 | $\infty \quad 0$ | $\infty$ | 000000 | 000000 | 000 | 000 | 000000 | 000000 | 00 | 000000 | 000000 |
| Ls | $\infty$ | $\infty$ | 0 | $\infty$ |  | 00 | $\infty$ | 000 | 00 | 0000 | $\infty$ |
| 15 | 000000 | 00 | 000000 | 000000 |  | 0 | 000000 | 000000 | 0 | 000000 | $\infty$ |

Fig.2. Chäracter format ior the numbers 0 to 9.
To this is added the number stored in POINTER and copied to the program counter $\mathbf{P C}$. The value is then returned in W and copied to the memory location SHIFT. POINTER is effectively the current line of the character:

For example. if line 5 of the character $\cdots 2^{\prime *}$ is required. then the value added to the PC is as follows:

Character in W 25 h andlw $\quad \mathrm{FOh}$ result $\quad 20 \mathrm{~h}$ div by 2 10h add POINTER 05h result
the MSB ${ }^{* 2} 2^{\prime \prime}$ is to be displayed clear the low nibble
this is the character ofiser line number
this is added to the PC

On returning from the segment toutine the value in $W$ is placed in register SHIFT. The software then shifts the eight bits (01111110) of this data, MSB first, into the main R.AM chip ICS by outputting each-bit in turn and generating a WE and CS putse for the RAM. If inverse video is selected then all-bits are simply inverted before shifting into the RAM.

After each-bit is shifted to the RAM, the RAM address counter IC4 is incremented by the PIC (IC3) generating a clock pulse on pin RB3.

At the beginning of each Jine the PIC resets the RAM
151 is added to the Program Counter and the PC is changed to address +15 h and executes the following instruction
retlw $h^{2} 01111110^{\prime}$
iherefore retuming with the value 01111110 in the $W$ register

In summary
If pointer $=00$ then retis $b^{3} 00000000^{\prime}$ is executed If pointer $=01$ then retlw b'01111110' is executed If pointer $=02$ then rellw $b^{\prime}\left(00000110^{\prime}\right.$ is executed If pointer $=03$ then retlw $b^{\prime} 01111110^{\circ}$ is executed If pointer $=04$ then retlw $b^{\circ} 011010000^{\circ}$ is executed If pointer $=05$ then retlor $b^{\prime} 01111110^{*}$ is execuied If pointer $=06$ then retlw $b^{\prime} 0000000100^{\prime}$ is executed

Table 1: Example of the use of a Look-up table for character generation

address counter to zero and increments the RAM address A8.A9.A10 by incremensing PORTA. In this way all the pixels are loaded into the RAM for each line of all characters to be displayed.

## DISPLAY CIREUIT

Some relevent waveforms to help understand the display circuit operation are shown in Fig.4. The PIC is internupted every line sync and counts the number of lines following the frame sync. At the appropriate display line (lines 256-291 untder software control) the PIC releases control of the RAM clock to a fast clock symeltonised to the line syme.

Initially at the start of each display line the PIC resets the counter ICA by raising and lowering PORTB pin RBS (CNTRES). Then it allows the gated oscilfator (IC2a) to clock the counter IC4 by simply setting PORTB pin RB3 tristate. allowing the gated oscillator IC2a to clock the counter IC4 via resistor R8. The counter increments the RAM address and the RAM outputs the previously stored data for the line of the display controlled by the upper three address lines set by PORTA pins RAO. RAI and RA2.

The counter IC4 increments 256 times until counter output Q9 gaces high which pulls the clock input high via diode D2. This inhibits any more of the gated oscillator clocks until the counter is reset at the beginning of the next line. Without this mechanism the comer would overilow and the line could be partly displayçd again.


Fig.3. Complete circuit diagram, including back-up battery, for the Time and Date Generator,


Fig.4. Example waveforms for the Time and Date Generator circuit

## RAM DUTPUT

The RAM output contains a few glitches due to the address counter IC4 being a ripple counter. This means that the higher Q outputs change state later than the lower outputs after a clock inpul resulting in a temporarily incorrect address being presented to the RAM address inputs. This effect is overcone by adding capacitor C7 which reduces the width of the glitches to a level that is not discemible on the monitor or TV screen.

The deglitched RAM output is gated by an active high signal called ENVIDO from the PIC pin RB1 which goes high only during the lines that are to be used to show the display. These signals are ANDed together by 1 C 2 c and 1 C 2 b . The output of IC2b then overwrites the existing video signal. The ENVIDO signal is timed so as to prevent any modulation of the input video signal during the syne period.

Resistors R3 and R4 form a potential divider to reduce the gate output voltage to 2.5 V . This is then added to the video signal by resistor R2, producing a peak white signal level. The PIC does this for each of the seven lines that make up the display characters resulting in a steady display of the time and date. The character height can be selected by making the RAM output each of the character lines $1,2.3$ or 4 times.

This design is simple but has some drawbacks. For examiple, the time/date data simply superimposes a peak white level on the composite video signal. Therefore, if peak white is currently being displayed it is possible that parts of the display will not be discenible from the picture information. However, this can usually be overcome by adjusting the line display position during setting-up and has rarely lxeen found to be a problem. To enclose the graphic output and insert the data would require the design to be much more complicated.

## SYNC SEPARATOR

The video signal input (at temmal strip TB! pin 2) is capacitively coupled by capacitor Cl to the Video Sync Separator chip ICI. This chip can be either an LM1881 or the slightly inproved EL4581. both devices being pin compatible.

The only extermal components required for the video syne separator chip to function are resistor R 5 and capacitor C4. C4 is not
really required for use with the EL4581 so can be omitted if required.

Here the video sync separator produces active-low frame and line sync signals extracted from the composite video input. Both these signals are passed to the PIC nicrocontroller (IC3) and the line sync is used as an interrupt. The line sync signal is also used to synchronise the gated oscillator circuil ICla to enable a steady display to be obtained.

## GATED OSCILLATOR

The Gated Oscillator is based around the 2-input Schmiti NAND gate IC2a, see Fig.3. The oscillator is formed by teedback resistor R 7 between the gate output (pin 3). input (pin 2) and capacitor C3. The gate oscillates al approximately 6 MHz to 7 MHz when the other input ( pin 1 ) is high.

However, this inpui (pin 1) is pulled low every line sync by the sync separator line output via diode D1, which discharges capacitor C 2 causing oscillatión to be stopped. The active-low line sync pulse is only about $1 \mu s$ wide. so after the pulse returns high, capacitor C2 starts charging again, via resistor R6. After approximately $10 \mu$ s the voltage across C 2 exceeds the gate logic 1 input positive threshold and the gate stants to oscillate again.

This arrangement synchronises the gate output to the line sync and starts the oscitlator $10 \mu \mathrm{~s}$ after the line sync. This time determines the display stant point from the lefi-hand edge of the montitor screen. The frequency of oscillation determines the apparent width of the display on the monitor screen. If it is found necessary 10 adjusi these values to best tit the display into the monitor screen then adjust resistor R6 to control the start position and R7 to adjust the display width.

## BRDWNOUT DETECTOR

The brownout detector circuit, made up of transistor TRI and resistors R9, R10 and R11, will deleet when the 5 V supply begins to collapse and apply a hard reset level to the PIC. This is necessary in this circuit to prevent erroneous values being written to the data EEPROM when the power supply is removed.

Without the brownout detector it is not uncommion to get rubbish written into the

EEPROM, resulting in incorrect time and date data being stored.

## POWER SUPPLY

The circuit is designed to operate from the same +12 V supply as the camera but will actually operate from about +7 V to +30 V . The lower limit depends on the type of +5 V voltage regulator (IC6) used.

A low drop out version will allow the supply to approach $\div 5$ volts. The upper in put volage is really only imposed by the maximum regulator input voliage. The current consumption is only about 10 mA .

The design also allows for a back-up bat= tery to be used which nay, if required, be a rechargeable type. It is recommended that a PP3 NiCad type be used.

If the nomal supply is 12 V and link GK2 is filted, the battery will be trickle charged at approximately 0.3 mA via resis= for R17. If a hither supply is used then the value of R17 may need to be increased to ensure that the rechargeable battery is not over charged, although the value specified is probably sufficient for an input voltage of up 1020 V .

Note that if a supply of less than the battery voltage is used, then power will be supplied from the back-up battery and not the main power source!

## EXTERNAL CAMERA INPLT DATA AND SYMBDL

In a multiple camera system, where the image shown on the monitor can come from a video selector. it is useful to be able to display the number of the camera in use on the screen. This design allows a binary number input to terminals TBI pins 7,6 and 5 (bits 0.1, 2 respectively) to be displayed as a decimal number on the screen. The actual decimal value displayed is the inputted binary plus one. in other words. if the binary input is 0 then 1 is displayed, and if the binary input is 7 . then 8 is displayed on the screen.
The three input bits are read during the space of time after the PIC loads the RAM and before the gated oscillator causes the RAM to superimpose data onto the video data. During this period PORTA pins RA0. RA1. RA2 and RA3 are changed to inputs and all the bits are read via input resistors R13 to R16.

Additionally. the input to TB1/4 alters the symbol between the " $C$ " and the camerd number. This can be used to indicate an exiernal event or simply be set for preference.

| Terminal (TB1) |  |  |  | Display |
| :---: | :---: | :---: | :---: | :---: |
| 4 | 5 | 6 | 7 |  |
| 0 | 0 | 0 | 0 | C -1 |
| 0 | 0 | 0 | 1 | $C-2$ |
| 0 | 0 | 1 | 0 | $C-3$ |
| 0 | 0 | 1 | 1 | $C-4$ |
| 0 | 1 | 0 | 0 | $C-5$ |
| 0 | 1 | 0 | 1 | $C-6$ |
| 0 | 1 | 1 | 0 | $C-7$ |
| 0 | 1 | 1 | 1 | $C-8$ |
| 1 | 0 | 0 | 0 | $C / 1$ |
| 1 | 0 | 0 | 1 | $C / 2$ |
| 1 | 0 | 1 | 0 | $C / 3$ |
| 1 | 0 | 1 | 1 | C/4 |
| 1 | 1 | 0 | 0 | C/5 |
| 1 | 1 | 0 | 1 | C/6 |
| 1 | 1 | 1 | 0 | C $/ 7$ |
| 1 | 1 | 1 | 1 | C/8 |

A logic 0 is any voltage less than $2: 5 \mathrm{~V}$ A logic 1 is any vollage greater than about 2.5 V up to 12 V


Fig.5. Selup and main hop initialisation flowchart.

The camera number display cifn be disabled if not required during seting up with the F6 setting.

## SDFTWARE

The PIC program for the PICI6CK4 has been written using MPASM. but may casily be converted to T.ASM assembler format.

Software for the Time and Date Generator is available un a 3.5 in . PC-compatible disk from the EPE PCB Serr= ice. The software is free but there is a nominal postage and handling charge. If you have access to the internet, it can be downloaded free from our web site: fip:/ffip.epermag.wimborne:co.uk/pub/

## PICS/limedate.

See Shopralk page for details of how 10 obtain a ready-programmed PIC16CS4-10 microconmoller.

When progranming the PIC the fuses should be sel as follows:

WD on, OSC HS. CP off. PU on.
All of the PIC's memory is used except. for just a few locations. The sotiware is really 100 complex to discuss in any detail here, but two lloweharts are included to indicate the main functions.

The initialisation and MAIN loop of the software where a line of data is writen to the R.AM every frame is shown in Fig. 5 , the switch is checked and the real time


Fig.6. Simplified flowchart for the interrupt routine.
clock (RTC) is mannained. This protess is repeated continuously. Fig. 6 shows what happens after each line syme interrupt and how control of the RAM is released to the gated oscillator when the display line is reached.
Other aspects of the software such as the character pixel generation have already been diseussed in detait.

## EONSTAUCTION

Detaits of the printed circuit board topside component layout and underside copper tracking are shown in Fig.7. This boand is available from the EPE PCB Service. code 221.

| Comboryucs |  |
| :---: | :---: |
| Resiistors |  |
| R1 | 1000 |
| ${ }_{\text {R3, }}^{\text {R }}$, 4 | 470 ? (2 off) |
| ${ }_{\text {R }}^{\text {R }}$ | ${ }_{\substack{\text { big } \\ 150 \mathrm{k}}}$ |
|  |  |
| R88 88 | ${ }_{220 \mathrm{k}}^{1 \mathrm{k}}$ ( ${ }^{\text {2 }}$ (ALK |
| Rio, Rif | $1 \mathrm{M}(20$ ) Page |
|  | 100 k |
| A13 10 R17 10 N (5 off) |  |
| Capacitors |  |
|  |  |
| $\begin{array}{ll} \mathrm{Cl}^{2} & 100 \mathrm{n} \\ \mathrm{C} 2 . \mathrm{C3} & 100 \mathrm{p} \end{array}$ |  |
|  |  |
| C5, C6 ${ }^{33 \mathrm{p} \text { ceramic dis }}$ |  |
| $\mathrm{Cl}^{\text {c }} 1220 \mathrm{p}$ ceramic d |  |

Semiconductors

| D1 to D4 | 1N4148 signal diodes (40ff) |
| :---: | :---: |
| TR1 | BC557 pnp transistor |
| C. | EL4581 or LM1881 video sync separator |
| 1 C ? | 74HC132 quad Schmitt |
|  | NAND gate |
| IC3 | PIC16C84-10 |
|  | pre-programmed |
|  | microcontroller seex |
|  | Shoptalk |
| IC4. | 74HC4040 12-stage |
|  | binary counter |
| IC5 | $61162 \mathrm{~K} \times 8$ static RAM |
| IC6 | $78 \mathrm{~L} 05+5 \mathrm{~V} 100 \mathrm{~mA}$ |
|  | voltage requlator |

## Miscellaneous

S1 min. pushbution. right-angle, p.c.b mounting switch (press-to-make, release-tobreak)
X1 10 MHz crystal
TB1 10 -way screw ierminal strip, made up from linkable 2-way 5 mm p.c.b. mounting terminial block (5 olf)
Printed circuit toard available from the EPE PCB Senvice, code 221; 2-way pin jumper and strip (2 off); 8-pin di.i. socket; 14 pin diil. socket; $16-\mathrm{pin}$ di.i. sockef: 18 -pin di.i. socket; 24-pin did. sockel: coaxial cable; optional 4 -way 2-pole rotary switch; multistrand connecting wire; 9 V rechargeable battery (PP3); solder etc.

There are six bare wire links on the p.e.b. and construction should commence by soldering these in position first. These may be made from lengths of tinned copper wire or zero olm resistors if you have any.

Solder all the other components on the board. working from the smallest up to the largest. The PIC microcontroller and the other i.c.s should all be mounted in d.i.1. suckets. Pay particular attention over the orientation of the i.c.s and diodes.

The p.c.b. lemminal block may have to be slotted rogether to form the correci number of "ways". depending on the type purchased. Do not forget the two "jumper" links LKI and LK2 if required. see later.

If the generator is to be mounted away from an accessible case edge, then an extemal swith may be included. This is efrectively wired in parallel to the on-board switch S1. Which may be omitted if not required.

No details have been provided for filting the Time and Date Generator into a case, because the unit was intended to be included within the enclosure of an existing video switcher. However, if it is required to case the generator there are many suitable plastic or metal cases available and there are no special precautions? that need to be taken.

Holes have been provided ateach comer of the p.c.b. to facilitate fixing if tequired. Alrematively the p.c.b. could be fixed to another boand by replacing the lemminal strip with stiff wire links and soldering these down directly.

Finally, double-check for solder shorts or integraled circuits the wrong way round. When confident, wire up the generator, as shown in Fig.S and switch on the 12 V power supply. Hopefully some form of display will be shown on the monitor screen. Initially there may be a lot of dashes, indieating silly values. but after following the setting up procedure a clean display should soon result.

## SETTING LJP

How to connect the Time and Date Generator up to the video camera and power supply is shown in Fig.X A typical four-camera system set-up, that allows the camera number to be displayed. is shown in Fig. 9.

If video termination is required then use shorting link LKI. otherwise leave the link open. With LK। open the input impedance is approximately 355 ohms, which will not affect the pieture quality much. With LK L. fitted the input impedance is 78 ohms.

The terminal block TBI pin functions are as follows:

## Pin Function

[^2]

Fig.7. Priñted circuit board topsidè component layout ànd full size underside copper foil master pattern for the Time and Date Generator. The right-angle 10 -way screw terminal block (TB1) may have to be made up from "linkable" 2-way types.


Fig.8. Typical interwiring and power supply arrangement for a single video camera setup. The external swith is optional and replaces S1 on the p.c.b.

Table 2: Function Settings

| T | Select character height of 5 . 10.15 or 20 lines. |
| :---: | :---: |
| F2 | Select line display from line |
|  | 256 to line 291. |
| F3 | Inverse or normal video. |
|  | display. |
| F4 | Select time or date or both. |
| F5 | Swap day and month display. |
| F6 | Dispriay camera number |
|  | (1 to 8) and symbol |
| F7 | Trim accuracy by up to |
|  | „9 seconds per day. |
| HOURS | adjus! |
| MINUTES | adjust |
| SECONDS | adjust |
| DAY | adjust |
| MONTH | adjust |
| YEAR | adjust |

The display time, date and switchable functions are all controlled by a single pushbution switch. S1. The switch used may be Sl on the p.c.b. or it may be an extenal one wired between ferminals TB $1 / 1$ and $T B 1 / 10$ (Gnd).

When the pusibution switch $S I$ is pressed for more than five seconds the display enters the set-up mode and shows "F1'. A typical display being as shown below:

## FI 23:59:33 23/02/1998 $\quad \mathrm{C}-5$ (function 1 sclected)

Quick presses of the pushbutton then allow the required selection to be changed and displayed for that function. In the case of F1 the display height will be changed every quick press. The display is cyelic, in that after the maximum height. the next press will seleet the smallest height.

To gel to the next function hold the push button on for five more seconds. In total there are seven function settings (FI to F - see Table 2) before the time adjusiment is reached. After iF the real time clock adjustment can take place and the timer being controlled will flash. The year value increments from 1997 to 2096 before returning to 1997.

All settings and times except seconds are stored in the PIC's EEPROM such that if all power is lost then only a few settings will require changing when power is restored. A description of each of the


Fig.9. A typical four camera system using a 4-way 2-pole rotary switch for camera selection. The power supply connections are not shown in this diagram - see Fig. 8.
function settings oblained by keeping the pushbution switch SI pressed for more than'ilew seconds is shown in Table 2.

If the pushbuton is inactive for approximately ten seconds the set mode is aborted ind the display retums to normal.

## TRIMMING

The best way to trim the real time clock is to set the trim value in the function F 7 setting to zero. Adjust the clock display to read the same as a known accurate clock (e.g telctext) and leave for 24 hours. After 24 hours the clock will probably have lost or gained a few seconds. Adjust the trim
value (F7) to match the number of seconds as in the following examples:
If the display is five seconds fast, set $\mathbf{F 7}$ to 5 . If the display is nine seconds slow. set F7 to - -9 (the ' - ' indicates minus here). This will apply the appropriate trim value to compensate for the error. Up to plus or minus nine seconds can be accommodared with this method.
If the real time clock runs faster or slower than this the crystal oscillator X1 must be oscillating more than 100 ppm fast or slow. If last increase (try doubling) the value of capacitors C5 and C6. If slow decrease (try halving) the value of these capacitors.


Layout oi components on the finished p.c.b. The pushbutfon function setting switch is on the bottom-feft corner.


Monitor screen shot capturing the time and date together with the "camera-on" indication ( $C$ - 1 )


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## Motor Controller With Centre Off

Pvise width modulation (PWM) provides an efficient and reliable way of controlling the speed of d.c. motors. especially at low speed. In addition to a rotary speed control. these pulse controllers otten have some means of swithing the motor direction.

However, the use of two controls can be cumbersome, especially if frequent direction changes are needed. One solution is to have a single rotary control configured so that in its mid-way position. the motor is stationary. Turning the control clockwise, will result in forward motion, and anti-clock wise. reverse. The speed increasing when it is advanced.

The circuit diagram presented in Fig. 1 is a PWM Motor Conirolier, with a centre-off facility. The first section of the circuit generates two pulse-width modulated signals. one for forward. the other reverse. The second section combines these signals to determine which direction is required and also to provide the output drive for the motor.
The pulse signals are formed by two retriggerable monastahles, contained in IC?. a 4538 B dual precision monostable. Both monostables (IC2a and IC2b) are triggend simultaneously by a common clock pulse provided by ICl . which is a 4047B used in astable mode. Timing components resistor RI and capacitor C2 produce a ifequency of approximately 390 Hz

When the Speed/Direction control VRI is in the central "off" position. the time perious of 1C2a and IC2b will be slightly lomger than that of the triggering clock pulse. Both of the invented outputs (pins $\overline{7}$ and 9) will be continuously low, because both monostables will retrigger before their time elapses.

By adjusting VRI. the resistance between capacitor C3 and resistor R2 can be reduced. so the time period associated with IC2a will decrease. Since the monostable will now complete its siming before being retriggered. a square wave will appear on IC2a pin 7.

The mark-io-space ratio will be determined by how far the contral VR1 is pdvanced. When fully advarked, only R2 ( 4 k 7 ) will be used to charge $C 3$ and the time period will be about I20ps. which is approximately 4.7 per cent of the width of the clock pulse. This gives an output with a 95 per cent duty cycle.

When VRI is tumed the other way the same thing happens to the other monostable IC2b which provides the second pulse signal. Note that 4.7 kilohms is the minimum recommended value of timing resistor.

By pertorming a logic OR operation on the monostable outputs, a signal is produced whose duty eycle is dependent on the position of VRI. It will have a high mark-to-space ratio at either end of VRI's
travel. reducing towards the middle: and completely off when centrally positioned. IC3c and IC 3 d ( 40018 quad NOR gates) are connected to provide the OR function, which is used to drive the emitter follower output stage of Darlington transistor TR2.

## In Reverse

The relay RLA revarses the motor direetion and is driven, via transistor TRI. by an RS flip-flop, comprising the NOR gates IC3a and 1 C 3 b . The two monostable outputs are connected to the tip-flop $R$ and $S$ inputs.

Since the monostables outputs will not both be high at the same time, the output of the tlip-flop will go high when the outpur of IC 2b goes ligh. The flip-flop remains in this state until reset by the output of IC 23 going fligh, which indicates a direction change, the relay then reverses the molor's rotation.

In practice it may be necessary to adjus! the frequency at which ICl oscillates, by varying the value of resistor R1. The supply voltage is not critical and can be chosen to stit the notor used.

However. the supply must stay within the limits of the 40001 series, that is 5 V to 15 V . iand also comply with that of the relay coil. Capacitor Cl was included to decouple the supply.

Steve Teal. Witney, Oxam:


Fig. 1. Circuit diagram for a PWM Motor Controller, with centre off.


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# \section*{Constructional Project} <br> WIRELESS MONITORING SYSTEM 

## JAMES HUMPHRIS



## Part Two

## Low cost radio-linked short-range dạta logging is now readily and simply available.

LAst month we gave the circuits and constructional details for building an a.m. Transmitter and Receiver. This month we set abour testing the system modules, consider the software and also offer the option of going f.m.

## RECEIVER TESTING

It is essential to get the Receiver unit to operate comectly before the transmit!er units as the later do not have any physical indicators as to whether or not they are functioning properly.

Without a PIC inserted into the Receiver. plug in and switch on the power supply. If the voltage regulator ICl is working correctly and there are no power supply shorts on the board, l.e.d. D3 should light.

If this is the case, then test the powef connections on all the i.c.s to ensure a clean 5 V supply of the correct polarity. In this state. the board should consume around 16.3 mA . If it is a little higher or lower than this level then don't worry. as long as it is not excessive.

Check the COMM connection on the board near ICt, it should be at +8.7 V . Also check that the case is well grounded with a continuity tester or multimeter.

Switel off the unit, carefully insen the programmed PIC and conneet the serial lead between the receiver and the computer, connecting to the port that is in use dit 9600 band with 8 data bits, I stop bit and no parily.

Load either HyperTerminal in Vindows 95 or Terminal in Windows 3.1 (or any other terminal package that is available). Check the flow conmrol is either set to Hardware or Nore and the emulation is set 10 ANSI.

When the receiver is first switched on. le.d. DI should fash briefly and the PC screen should display the message "Hello". This indicates that the unit has booted up correctly and is ready to receive r.f. signals.

If this is not the case, use an oscilloscope to check that a $3: 6864 \mathrm{MHz}$ square

Wave is present on pirts 15 and 16 of 1 C 2 . If this is OK. then check that 1 C 2 pin 4 has a good 5 V connection and look for a brief pulse train on pin 11 of IC2 as power is applied. If this is present. test IC4 pin 14 in the same way for a brief pulse chain of $+8.4 \mathrm{~V} 10-8.4 \mathrm{~V}$.

Note that the duration of this pulse train is only tive bytes at 9600 baud $(5.2 \mathrm{~ms})$ and can casily be missed. The receiver unit must be operational tefore the tuning of the iransmitter units as, otherwise, you could end up in a very difficult situation where nothing appears to work!

## TRANSMITTER TUNING

Remove the PIC from the Transmitter module and shor pins 1.3 and 14 of its socket together using a small wire link. This supplies the ransmitter moxule with a per= manent high level so that a continturs $418 \mathrm{MH} z$ canier wave is transmitted.

Switeh on the receiver module and conneet a multimeter measuring d.c. volts on a suitable range between ground and test point TPI. With the transmitter module switched off. a reading of around 2.6 V should be obsained.

Separate the transmitter and receiver modules by a couple of merres and apply power to the transmiter. The meter reading should increase to between 2.85 V and 3.15 V .

Slowly adjust variable capacitor C4 on the transmitter using a non-ferrous tuning tool until the reading is maximised at around 3.15 V . This may take some time and requires the separation between the units to be adjusted so that this maximum can be achieved. Remove the tuning tool and nove away from the transmitter so that the best reading can be observed.

Note that best results will te achieved by using a clear live of sight between the two units. Once this has been done, switch off the irunsmitter. remove the temporary link and replace the PIC.


Cömpleted Receiver module and Transmitter, with licence exempt label.

This process needs to be completed for each individual transmitter unit and once done, no further adjustments are necessary.

## RADID LINK

Although all the modulation and demodulation is completed by the r.f. modutes and no particular r.f. knowledge is required, the radio link is not analogous to a length of wire linking the two units together!

Sometimes what is received is very different from what is transmitted. This is due 10 interference that occurs either when two transmitters attempt to transmit simulaneously, or due to ather electrical appliances that generate radio frequency interference (r.f.i.) at the same frequency as the system.

All the transmitter units operate on the same r.i. channel. Therefore, if several events occur simultaneously then the channel will contain a mixture of information. An example of this is illustrated Fig. 10.

If possible, the events that are being monitored should be time independent, i.e, not always occurring at the same instant in time. This is sometimes unavoidable and some iransmissions will either occur together or overlap one another. The result of this is that all of the transmissions will be corrupted.

Each transmission takes only 330 ms and events are usually separated by a longer period. i.e. a few seconds or minutes. Therefore. the chance of two independent events occurring at once is remote.

## VALIEITY CHEEKS

In order to delect if the information is corrupted. some method of checking the validity of the received data is required to be included in the data that is transmited. It is important to realise that almost any method of data checking is not perfect and sometimes a corrupted message is interpreted as being correct. It is the software designer's responsibility to ensure that the chance of accepting an incomed message is minimised.

The soltware algorithm in the trarismitter modules encodes the digitised sensor information into discrete data packets that are sent across the r.f. link. Each data byte consists of ten data bits, as ilfustrated in Fig. 11.

The high level Start Bit serves to let the receiver know that a data byte is on its way. and to prepare to receive the folfowing cight bits that constitute the actual byte. A low level Stop Bit ensures that a shon time exists between sequential data bytes and follows the eight data bits to complete each byte.

Each packet consists of nine data bytes as can be seen in Fig.12. Full descriptions of each byte are given in Table 1.

The Transmil Sync Code is used to stabilise the receiver moditle and allows it to lock to the correct frequency. It also serves to indicate to the receiver software that a valid iransmitter module is:


Fig. 10. Example of data corruption in r.i. channel.


Fig.11. R.F. data byte details.
Table 1. Data Packet Description

| Byte Number | Typical Value (Hex) | Description |
| :---: | :---: | :---: |
| 1 | 55 | Synchronisation Byte - Tells the receiver that a transmitter has started a packet and that it should wait for the rest of the packet |
| 2 | Any | Identification Byte - Allows the PC to identify which unit has made the transmission |
| 3 | 200 10 FF | TX Number - The number of transmissions that the unit hes made ( 00 follows FF). This allows the PC to determine if any fransmissions have been missed due to interference |
| 4 | Any | Data 1 - Sensor level data byte 1 |
| 5 | Any | Data 2 - Sensor level data byte 2. (Not used) |
| 6 | Any | Data 3 - Sensor level data byte 3. (Nol used) |
| 7 | Any | Data 4-Sensor level data byte 4. (Nol used) |
| 8 | Any | Data 5-Sensor level data byte 5. (Noi used) |
| 9 | 00 to OF | Cyclic Redundancy nibble - Used to detect if any errors have occurred in the radio link due to interference. |

producing the signal and not another device using the 418 MHz frequency.

The Idenification byte enables the PC to identify which transmitter has produced the data. The Transmit Number allows the analysis of the number of any missed data packets by the PC. The Data bytes represent the actual digitised data received by the transmitter PIC. The software only uses the first data byte but. if required. the remaining four bytes can easily be implemented by changing the PIC software. The cyelic redundancy clieck (CRC) nibble is mainly the method used to determine the validity of the data.

## cYCLIC REDUNDANCY CHECK

The CRC nibble (half byte) is reasonably unique to each data packet and is found by performing a complex polynomial computition upon the whole data packe: (excluding itself) as it is transmitted. It is essentially the remainder of a long division of the data stream by a binary polynomial.

The receiver performs exactly the same calculation on the received data stream excluding the CRC nibble and produces its own second CRC nibble. If any corruption has occurred, the second CBC will be


Fig.12. Breakdown iniormation of the r.f. data packet transmission.
different to the first. Therefore, a comparison of these two nibbles determines the validity of the received data.

The CRC nibble consists of four bits and has sixteen possible values. The rest of the data packet is essentially an sobit binary word that has a slaggering $2 \mathrm{~s}_{0}$ $\left(1: 208 \times 10^{24}\right)$ values. Each of these values will correspond to one of the sixteen CRC valucs, i.e. each CRC value corresponds to $7.55 \times 10^{22}$ data values.

Distribution of these values is almosef completely random amongst the $2^{311}$ possible combinations and, therefore, the chance that corrupted data produces the same CRC as the correct data stream is very slim! In fact. with the author's units, a compted data message has never been interpreted as correct!

You will next be asked if vou want to load the previous calibration setings. If this is the first time that you are using the software, then there are not any calibration settings stored and you must click on No.
Following this, the View options window is displayed. This allows you to select the type of time recording that you require. Full Time means that the time is recorded as the time that each message is received in 2.4hour format. Similarly. Seconds Since Stan or Minutes Since Start allow different types of recording depending on the frequency of messages that are anticipated.
Audible Alerts for Messages means that the PC will beep every time a message is received and is useful for testing the range of the transmiters. Clicking Done will present the main window


Fig. 13 (left). Data capture application screen shot.
sape staius. If a packet is received correctly. the last column of the table shows OK. If it is not received correctly the column shows ERR.

Statistics are calculated for the number of messages received correctly and incorrectly and a calculated current Packet Error Rate is determined. Messages that have leen missed are counted and a current total is shown. The packet error rate total does not take into account any missed packets.

These statistics give an indication as to the performance of the system at any point in time and allow adjustruents of the transmitters for better reception.

When you exit from the program using the File Exit nent, you will be asked if you want to save your calibration settings. This means that you can re-start the program again without having to entes in all the settings for the sensors each time. If the settings are not wanted. then click No to exit.

The graph in Fig. 15 shows some sample results that have been collected from a

The receiver does not compare the two CRC values. but simply appends the second CRC nibble to the received datat. This forms a ten-byte packet that is transmited across the RS232 serial link. The raw RS232 information can be observed by using a terminal package with a hexadecimal display capability: Such a window is included in the systemis software for diagnostic purposes s $_{t}$

## SOFTWARE

When the hardware has been buil and tested. the information that is received can be analysed.

The software writien for this purpose is available free as detailed later. It provides a simple interface between the received data and the user's preferred spreadsheet or database application. If the spreadsheet or dutabase that is used las the ability to collect data directly from a serial port. then this is an alternative route that can also be used for unalysis.

Running the sofiware is simple install the package onto the PC's hard-drive (see later). stan the application and at dialogue box will be displayed asking you to identify which serial port the receiver is connected 10. Type in the COM port number in the box and click the mouse on Test.

The program will then test the port and ask you to switch on the receiver unit. Once the receiver's boot message has bean checked, the Proceed button will be enabled and once clicked on. the main window will be displayed.

Fig. 14
(right) Screen shot for sensor calibration.


Where the messages are displayed. A ippical screen shot is shown in Fig. 13.
Switch on the transmitters one at a time and force a message to be transmitted by changing the condition that the sensor is used to monitor, e.g. manually induce a change in the temperature of the temperature sensor.
When a message is received from a new transmiter, the calibration window is displayed. A Help window is available from here that will help you enter the correet calibration settings for each transmitter and ensure that the data is logged in the corred manner. See Fig. 14.
The Expont Options window from the Options menu allows you to sef-up the nethod that is used to export the data to a file that is subsequently used by your spreadsheet or database. This allows fully autonutic exporting of data to a file every time a certain number of messages are received, or manual exporting whenever required.
The other menus available allow the message window to be cleared, the calibration settings to be deleted. the View Options window to be displayed again or hexadecimal data to be shown.
In Fig.13, the calibrated information can be seen alongside the date, time and mes-
temperature sensor fransmiter unit. They correspond to the temperature of fruit juice placed in the fridge to cool down, then removed and ice added. The dots on the graph indicate the times that messages were received. i.e. when the temperature changed and a transmission occurred.

The tilt switel transmitter unit-is probably most useful in testing the range of operation of the units. The advantage of the switch is that it can easily be moved and its orientation changed such that the environment within which the system is used can be explored for range and suitability.

A typical applicalion is shown in the graph of Fig.16, where the till 'switch is used within a vehicle to monitor the opening and closing of the boot hatch. This is combined with information recorded from another sensor that is being used to monitor pulses from the odometer in order to measure distance travelled.

A laptop PC in the boot of the vehicle was used to record all the received information. This information can be used, for example. to ensure that a secure load carried in the boot of a vehicle is only accessed at particular points along a specifiv joumey.

## OPERATING RANGE

If the range of operation of the fransmitter units is not acceptable for the panticular application required, several methods could be implemented to increase the pliysical separation between the transmitter and receiver units. Two possible methods exist: replacing the radio modules with more sophisticaled f.m. types and. secondly, increasing the power that is supplied to the existing a:m iransmitter nodule.


Fig. 15. Sample graph of temperatures obtained from a fruit juice drink placed in a iridge.

## BOOSTING A.M. POWER

An easy method of incresising thie range of operation with a.ni. is simply to provide the transmitter with a higher operating voltage and hence allow more current to be consumed by it. The PIC has a maximum operating voltage of 6 V and. therefore. without any extra interfacing hetween the PIC and iransmilter. this provides an upper limit on the voltage allowed. In order to limit the current that is consumed by the transmitter when running from 6 V . the value of resistor R5 heeds to be adjusted according to Table 2.

Table 2: Resistor 25 values according to $V_{\text {ec valtage. }}$

| $V_{\text {cc }}$ Min | $V_{\text {cc }}$ Max | R5 Value (Ohms) |
| :---: | :---: | :---: |
| 2.5 | 3.5 | 100 |
| 2.8 | 40 | 220 |
| 3.2 | 1.5 | 330 |
| 3.6 | 5.2 | 470 |
| 4.2 | 6.1 | 680 |
| 5.2 | 7.6 | 1 h |

Performance can be firther improved by the use of a mains adaptor power supply that can provide a regulated and smoothed 5 V d.c. output. This provides the transmitter with a good ground connection that cannot be achieved with the use of a float ing power supply such as a batlery.

## SWITEHING TO F.M.

Frequency Modulation (f.m.) is considerably more reliable than the older technology. Amplitude Modulation (a.m.). This can be casily observed by listening to an a.m. radio station in a car and traveling around an urban area or under a bridge. The signal strength received is subject to variation due to buildines, hills, etc.

With fini.. however. these effects are reduced and a strong signal is either received or not received. and is reasonably independent of direction. This is true in the case suggested here also. An f.m. system. although not tested, would provide a signiticant improvement in performance and range of operation, but at an increased cost.

Sivitching to f.m. involves signiticantly more effort than increasing the power of the a.m. modules. It also consumes more power than a.m. and is not as well suited to battery operation. Consequently. f.m. is reconsmended only for use where greater ranges are required. Manufacturers" data sheets for f.m. modules suggest that a range of up to

Fig. 15. Graph details from a boot sen sor and a distance travelled sensor.
200 metres free space and 30 metres worst case are possible.

## F.M. RECEIVEA

A modified schematic for the f.m. receiver unit is shown in Fig.17. a suitable adaptor p.c.b. is shown in Fig.18. This board is available from the EPE PCB


Fig.17. Receiver modification details for f.m. operation


Completed a.m. receiver board. The f.m: adaptor p.c.b replaces the a:m. module.



Two completed Transmitters. One with a remote thermistor sensor (right-hand corner), and the other showing the required "licence exempt" label.

Fig. 18. F.M. Receiver adaptor printed circuit board details.

Service. code 220 attogether with the main p.c.b.

The adaptor board allows direct replacemant of the a.m. module with the f.m. type. This board should be mounted above the existing p.c.b. and connection made via pins from the aim. receiver holes through to the matching holes in the adaptor.

The f.m. receiver should then be soldered into the adapior board and a $10 \mathrm{\Omega} \Omega$ resistor soldered into the holes for R7. The antennit should be connected directly to the adaptor board and brought out through the case as normal. The f.m. module uses a more conventional quarter-wavelength short whip antterna of 170 mm length.

## F.M. TRANSMITTER

The modified E.m. transnitter schematic is shown in Fig. 19. Capachor C? needs to be removed and R5 replaced with a wire link or zero ohm resisior. Components C4. 1.1 and $I C$ ? also need to be removed.

The transmission adaptor p.c.b. is shown in Fig.20. This board is also available from the EPE PCB Service, code 119 a.

The board should be connected to the main p.c.b. via shon flying leads. DATA IN and GND connections are taken from the existing holes for IC2. The power should be taken directly from the battery or other convenient location on the transmitter p.c.b. The antenna must be 170 mm long rather than the 90 mm type described previously.

The adaptor board must be located is close to the transmitter board as possible, in order to reduce conducted interference along the ground connection.

The 3.6 V supply should be replaced by a 6 V type in order for the unit to operate correctly. This must not be exceeded or reduced. as it is the upper limit for the PIC and lower limit for the transmitter module:

It should be noted that MPTI340 is independent of the type of modulation used. That is to say, all the antenna constraints
detailed previously for the a.m. system still apply and must be conformed with.
More details of the f.m. radio modules used are available from the


Fig. 19. Transmitter modificatiōn details for f.m. operation.


Fig.20. Full size F.M. Transmitter adaptor board details.
manufacturing companys web sire at: http://www.radiometrix.co.uk/products/ product l.htm.

## SDFTWARE INSTALLATION

Installation of the application software is the same as for any other Windows based application. Before installation. please ensure that all applications are closed. this includes the Microsoft Office toolbar that is easily forgotten! Then, simply double click on the file named SETUP.EXE and follow the on-screen instructions.

The default installation directory is C: EPE This can be changed to install the software on any other drive or directory. Once this is completed. a Program Manager icon called Data Capture is created. The application is executed by double clicking on this icon either from the Program Manager in Windows 3.1 or the Start menu in Windows 95 or later.

If any problems are experienced during installation such as "Version Conflici" or "File in use, cannot replace" then this generally means that a later version of the same file is already installed and does not need to be replaced. If this is the case, select the Ignore or Skip butions.

## AESOUFCES

Software for the Wirêless Monitoring System is availahle on a $3 \cdot 5$-inch PC-compatible disk from the EPE PCB Service (for details see that page in this issue - the soltware is free but there is nominal post and handling charge for the disk). It is also available for free downloud from our web site: fip://ftp.epemag.wimborne:co.nk/pub/ PICS/wireless.

Pre-programmed PICl6C7Is are avaitable. see this month's Shoptalk page. Information on ohtaining other "special" components for this design are also given on this same page.

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## ALAN WINGTANLEE

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A second difference is that the online edition is tailored to an intemational audience rather than being UK-centric. Hence there is litte advertising within EPE Online. although the inclusion of advertisements has not been ruled out for the future. None-the-less, the subscription price for EPE Online is ligghly competitive - less utan US $\$ 2$ per issue. Whichever option you choose, we hope you'll agree that with the advent of the Intemet, this is now a very exciting time to be involved in electronics.
In coming months. I will be discussing more finternet services and applications which may help EPE readers to get more out of the Intene. For example. I'll be examining what is probably the world's most popular software download at the moment - ICQ I will also try to offer more advice and pointers for novice intemet users.

As usual. 1 have placed some ready-made links for you to elick on, in the Net Work page of the EPE web site. I welcome sugges: tions by E-mail to alan@epemag.demon.co.uk. My Home Page is http:/homepages.tep.co.uk/~alanwin.
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