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HP3081A Industrita workstation clw Bercode swipe reade HP8264 Rack mount variable 0-20V O 20A metered PSU HP54121A DC to 22 GHz four channel test set
HP7500A A1 8 pen HPGL high speed MP7580A A1 8 pen HPGL high speed drum plotte
EG $+G$ Brookdeel 95035 C Precision lock in amp Viow Eng. Mod 1200 computerised inspection system Uing Dynamics 2 kW programmabie vibration test system Computer controlled $1056 \times 560 \mathrm{~mm} \times$ Y table $\&$ controlle Kerthloy 590 CV capacitor I voltage analyser Recal ICR40 dual.40 channel voice recorder system Figt R5030uv34 Mann Tally MTG45 Hioh speed line printer Inter SBC 486/1335E Multibus 486 system Zota 3220-05 AO 4 pen HPGL fast drum plotters Nikon HFX-It (Ephiphot) exposure control uni Motorole VIME Bus Boards \& Components List. SAE / C Fulteu M3041月 600 LPM band primer

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Fulteu M3041A 600 LPM band primer
Fulteu M3041D 600 LPM printer with
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\begin{aligned}
& \text { Fufitti M30410 } 600 \text { LPM printer with network interiace } \\
& \text { Perkin Elmer 2900 Infrared spectrophotoneter } \\
& \text { VG Electronics } 1025 \text { TFI FTEXT Darnding Marnin Mete }
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Surnic SD 150H 18 In Satelimo Dish + mount (For Vovager Sekonic SD 150H 18 channel digital Hybrid chart
TAYLOR HOBSON Tallysurf amplifier / recorder Syitem VIdeo 1152 PAL wavelorm monitor Tyent Leb - 2 mtr square quietised acoustic

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$\mathrm{LL}=\frac{50}{157.0796}=.3183099=318.3099 \mathrm{~mA}$
$1=\sqrt{.5+(1.570796 \cdot 3183099)^{5}}=1.3486=1.3486 \mathrm{~A}$
$\theta=\tan ^{-1} \frac{1.570796 \cdot .3183099}{5}=68.2378^{\circ}$
$100 \times 1570796 \times 3183099$
$Z=\sqrt{157.0798^{2} \times 3183099+100 \times 1157.0796 \cdot 31.83099 \mathrm{R}}=37.0755 \mathrm{R}$

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Triggers only when sounds are detected. Very low standby current. Variable sensitivity and delay with LED indicator. Size $20 \mathrm{~mm} \times 67 \mathrm{~mm}$. 9 V operation. 1000 m range. $\mathbf{£ 1 9 . 4 5}$ HVX400 Mains Powered Room Transmitter
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Scrambled output from this transmitter cannot be monitored without the SCDM decoder connected to the receiver. Size $20 \mathrm{~mm} \times 67 \mathrm{~mm}$. 9 V operation. 1000 m range...... $£ 22.95$ SCLX Subcarrier Telephone Transmitter
Connects to telephone line anywhere, requires no batteries. Output scrambled so requires SCDM connected to receiver. Size $32 \mathrm{~mm} \times 37 \mathrm{~mm}$. 1000 m range......... $£ 23.95$ SCDM Subcarrier Decoder Unit for SCRX
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Remote control anything around your home or garden, outside lights, alarms, paging system etc. System consists of a small VHF transmitter with digital encoder and receiver unit with decoder and relay output, momentary or alternate, 8 -way di.i.l. switches on both boards set your own unique security code. TX size $45 \mathrm{~mm} \times 45 \mathrm{~mm}$. RX size $35 \mathrm{~mm} \times 90 \mathrm{~mm}$. Both 9 V operation. Range up to 200 m .
Complete System (2 kits). $\qquad$ .. $£ 50.95$
Individual Transmitter DLTX. ع19.95
individual Receiver DLRX................................
MBX. 1 Mi-Fi Micro Broadcaster
Not technically a surveillance device but a great idea! Connects to the headphone output of your HI-FI, tape or CD and transmits Hi-FI quality to a nearby radio. Listen to your farvourite music anywhere around the house, garden, in the bath or in the garage and you don't have to put up with the DJ's choice and boring waftle.
Size $27 \mathrm{~mm} \times 60 \mathrm{~mm}$. $9 V$ operation. 250 m range.

UTLX Ultra-miniature Telephone Transmitter
Smallest telephone transmitter kit available. Incredible size of $10 \mathrm{~mm} \times 20 \mathrm{~mm}$ ! Connects to line (anywhere) and swiches on and off with phone use
All conversation transmitted. Powered from line. 500 m range.
TLX 700 Micro-miniature Telephone Transmitter
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High performance transmitter with buffered output stage providing excelient stability and pefformance Connects to line (anywhere) and switches on and off with phone use. All conversations transmitted. Powered from line. Size $22 \mathrm{~mm} \times 22 \mathrm{~mm}$. 1500 m range
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LED and piezo bleeper pulse slowly, rate of pulse and pitch of tone increase as you approach signal. Gain control allows pinpointing of source. Size $45 \mathrm{~mm} \times 54 \mathrm{~mm}$. 9 V operation.
CD600 Professional Bug Detector/Locator
Multicolour readout of signal strength with variable pate bleeper and variable sensitivity used to detect and locate hidden transmitters. Swith to AUDIO CONFORM mode to distinguish between localised bug transmission and normal legitimate signals such as pagers, cellular, taxis etc. Size $70 \mathrm{~mm} \times 100 \mathrm{~mm}$. 9V operation.
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## QTX180 Crystal Controlled Room Transmitter

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QLX180 Crystal Controlled Telephone Transmitter
As per QTX180 but connects to telephone line to monitor both sides of conversations. $20 \mathrm{~mm} \times 67 \mathrm{~mm} .9 \mathrm{~V}$ operation. 1000 m range.
$\{40.95$
QSX180 Line Powered Crystal Controlled Phone Transmitter
As per QLX180 but draws power requirements from line. No batteries required. Size $32 \mathrm{~mm} \times 37 \mathrm{~mm}$. Range 500 m ..
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Ref: D95.
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ROTARY SWITCH, 9-pole 6-way, small size and 's spindle, pack of two, Ref: 054
FERRITE ROOS, $7{ }^{7 \prime}$ with coils for Long and Medium waves, pack of two. Ref: D52.
DITTO but without the coils, pack of itree, Rel: D52.
MAINS DP ROTARY SWITCH with ' \& control spundie. pack ol five. Ret: D49.
ELECTROLYTIC CAP, $800 \mu \mathrm{~F}$ at 6.4 V . pack of 20, Ref: D48.
ELECTROLYTIC CAP, $1000 \mu \mathrm{~F}+100 \mu \mathrm{~F} 12 \mathrm{~V}$. pack of 10. Ret: D47.

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VENNER 75A TIME SWITCH. This is a top class instrument, costs probably around $\mathbf{£ 6 0}$ new. Ex-electricity board but taken out of service because they changed to solar control. These have 2 on/off per 24 hours, price 88 each, Order Ref: 8P66.
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## COMPUTING CONUNDRUM

The Colossus Recreated article by Barry Fox in this issue raises some interesting points about the future of computers. We naturally tend to feel that modern PCs are way ahead of anything built more than fifty years ago using valves, so why can the world's first electronic computer still outperform a Pentium PC? It all comes down to single task computing.
If you build a computer to perform a single task - like cracking a secret code - then you can make it do that task very efficiently and therefore very quickly. If you build a multi-tasking machine and then write volumes of software to give it the most user friendly interface possible for all those tasks, then you need masses of computing power, speed and memory to achieve a result and, of course, you slow down the process with overblown software.
PCs running Windows are not the most efficient answer to most tasks and if you use one professionally for a single task you will soon realise that Windows is an unnecessary inconvenience. It may be that Colossus will change the future of computing in a number of areas.
Isn't it fascinating how much we can learn from the past. and isn't it rather worrying how we quickly assume that what we have recently developed is best? It's nice to have the latest, fastest machine and we regularly come across people who look down their noses at less fortunate. or simply less interested, individuals, but surely there is much to be said for the minimalist approach. Why use a computer when it is not necessary, and why use a 200 MHz Pentium PC with a Gigabyte of hard disk and massive RAM just to maintain a simple database, do a few calculations or for wordprocessing? The word "bloat" is used by Barry Fox and it is easy to see why.

## BLOAT

We are probably as guilty of "bloat" as anyone else. You could say that our PIC-olo Music Maker uses unnecessary technology to produce a few notes - PIC chips are not in fact well suited to music making - but it is an excellent and instructive exercise in programming and the resultant instrument (if that is not too grand a word for it) is very simple to build and great fun. Besides, to keep everyone happy, we have a simple organ design based on an RC oscillator coming in a few months.


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# VARIABLE BENCH POWER SUPPLY 

## ROBERT PENFOLD

## A low-cost, regulated $1 \cdot 2 V$ to 12V supply with four switched current ranges from 20mA to $500 m$.

Aregulated mains power supply unit is undoubtedly one of the most useful pieces of equipment for the electronics experimenter. Ideally the supply should have a wide output voltage range, and be capable of providing high currents, but a high specification is invariably accompanied by a high price.

Fortunately, for most purposes a supply unit having a modest specification will suffice. Most projects require a supply of between about 3 V and 12 V , at output currents that are usually no more than a few hundred milliamps.

The mains power supply unit featured here has an output voltage range of 1.2 V to 12 V d.c., and it can supply output currents of up to 500 mA . Despite the simplicity of the circuit, it provides excelIent regulation. In fact, the output potential varies by no more than a few millivolts when the output current is taken from zero to full load.
A high quality reference voltage generator ensures that the circuit is free from

somewhat at higher voltages with output currents of more than about 400 mA .
Current limiting at an output current of 500 mA protects the circuit against the inevitable short circuits and overloads on the output. As 500 milliamps is a fairly high current that is capable of damaging many semiconductors, additional limit currents of 20 mA .51 mA , and 200 mA are available.

An optional voltage meter enables the output potential to be set with reasonable precision, but a significant saving in cost can be made if the voltmeter is omitted and the output voltage is set with the aid of a multimeter.

## SAFE OPERATION

Normally a project of this type would not he suitable for beginners, as it would involve wiring carrying the dangerous mains supply. This power supply unit is perfectly safe for heginners as it uses a ready-made 12 V "battery eliminator" as
the power source. There is no mains wiring to contend with. and the maximun voltage in the circuit is a safe 20 V or thereabouts.

Also. the double insulation in the "eliminator" power source ensures that the unit is well isolated from the mains supply.

It aditionally means that the unit has "floating" outputs i.e. neither output socket is earthed. This can be helpful in avoiding short circuits and "hum" loops when using the unit with equipment that has an earthed chassis.

## SYSTEM OPERATION

There are two standard approaches to providing a variable voltage supply. and one of these is to use a potentiometer to provide a variable voltage source from a fixed regulator circuit. A buffer amplifier is then used to effectively boost the maximum output current from the potentiometer to a suitable level.

An alternative is to use a voltage regulator to provide a tixed voltage, plus an amplifier which multiplies the output voltage to the required figure, and also boosts the maxinum output current to the required level. This second method generally offers better voltage regulation. and is the one adopted in this design.

The final circuit uses the arrangement shown in the block diagram of Fig. 1. It is based on an operational amplifier (op.amp) which is used in the non-inverting mode. An accurate and highly stable reference voltage is supplied to the non-inverting $(+$ ) input of the amplifier. The choice of reference voltage is important. since the minimum output voltage cannot be less than the reference potential. In this case a 1.2 V reference source is used. and this enables the unit to be used in place of a single "dry" cell or nickel-cadmium battery.

## OUTPUT CURRENT

Maximum output current from a normal operational amplifier is only a few milliamps, or possibly a few tens of milliamps. This is insufficient for a bench power supply unit, and a buffer stage at the output of the amplifier is therefore used to boost the maximum output current.


Fig. 1. Block diagram for the Variable Bench Power Supply.

A severe overload on the output could cause a very large current flow. which could damage the buffer amplifier and the power source before a conventional fuse would "blow." So, some form of electronic "fuse" has to be regarded as mandatory in a circuit of this type.

A low value resistor in series with the output of the circuit enables a current limiting circuit to detect an excessive current flow. The voltage developed across this resistor is proportional to the output current.

Provided the output current does not exceed about 500 mA , the voltage across the current sensing circuit will not be sufficient to activate the amplifier and electronic switch. The circuit then works normally. providing a well regulated output voltage.

If the output current exceeds much more than about 500 mA , the voltage across the current sensing resistor exceeds about 0.6 V . The "electronic" switch then starts to turn on. and it connects the output of the op.amp through to the final output.
On the face of it. this will not have much effect, but the crucial factor here is that there is a voltage drop of more than one volt through the buffer amplitier. The switch therefore tends to pull the output of the op.amp lower in voltage, which in turn reduces the final output voltage. The switch then pulis the output of the op.amp to an even lower voltage.
Any attempt to draw more than the limit current simply results in the output voltage rapidly diminishing. with no significant increase in the output current. Even with a short circuit across the output terminals, the circuit will provide an output current of only about 600 milliamps or so. This form of current limiting responds almost instantly to any output overloads, and is fast enough to ensure that the supply's output stage and the power source are not damaged.

## FEEDBACK

The closed loop voltage gain of a noninverting amplitier is controlled by a negative feedback network connected between the output and the inverting ( - ) input. In this circuit the feedback network is a potentiometer (VR).

An operational amplifier amplifies the voltage difference across its inputs, and its innate (open loop) voltage gain is extremely high at d.c. and low frequencies. In fact, it is typically in excess of 100,000 .

Taking the non-inverting input positive of the inverting input sends the output to a
higher potential. Taking the inverting input to the higher voltage has the opposite effect. with the output going to a lower voltage. In the non-inverting mode the input potentials are balanced by a negative feedback action from the output to the inverting input.
With the potentiometer's wiper ( $w$ ) at the top end of the track the inverting input is connected direct to the output of the supply. If the output should go above 1.2 V , the inverting input will also be taken above this level, causing the output potential to be reduced.

If the output potential is pulled below 1.2 V . this will again unbalance the input levels to the op.amp, this time causing the output to increase in voltage to correct matters. Note that the feedback is taken via the buffer amplitier and the current sense resistor, so that it will compensate for any increased voltage drop through these when the output loading is increased.

If the potentiometer's wiper is taken down its track, the negative feedback action still balances the input voltages to the op.amp. However, the voltage drop through the potentiometer means that the output settles at a higher voltage.

For example. suppose that the wiper is at the mid-point of the track, it will be at half the output voltage, which means that an output voltage of 2.4 V is needed in order to deliver 1.2 V to the inverting input of the amplifier.

The further down the track the wiper is taken. the greater the voltage drop through the potentiometer, and the higher the output voltage. The potentiometer therefore acts as the output voltage control.

## CIRCUIT OPERATION

The full circuit diagram for the simple Variable Bench Power Supply appears in Fig. 2. The unregulated input voltage supply connects to socket SKl, and the actual voltage here is nearly 20 V with no load connected at the output of the regulator circuit. With an output current of 500 mA the input voltage is still almost 16 V .

There are voltage drops through the regulator and current limiting circuits. but the circuit can just about maintain an output potential of 12 V at 500 mA . Fuse FSI protects the battery eliminator if a serious fault should occur in the regulator circuit. Cl is the supply decoupling capacitor.

The regulator diode DI provides the 1.2 V reference source. This is used much like a Zener diode. but it has a much higher level of performance than a Zener diode. It will operate efficiently with currents from $50 \mu \mathrm{~A}$ to 5 mA , and in this case resistor RI sets the current flow at about $50(0) \mu \mathrm{A}$. Capacitor C2 filters any noise generated by DI.

Operational amplifier ICI is a type that will operate properly with its inputs and output at low voltages. Most other op.amps will not operate properly in this circuit.

A Darlington power device TR1 operates here as an emitter follower buffer stage at the output of IC1. The very high current gain of TRI ensures that the circuit can easily accommodate output currents of up to 500 mA .

Current limiting is controlled by transistor TR2, and R3 to R6 are the four current sensing resistors. These bring the current limiting into action at approximate threshold currents of $20 \mathrm{~mA}, 50 \mathrm{~mA}$. 200 mA , and 500 mA respectively.

Note that the short circuit output currents are somewhat higher than these figures. particularly at the lower limit


Fig.2. Complete circuit diagram for the Variable Bench Power Supply.


Fig.3. Printed circuit board component layout and interwiring to offboard components, plus the full size
 underside copper foil track master pattern.

## COMPONENTS

| Resistors |  | See |
| :---: | :---: | :---: |
| R1 | 33k | (3n) |
| R2 | 2k2 |  |
| R3 | $33 \Omega$ | TALK |
| R4 | $12 \Omega$ | Page |
| R5 | $3 \Omega 3$ |  |
| R6 | 1 L 2 |  |
| R7 | 100k |  |
| R8 | 200k |  |

## Potentiometer

VR1 $22 k$ rotary carbon, lin

## Capacitors

$\begin{array}{ll}\text { C1 } & 10 \mu \text { radial elect. } 25 \mathrm{~V} \\ \text { C2 } & 47 \mu \text { radial elect. } 16 \mathrm{~V}\end{array}$
C3 $4 \mu 7$ radial elect. 50 V

## Semiconductors

D1 ICL8069 1.2V precision
IC1 CA3140E PMOS op.amp
TR1 TIP121 or TIP122 npn power Darlington transistor
TR2 BC547 non silicon transistor
Miscellaneous
FS1 500 mA 20 mm "quick-blow" fuse, with chassis mounting fuseholder
ME1 $\quad 100 \mu \mathrm{~A}$ moving coil panel meter
SK1 $\quad 3.5 \mathrm{~mm}$ mono jack socket
SK2 4 mm socket, red
SK3 4 mm socket, black
S1 3-pole 4-way rotary switch, make-before-break
S2 s.p.d.t. min toggle switch Multiple printed circuit board available from the EPE PCB Service, code 932; metal instrument case or box, size to suit - see text; 12 V 750 mA or 800 mA unregulated mains (adaptor) power supply; control knob (2 oft); JO220 insulating kit; 8 -pin d.i.l. holder; nuts and bolts; output leads, wire, solder, etc
currents where the output current from ICl becomes a significant factor.

## VOLTAGE CONTROL

Potentiometer VR1 is the output voltage control. Resistor R2 limits the maximum output voltage to a little over 13 V . but remember that currents of up to 500 mA can only be maintained at output potentials of about 12 V or less. Meter ME1 is the optional voltmeter.
With resistor R7 selected. using toggle switch S 2 , the full scale voltage is 10 V . but this is increased to 20 V with R8 selected This second range is needed to accommodate output potentials of 10 V to 13 V .
Capacitor C3 is needed in order to ensure good stability at high output currents. Without this component the circuit has a tendency to oscillate.

## CONSTRUCTION

This project, like last month's $I R$ Repeater circuit. is constructed using the

EPE multi purpose printed circuit board (p.c.b.). but a fair percentage of the small components are not mounted on the circuit board. This p.c.b. is available from the EPE PCB Service, code 932.
The component layout for the printed circuit board. together with the hard wiring and copper track pattern is shown actual size in Fig. 3. Start construction by fitting the appropriate components onto the p.c.b. in the usual size order, working up to largest.
The CA3140E used for IC1 has a PMOS input stage, and the normal anti-static handling precautions must be observed when dealing with this component. In particular. it must be fitted in a holder, but it should not be fitted in place until the board and all the wiring have been completed. When fitting IC1 try to touch its pins as little as possible. and be careful to fit it the right way round.

The leads of transistor TR2 need a small amount of manipulation before they will fit

this layout. but TR2 should then drop into place quite easily. Reference diode DI has a standard TO92 transistor style encapsulation. but it only has two leadout wires.

At this stage only fit single-sided solder pins at the points where fuse FSI, rotary switch SI. etc. will connect to the board.

Before mounting the p.c.b. in its case, remember that less than 50 per cent of the copper pads on the board are used. This makes it essential that the board be doublechecked for any component positioning errors.

## DFF THE BOARD

The completed circuit board is mounted in the metal case using plastic stand-offs or 6BA bolts. If bolts are used, spacers about 6 mm long must be used to keep the sol dered joints on the underside of the board well clear of the metal case.

Darlington transistor TRI is mounted on the base panel of the case, which must be a metal type so that it acts as an efficient heatsink.

A comparatively small case will suffice if the voltmeter is omitted, but a much larger case will be needed if the meter is included.

This is due to the increased front panel area required for rotary switch S2 and the meter. Make sure you select a case that has a large enough front panel to comfortably accommodate everything, even if this means choosing one that is substantially over-size in other respects.

Note that TR1's heat-tab connects internally to its collector (c) terminal, and it must he insulated from the case using a standard TO220 insulating kit. This kit consists of a mica (or plastic) washer which insulates TRI from the case. plus a plastic bush. The latter fits over and inside the metal heat-tab of TR1, so that it is insulated from the 6 BA mounting bolt.

This arrangement ensures that there is no direct connection between TRI and the case, or an indirect connection via the mounting bolt. Fig. 4 shows how everything fits together. It is advisable to use a continuity checker to ensure that TRI's


Fig.4. Isolating TR1 from the case.

heat-tab is reliably insulated from the case.

## MAINS ADAPTOR

The battery eliminator used with the prototype has no less than five different types of plug on its output lead. Obviously, the power input socket SKI must be a type that matches one of the plugs on the power source, and a 3.5 mm jack plug is probably the best choice where this option is available.

The wiring for a socket of this type is shown in Fig. 3. and the battery eliminator set so that the tip of the plug is positive. Socket SKI is mounted on the rear panel of the case. Note that the battery eliminator must be a non-regulated 12 V d.c. type, and that no other type is suitable.

## CASE DETAILS

Fuse FSI is fitted in a chassis mounting fuseholder mounted at any convenient place on the base panel of the case. The front panel layout is not critical from the electronic viewpoint, but try to have the meter and voltage setting potentiometer VRI reasonably well separated so that the meter is not obscured when VRI is adjusted.

Most panel mounting meters require a large circular cutout plus four small mounting holes. The normal diameter for the large cutout is 38 mm , but check this point before cutting the hole.


Probably the easiest way of making the cutout is using a brace plus a device known as a "'tank cutter", or simply as a "hole cutter." Alternatively, use a coping saw, miniature round file. "Abrafile". or any similar tool. Whatever method is used, it is advisable to make the hole slightly undersize initially, and then enlarge it to precisely the correct size using a half round file.

The meter itself can then act as a improvised template to aid accurate positioning of the four small mounting holes. These holes should have a diameter of about 3.2 mm . and are for the threaded rods that are built into the rear of the meter. The meter should be supplied complete with four fixing nuts and washers.

## FINAL ASSEMBLY

With everything now fitted to the case. the unit is completed by adding the hard wiring. This is mainly straightforward, but be careful to get TR1 wired to the board correctly. TRI's leadout wires should be bent upwards slightly to ensure that they are kept well clear of the metal case.

Resistors R3 to R8 are easily mounted on switches S1 and S2, provided the tags of the switches and the ends of the leadout wires are both tinned with solder prior to making the connections.

## IN USE

Most 12 V battery eliminators also offer a range of lower output voltages, so make sure that the voltage selector switch is at the correct setting. Also double-check that the polarity switch has the correct setting (the tip of the plug being positive).

With the power source connected to input socket SKI and plugged in at the mains. it should be possible to vary the output voltage over a range of 1.2 V to at least 12 V . If not, unplug the battery eliminator from the mains supply immediately, and recheck the wiring.

It is advisable to check that the current limiting circuit is functioning correctly. Set rotary switch SI to the 500 mA setting (R6), and adjust Voltage control VRI for an output potential of around 12 V . With a 4.7 ohm 2 W resistor connected across the output sockets (SK2 and SK3) the output voltage should fall to about 3 V .

# New Technology 

# For some time, digital chips and their design uses have been benefitting from new lower operating voltages; now analogue chips are being given similar treatment - lan Poole reports. 

RECENTLY, there has been a lot of information in the technical press about transfer from 5 V to 3 V logic. This change has been mainly brought about by the increase in the number of portable systems being used. as well as the need for higher speeds and lower heat dissipations. Most of the major processors have "road maps". or planned developments which show them increasing the speed of their operation and migrating to the 3 V standard if they have not already done so.
One of the major problems in this migration is that a number of the more specialist i.c. functions are not yet available at the lower voltage. This gives rise to difficulties in using two voltage levels.

Fortunately, in many cases it is possible to drive a 3 V chip from a 5 V one without any damage or operating problems. This has helped the acceptance of the new standard. because, without being able to interface the two standards, very few pieces of low-voltage equipment could have been designed. Now, many new computers and other logic driven circuits use the lower voltage standard.
The "glue" logic used to perform some of the functions outside the chips is also available. A range of 74 low voltage HCMOS chips are fabricated by the main manufacturers, including Texas. National Semiconductor, Philips, etc. However, the range of functions is less than that which was available in the more familiar 5 V series of HCMOS.

The reason for this reduction in choice is that some of the less well used functions are unlikely to be needed in sufficient quantities to make their production viable. Instead, it is more likely that these functions will be included in the VLSI chips or within programmable gate arrays.

## Analogue Chips

Although the main thrust for low voltage development seems to have been focused on the digital side, analogue electronics has not been left behind. Although there may not be as much analogue electronics in items such as personal computers, there is still a vast requirement for analogue circuits in cellular phones, and this sector of the electronics industry is booming as much as any other. Accordingly, there is a need for very low voltage and low current analogue circuits.

One of the most versatile analogue circuits is the operational amplifier. Some of us can remember the introduction of the famous 741. some 30 years ago. Whilst these chips seemed like the ultimate in technology at the time. developments have
moved well on from this. New chips offer much higher speeds as well as lower voltage operation. lower noise, higher input impedance and a host of other advantages.

As with digital chips, there is a major emphasis on trying to reduce the operating voltage of analogue circuits. Just as in the case of the digital circuits. there is a need to reduce the power requirements of equipment to enable battery sources to be used more easily. The fact that heat dissipation is lower has less impact on the design of the chip itself because the levels of integration are lower.

For VLSI digital chips running at 150 MHz , and more, on-chip heat dissipation is a major problem. For analogue chips, where levels of integration are less. this problem is not as acute. Nevertheless, this does not mean to say that. in the future, integration levels will not rise to the extent where it will become a major issue.

## Lower-voltage Op.amps

In response to the needs for lower voltage analogue chips, a number of manufacturers are now introducing new devices. Motorola has recently launched an op.amp which is specified to function with a rail-to-rail voltage of just IV! Previously, the lowest operating voltage for an op.amp was around $1 . X V$. This represents a major reduction.

Achieving the low voltage operation required the use of a number of interesting techniques. Most low voltage op.amps use a bipolar input. For those using CMOS technology, the lowest rail-to-rail operating voltages are around 2.5 V or more.

To achieve the very low operating voltages. Motorola has employed depletion mode f.e.t.s. These are normally on when no bias is applied and enable low voltage operation. However, they have been designed so that they switch to enhancement mode as the voltage increases. This offers advantages in terms of higher transconductance and speed for the same drain currents.

Also included in the design, are $n$-channel MOSFETs fabricated using the SMARTMOS process and vertical pmp and high frequency $n p n$ devices. Further improvements have been added to the fabrication process to enable the required level of performance to be achieved.

Very tight control on the process stages. along with thinner layers of oxide and buried channels, have enabled the noise to be considerably reduced. In fact. it is in the area of the silicon-oxide interfaces where major improvements in noise performance can be made. By refining the process.
noise levels can be reduced by as much as a factor of three.

## Input Concentration

Much of the design effort for the low voltage operation has been placed into the input stages. Here ion implantation techniques are used to create the negative threshold devices which are required for low voltage operation. The way in which these devices operate is quite complicated.

There is a highly doped $p$-well in the structure to allow operation from very low voltages up to the specification limit by changing the mode of operation. Also. the drain area is lightly doped to ensure a sufficient breakdown voltage.

The input circuit itself is configured using high impedance current mirrors to provide high levels of gain. These mirrors must be very well balanced to ensure that the offsets are kept to a minimum.

This was a challenge to the design team because it was necessary to ensure that this parameter was met even down to the lowest supply voltages. It was particularly important at lower voltages because any increase in offset would manifest itself as a larger proportion of the available output swing.

## Boosting Output

Apart from the input stages, development was also required for the output. In view of the very low voltages involved. the normal Darlington contiguration could not be used. Instead, a contiguration with a bass boost amplifier for the $n p n$ side of the output is used. This bass boost gives additional current drive for lower frequencies where a sustained current is required. A complementary circuit is then used for the $p m p$ output device.

The performance of the MC33502. a new op.amp from Motorola, for example. is quite remarkable. Operating between IV and 5 V . the performance changes are relatively small. In addition to this. it boasts a unity gain bandwidth of greater than $4 . X \mathrm{MH}$., as well as a maximum operating voltage of 7.5 V .

This is just one op.amp from one manufacturer. It is likely that other manufacturers will follow the lead taken by Motorola because the industry is fast moving towards the use of much lower rail voltages. Not only will the operating voltages of the future be much lower, but other features will be more easily available, making the compromise which often faces today's analogue designers less of an issue.


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# Constructional Project <br> P/C.OLO MUSIC MAKE <br> <br> JOHN BECKER 

 <br> <br> JOHN BECKER}

# How to make electronic music simply for the fun of it, and have a mini-lesson in PIC programming logic. 

Bournemouth Symphony Orchestra is renowned as source of inspiration, but normally in matters of music. It might, then, come as a surprise to learn that two EPE constructional projects have also been inspired by their playing.

The first was the EPE PIC-A-Tuner of May '97. On numerous occasions the author has watched the timpanist tune his timpani while using an electronic tuning aid, obviously assured of its accuracy. So came the notion that as a professional musician finds tuners useful, then let's design a DIY one - and so it came about!

On another occasion, the melodic talents of the Piccolo player and her instrument took prominence. Suddenly. sparks of association leaped across the author's brain cells - piccolo? PICcolo? . . . PIC-olo! musical instrument designed around a PIC. the microcontroller with which we have become well acquainted.

Here it is, then. PIC-olo Music Maker, another electronic design inspired by the BSO!

## pRELUDE

However, don't expect to build PIColo and hear an electronic simulation of the real piccolo wind instrument (incidentally, piccolo is Italian for small). PIC-olo is electronic in nature and electronic in sound, simply generating thirteen harmonically related notes that are basically square waves with the comers lopped off.

Up to four of these notes can be played simultaneously, that is, chords can be played - it's not a "single-note" instrument as some of its historic predecessors were - remember the Stylophone of the

1970s made famous by Rolf Harris (currently of Animal Hospital fame)? That extraordinary (for its time) little musical instrument had a stylus which had to be touched on metal pads in order to create the notes.

Over the years, there have been several imitations and enhanced versions. Unashamedly, PIC-olo is the latest. But. it takes the original idea a triplet of logical steps forward: it uses a PIC16C84 microcontroller, thirteen pressbutton switches arranged in piano keyboard style ( $C$ to $C l$ including sharps) and it is polyphonic.

PIC-olo is housed in a slim plastic case and can be used with headphones, a small speaker, or plugged into the hi-fi system. Not a stylus in sight! Allowing for inflation, it might also be described as the hitech evolution of the "penny whistle"!

How you obtain the software, and preprogrammed PICs, is detailed later.

## CIRCLIT NOTES

As will be seen from the circuit diagram in Fig.1, there is not much in the way of electronics involved with PIC-olo Music Maker. Basically, just microcontroller IC1. small power op.amp IC2a. and the bank of switches S1 to S13.

The microcontroller detects which switches are pressed and generates square wave outputs of appropriate frequencies on its Port A pins RA0 to RA3. Via resistors R11 to R14 and a.c. coupling capacitor C3. the output frequency signals are mixed by op.amp IC2a, which is capable of supplying an output current of up to 500 mA .

## MUTING

The gain. or rather the alfenuation, of the circuit around IC 2 a is preset by potentiometer VRI. The range of attenuation attainable is between about $\times 1 / 2$ and $\times 1 / 100$.
It may seem unusual to provide attenuation rather than amplification in the output stage, but there are two considerations which require this action.
First, the square waves arriving at IC 2a have an amplitude of 5 V . Since a.c. coupled mixing produces an output level which is the sum of the input amplitudes, the sum of the four input signals could. theoretically, be calculated as 20 V peak to peak.

The op.amp, which is powered at 5 V . cannot handle that amount of swing on its output. Indeed, at this powering voltage, its output will probably have a maximum swing of about $3 \cdot 5 \mathrm{~V}$. Any signal trying to achieve a greater swing will be severely clipped.

This would not matter for a single square wave, but with more than one being processed, "head-room" is needed to allow four notes to sound louder than just one. That is, a single note must have an output amplitude that is no more than a quarter of that allowable for four notes.

Consequently, attenuation is essential and is determined by the ratio of the resistors feeding into the input (R11 to R14) and the total feedback resistance (R17 plus VR1).
The other consideration is that when PIC-olo is used with headphones, attenuation is required to protect ear drums! With the prototype and using $40 \Omega 2$ headphones, an attenuation to one-fifth was found to be preferable.

## OUTPUT COUPLING

The output signal from IC2a is a.c. coupled by capacitor C5 and fed to socket SK1. Even though the signal is monophonic (in the sense that it uses one


channel only and is not stereophonic - note-wise it is polyphonic). it is suggested that SKl should be a 3.5 mm stereo socket, allowing "personal" stereo headphones to be used.

The use of a stereo socket, though, means that a loudspeaker or amplifier lead must be terminated with a stereo plug. Alternatively, a mono socket could be used. though this would mean that stereo headphones would only produce a single-sided sound.

Capacitor C8 and resistor R19 are included in the output circuit according to the L272 data sheet recommendations regarding signal stability. Capacitor C7, in the op.amp feedback path. smooths off the corners of the square wave signals. making them sound slightly less harsh. The capacitor may be omitted if a harsher sound is preferred.

The second op.amp, IC2b, which is in the same package as IC 2a, is not used. Note that the L272 has unusual pinouts and replacement by an "ordinary" op.amp is not practical without printed circuit board changes being made.

## SWITCHING

Switches S 1 to SI3 are push-to-make types which provide a signal path (in this case to 0 V ) only when they are pressed. Consequently, for the microcontroller to respond as though the switches were change-over types producing either 0 V or 5 V (logic 0 or logic 1). positive biasing of the "open" side of the switches is required.

All of ICI’s 13 port pins (RA0 to RA4 and RB0 to RB7) are used for switch monitoring. Port B's pins (RB0 to RB7) are only used for inputting data and, as they have pull-up resistors included internally. switches S6 to S13 do not need additional biasing resistors.

However, since Port A's pins (RA0 to RA4) are used for both input and output, the internal biasing resistors on those pins cannot be used (RA4 is actually an open-collector pin and is not internally biased). Consequently, switch biasing is provided by resistors R6 to RIO.

Also. because of the dual function for the Port A pins, resistors R1 to R5 are inserted to prevent electrical conflict when switches S 1 to 55 are pressed. Whilst it may seem that "clicks" would be heard when these switches are used, in reality, this does not seem to be the case.
(In retrospect, resistor R5 could have been omitted had the software not been programmed to set pin RA4 as an output - it's never actively used as such here. Programmers try to think of every eventuality, but don't always spot the obvious until later!)

## SPEED AND PDWER

Microcontroller ICl is operated at a clock frequency of 10 MHz , as set by crystal X1 in association with capacitors Cl and C2, and resistor R18. It is essential that only the 10 MHz version of the PIC16C84 is used in this circuit. Although the author has run the standard 4 MHz version at 10 MHz , this condition is likely to be unreliable, and possibly unrepeatable.

The circuit can be powered at between 5 V and 6 V d.c. A 6 V battery supply is perfectly acceptable. Whilst IC1 will tolerate a supply of 7 V , it is recommended that this level of voltage should not be used.

Although a 3.5 mm mono jack socket (SK2) is shown as the power input. any other type of connector can be used. Indeed the use of a "proper" power supply connector is probably a better way to do things.

Capacitors C6 and C9 provide power line smoothing. Resistors R15. R16 and capacitor C4 set a mid-way bias level ( 3 V with a 6 V power supply) for IC2a.

## SOFTWARE

It may appear that the circuit could have been designed so that each of the 13 outputs from IC1 could each have sent a different frequency to a 13 -input mixer around IC2a. Switches would then be inserted into each of the mixer lines. pressing them in order to connect the desired frequency to the mixer.
This, though, would have meant that each line would be constantly generating a frequency having an amplitude of 5 V . Inevitably, all 13 signals would "leak" into the op.amp circuit by a small amount even when the switches were unpressed. As a result. PIC-olo would never seem to be silent.

To avoid this undesirable situation, the software has been written so that an output from ICI is only active when the switch for that line is pressed. This complicated the software program a bit. but not significantly so. It all still takes place

within 236 bytes of the 1024 bytes of program space available.

## PROGRAM FLOW

A listing of PIC-olo's program source code is available as stated later. Studying the listing, you will see that the program starts with a one-off routine that sets the basic initialisation parameters, after which it enters a perpetual loop.

In this loop are 15 principle "cells" of instructions. The first cell, named GETKEY, is principally responsible for reading which keys, if any, are pressed. First, a "note quantity detected" counter is primed (BSF LOOPA.2). This limits the number of pressed keys which will be actioned to a maximum of four.

Next, all of Port A pins (RA0 to RA4) are set for input, the status of these pins is read and stored in a register (REG2). Port A is then reset for output mode.

Immediately following this, a register (REGI) which holds the contents of any previously detected keynote action (zero on the first time round the loop) is loaded into Port A for outputting to mixer IC2a, via pins RA0 to RA3 (pin RA4, although active as an output, is not used in this context). REGI is then reset to zero.

Following the first processing cell are 13 cells which are allocated individually to separate notes. All 13 cells have identical formats but read their data from different sources and have different counter values assigned to them.

Cells one to eight read directly from Port B (which is always set for input mode) and deal with the "natural" notes, $C, D, E, F, G, A, B$ and $C l$ (the latter being one octave higher than $C$ ). Cells nine to thirteen read from the register (REG2) which holds the last key status at Port A. They deal with the "sharp" notes, C\#, D\#, F\#, G\# and A\#.

Each of the 13 cells has two counters. One counter decrements (counts down by one) from a preset value each time the main program loop is repeated. The other increments (counts up by one) each time the first counter reaches zero. The first counter is, at this point, then reset to a starting value which depends on the note being generated by that cell.

In effect, the first counter generates a pulsed waveform at twice the required
audio frequency. The second counter divides the frequency by two and "squares" the waveform that is to be output to the mixer.

In the software example shown in Table 1, which details the cell routine for note $C$, NOTECI is the down-counter, and NOTEC2 is the up-counter:

Taking the cell instructions in detail:

## NC

this is the address name allocated to the start of this cell (standing for Note C)

## BTFSC PORTB, 5

tests the status of Port B pin RB5; is its key pressed (is Port B pin RB5 low/clear - logic 0)? Only one of the next two instructions will be actioned, depending on the "truth" of the answer. If it is "true" that Port B pin RB5 is low, a logic $I$ occurs and is added to the microcontroller's program counter (PCL), causing the program to jump to the second instruction rather than the first. If the answer is "false", a logic 0 is generated which does not affect the program address value, and so the " GOTO ND" ${ }^{\text {instruction }}$ is actioned.

## GOTO ND

to reach this instruction, the key has been found to be unpressed, and so the rest of this cell is bypassed, the program jumping to cell ND (Note D).

## DECFSZ NOTECI,F

to reach this instruction, the key has been found to be pressed, therefore this note's
first counter (NOTEC1) is decremented and checked if it is now zero. If it is "true" that the counter is zero (answer = logic 1) the next instruction ( GOTO AC) is bypassed (same reasoning as with BTFSC above).

## GOTO AC

to reach this instruction, the counter NOTECI has been found not to be zero, so now jump to the part of routine commencing at address AC :.

## MOVLW 58

to reach this instruction, the counter has been found to be zero, so load the W (Working) register with the value of 58 (the frequency setting value for Note C - other note cells have different values set here).

## MOVWF NOTECI

now transfer the contents of $W$ into counter NOTECI.

## INCF NOTEC2,F

and increment counter NOTEC2.

## AC

this is the sub-address jumped to from four instructions above, and the address to which the $I N C F$ instruction immediately above automatically moves.

## RRF NOTEC2.W

the contents of NOTEC2 are now copied

Table 1. Software example

| NC: | BTFSC PORTB, 5 GOTO ND | bit test PORT B pin RB5 for "clear" (zero) go to cell for Note D |
| :---: | :---: | :---: |
|  | DECFSZ NOTEC1 | decrement counter NOTEC1 and check for zero |
|  | GOTO AC | go to note $C$ sub-address $A$ |
|  | MOVLW 58 | move decimal 58 into register $W$ |
|  | MOVWF NOTEC1 | move W into counter NOTEC1 |
|  | INCF NOTEC2,F | increment counter NOTEC2 |
| AC: | RRF NOTEC2.W RLF REG1 F | rotate right counter NOTEC2 into register W rotate left register REG1 |
|  | DECFSZ LOOPA,F | decrement counter LOOPA a |
|  | GOTO ND | go to routine for note D |
|  | GOTO PAD1 | go to timing compensation routine named PAD1 |

into the $W$ register and shifted right by one place. This shifting moves the contents of the W register's bit 0 into a Carry register (actually, a Carry flag since only a single bit of a Status register is used). At the same time, the previous contents of the Carry register are shifted into the left hand bit (bit 7) of the W resister. This is irrelevant here, but is an automatic part of the process.

## RLF REGI,F

all eight bits of register REGI are now shifted left by one place, an action which automatically shifts the contents of the Carry register (now holding a copy of the value of NOTEC2's bit (0) into bit 0 of REGI. (Bit 7, the far left bit, drops out of REGI - in fact, it is shifted into the Carry register, but that fact is also irrelevant here).

## DECFSZ LOOPA,F

decrement the keypress counter (LOOPA) and observe if it is now zero. As with the previous DECFSZ instruction, only one of the next two instructions will be actioned. depending on the "truth" of the answer.

## GOTO ND

as counter LOOPA is not zero. jump to the cell starting at address ND: (Note D).

## GOTO PAD1

as counter LOOPA is zero, bypass the other note cells and jump to the synchronisation routine at address PADI

Each of the note cells are actioned in the same way, either until the counter LOOPA reaches zero (four keypresses have been detected), or until all 13 note cells have been processed in the search for keypresses.

## SQUARE WAVE FORMATION

The data bit value (logic 0 or logic 1) which is shifted into register REGI each time a cell's keypress is detected, changes at the rate at which that cell's'first counter reaches zero.

This rate depends on the starting value repeatedly loaded into that counter. The starting value thus determines the rate at which the second counter increments.

The second counter can be regarded as a divide-by- 2 counter, or flip-flop, whose output (bit 0 ) is alternating between high and low at an evenly spaced rate all the time that the relevant key is pressed. In other words, the second counter can be regarded as a square wave generator.

It is the contents of this generator which are being shifted (transferred) into bit 0 of REGI. For each new bit shifted in, the other bits that have already been set are shifted left by one place, eventually being shifted out and lost.

## NOTE ROUTING

Once the entire main loop has been completed (GOTO GETKEY later on in the listing) and restarted at GETKEY, the current contents of REGl are transferred to Port A. whose first four pins (RA0 to RA3) output the data to the mixer stage around IC2a. Only the first four bits of REGl are important in this program.

The actual Port A pin which is output to by each note generator (via REG1), will depend on where that note is in the sequence of keys being pressed. If only one note key is pressed, say note $G$, the output for that single note will always appear on Port A pin RA3. The other pins, RA1 to RA3, will be set at logic 0 .

If four notes are pressed, say $C . E, G$ and $C 1$, then $C$ will be shifted into RA3, $E$ into RA2, $G$ into RA1 and C1 into RA0. The order in which the notes will occur on Port A is the order in which the key cell routines are accessed, which is a fixed order within the program. Natural notes will always precede sharp notes.

This fact, though, is only of academic interest and will not be apparent to the listener. If no keys are pressed, then the contents of register REGl will be zero, and thus all four Port A output lines will be "silent". There is a bit more on this point later.

## PHASE RELATIONSHIPS

An interesting side-effect of the frequency generating technique, which may sometimes be apparent, is the changing phase relationship between multiple notes played at different moments.

Since the note frequencies are not generated by constantly running oscillators, but are turned on and off when required, notes will commence and end at different points

in their counter's cycles. Examination on an oscilloscope will clearly display this situation. The phases between the notes depend entirely on the starting and ending values of their counters.

The effect is not detrimental to the character of the sound produced. In fact. it enhances it. It is not especially pronounced, but when it is apparent, it adds a bit of "colour" to the sound.

## NOTE FRECUENCY

Of additional interest to the ear is the slightly imprecise tuning of the "oscillators". This is due to the counters having to count in whole numbers (integers) rather than in fractions. It, too, adds character to PIC-olo's sound.

Even though the circuit is being run at 10 MHz , because of the time it takes to perform each instruction, and thus the total time that elapses between each trip round the program loop, the counter starting values cannot be very large, and so cannot be "corrected" by small amounts. A much higher clock rate than 10 MHz (which is impossible with a PIC16C84) would have allowed higher count values to be used, resulting in greater frequency precision.

A selection of frequencies and cell count values, as established when test-programmed into PIC-olo, is shown in Table 2. It will be seen that the use of integer values cannot result in absolute note precision. but the tuning is near enough for all but the most critical ears.

Table 2. Frequency and cell count value relationships

| Note | Ideal freq in Hertz | Count | Measured Freq in Hertz | Chosen value |
| :---: | :---: | :---: | :---: | :---: |
| C | 261.625 | 59 | 255.3 | Yes |
|  |  | 58 | 259.6 |  |
|  |  | 57 | 246.2 |  |
| C\# | 277.182 | 55 | 273.8 | Yes |
|  |  | 54 | 278.8 |  |
|  |  | 53 | 284.1 |  |
| 0 | 293.66 | 52 | 289.6 | Yes |
|  |  | 51 | 295.3 |  |
|  |  | 50 | 301.2 |  |
| D\# | 311.126 | 49 | 307.3 | Yes |
|  |  | 48 | 313.7 |  |
|  |  | 47 | 320.4 |  |
| $E$ | 329.627 | 47 | 320.4 | Yes |
|  |  | 46 | 327.3 |  |
|  |  | 45 | 334.6 |  |
| F | 349.229 | 44 | 342.3 | Yes |
|  |  | 43 | 350.2 |  |
|  |  | 42 | 358.5 |  |
| F\# | 366.994 | 42 | 358.5 | Yes |
|  |  | 41 | $367.2$ |  |
|  |  | 40 | 376.4 |  |
| G | 391.995 | $39$ | $386.1$ | Yes |
|  |  | 38 | 396.2 |  |
|  |  | 37 | 406.9 |  |
| G\# | 415.304 | 37 | 407.0 | Yes |
|  |  | 36 | $418.2$ |  |
|  |  | 35 | 430.1 |  |
| A | 440.00 | 35 | 430.2 | Yes |
|  |  | 34 | 442.8 |  |
|  |  | 33 | 456.2 |  |
| A\# | 466.163 | 33 | 456.3 | Yes |
|  |  | 32 | $470.4$ |  |
|  |  | 31 | 485.6 |  |
| B | 493.883 | 31 | $485.6$ | Yes |
|  |  | 30 | 501.8 |  |
|  |  | 29 | 519.2 |  |
| C1 | 523.251 | 30 | 501.8 | Yes |
|  |  | 29 | 519.2 |  |
|  |  | 28 | 537.6 |  |

It will also be seen that there is little room for manoeuvre if raising PIC-olo's overall frequency range is contemplated. There is room, though, to lower its range; doubling the count value will halve the frequency.
From this it will be spotted that all halved frequencies would have even count value numbers, which would allow the actual frequency to be shifted closer to the ideal by changing some numbers to the nearest odd number.

## PROGRAM BALANCE

A final, but most important point about the software for PIC-olo, is that it has been "balanced". Obviously, the time taken to process each cell will depend on the action required:
If no keypress occurs for a cell, the cell's software routine takes only two instructions to perform (e.g. BTFSC PORTB. 5 and GOTO ND). It can take nine instructions if the key is pressed. Such disparities in timing require additional time to be added later in the loop, otherwise the frequency generated by all cells will vary, depending on which keys are pressed.
Consequently, towards the end of the program loop, another cell contains a series of "pads" (a padded cell? - curtail that humour!). The "pad" to which the program will jump depends on the position from which the jump occurs.



If, for example, notes $C, D, E$ and $F$ are pressed, all four allowable keypresses will have been registered and so the jump occurs from the end of cell NF: down to PAD4. The dummy actions within PAD4 through to PAD12 are then processed, each instruction increasing the total timing of the whole loop.
If. though. the fourth keypress is on note A\#, the jump is to PADI2, the final pad. In both four-key cases, the total timing for the loop's completion remains the same.

All "pads" are identical in function and format. They simply test a dummy bit of a dummy register (REG3), simulating the action that would have occurred if each key that has not been checked had been checked. As an example. PADs I and 2 are simply:
PADI:

PAD2:

## BTFSC REG3,0 GOTO PAD2 <br> NOP (no operation) <br> BTFSC REG3.0 <br> GOTO PAD3 <br> NOP

Had the remaining keys actually been checked, undesirable time penalties would occur if they were found to be pressed. Remember that the PADs are only actioned if all four permitted keypresses have occurred. Any further keypresses are unwanted.

In reality. PADs 1 to 3 will never be accessed, but they are retained to preserve the author's sense of program uniformity!

## MORE TIME WASTING

Should fewer than four keys be pressed, additional timing compensation needs to take place. This is carried out in a loop named CHKIT, its processing count being related to the actual number of keys pressed: once round the loop for three keypresses, twice for two, three times for one.
As with the PADs, actions within loop CHKIT are basically time wasters, they are simply there to delay matters by a predictable amount. One useful action does occur, though, register REG1 has its contents shifted left by one place each time round the CHKIT loop.
This makes Port A pin RA3 the dominant output line. The dominance of the other lines decreases in order of RA2, RAI, RA0. If a key is pressed. RA3 will always have a frequency on it, whereas RA0 will only carry an output frequency if four keypresses have occurred.

## CALTION

Under no circumstances should the balance of the main loop be upset by making amendments to any of the instructions within it, other than to change the counters' reset values. All frequencies
will be upset if any one cell's instruction timing is changed in any way.

Resist the temptation to simplify the program by using a look-up table for the count values. The use of a table will slow down the rate at which the main loop is processed.

## ROUND AGAIN

Once any timing compensation has been performed and one of the commands GOTO GETKEY has been reached. the program jumps back to the first cell at GETKEY: whereupon the software again reads Port A and transfers the contents of register REGI to it.

The speed with which the whole cycle occurs results in notes being generated at the rate shown in Table I and marked with "Yes".

## CONSTRUCTION

Details of PIC-olo's printed circuit board (p.c.b.) component layout and full size copper foil master are shown in Fig.2. This board is available from the EPE PCB Service, code 164.

Fit the three on-board link wires first, followed by the resistors, capacitors Cl and C 2 , and then the i.c. sockets.

Crystal XI and the remaining capacitors (C3 to C9) are all mounted on the back of the board and horizontal to it (see photographs). All these latter components should be as close to the board as possible to minimise the board's overall assembled thickness (its profile).

Observe the correct polarity for the electrolytic capacitors (C3 to C6). Use a piece of insulating tape below crystal X1 to prevent its metal case from shorting across the p.c.b. tracks.
Now mount the switches, SI to S13 ensuring that the indents in their sides are aligned as shown in Fig.2. If different switches are used, check their correct orientation using a multimeter on a resistance range.
A word of caution about the switches. those used in the version of PIC-olo published here are not the cheapest available. Cheap switches were used on an earlier prototype but their operation left much to be desired in a unit that relies heavily on their repeated use. As a result, more expensive ones were chosen and the board redesigned to accept them.

The type of sockets you choose for SKI and SK2 depends on the equipment with which PIC-olo will be used. Consequently the inclusion here of socket wiring guidance would be inappropriate. If in doubt, again check matters with a multimeter.
The socket wires may be soldered directly to p.c.b. tracks or to single-sided terminal pins (flat side on the track side).
Before applying power or plugging in the i.c.s, check all your soldering thoroughly

and double check the correctness of the component positioning and polarity of the electrolytic capacitors.

Correctly inser the i.c.s (ICI has to be pre-programmed, of course, as detailed in a moment). Connect up to a power supply and to something that allows you to hear what is happening - headphones, speaker or amplitier.

Press the heys in turn and check that each results in a different note being heard. and then that up to four notes can be played simultaneously.

All right? Good. now you can make music!
But, you'll want a case first.

## housing pic-olo

Originally. a narrow plastic plumbing pipe seemed likely to be the ideal housing for PIC-olo: nice and slender, shape reminiscent of a real wind instrument. Regrettably, the finished construction was just a bit too big to tit in such a pipe. Additionally. it was recognised that the curvature of the pipe would make adequate protrusion of the two rows of switches a difficult matter.

However the electrical trade uses plastic conduit of a rectangular shape and in several sizes. It was a length of conduit ( $240 \mathrm{~mm} \times$ $37 \mathrm{~mm} \times 25 \mathrm{~mm}$ - internally $35 \mathrm{~mm} \times 21 \mathrm{~mm}$ ) which was chosen for PIC-olo. It should be available from specialist electrical stores. and possibly from some major DIY stores. Ensure that you get the type which has one side which can be easily removed.

The internal measurements must be no less than those stated, but may be more: the length is not so critical. The size of the components used. especially the electrolytic capacitors, will also determine the conduit size that can be used. It is recommended. in fact. that the conduit is only bought after PIC-olo has been fully constructed.

## SWITCH HOLES

Care must be taken over the position of the holes through which the switches must protrude. First remove the detachable side of the conduit. Put that to one side for the moment

Take a photocopy of the p.c.b. track layout in Fig. 2 and securely tape it to the broadest surface of the other ( U -shaped) part of the conduit. Accurately mark the central position of each switch. and the points at which the securing bolts will pass through. then carefully drill small diameter holes at each point using a power drill. Use larger drill sizes to enlarge each hole as appropriate.

It is likely to be found that the switch holes required are a larger diameter than the largest standard drill size. If so, very carefully enlarge each hole using a counter-sink bit or cone-shaped holeenlarging tool. Beware that both tools are likely to " snatch" at the hole. making it jagged. so take extreme care. The use of a drill stand for this process is essential.

Ensure that the holes become the right size to allow the switches to pass through, permitting them to be pressed smoothly, and to release without friction. It may be necessary to trim off parts of any plastic ridges within the conduit to let the p.c.b. be positioned satisfactorily.

Using the four bolt holes provided (only two were used on the prototype, but two have since been added to the p.c.b. artwork). pass round-headed bolts through the conduit, heads on the outside. Tighten nuts down onto each bolt.

Fit another nut to each bolt. positioning it so that the p.c.b. will rest on it, allowing the switches to pass through their holes. Now fit nuts to secure the p.c.b. in position, and tighten them down. Double check that all the switches can still be used freely.

Drill holes in one of the conduit's plastic end caps (available from the conduit supplier) to suit the sockets used for SK1 and SK2. Secure the sockets in position.

Now press the detached conduit side back into position and push on the end caps. That's the case finished, unless you want to add legends to it, saying what it is and what the note names are. Rubdown lettering will do the job.

As an alternative to this case. PIC-olo could be housed in a large colourful case more suited to encourage a child's visual interest. To this end, coloured switches could be chosen instead of the black and white ones used in the author's model.

## PROGRAMMED PIC

Software for PIC-olo Music Maker can either be obtained on disk from the editorial office or down-loaded from our web site. There is a nominal handling charge for the former; we make no charge for the latter. See Shoptalk page for details.

It is again stressed that the 10 MHz version of the PIC16C84 has
to be used with PIC-olo. Before loading it with the software, it must be initialised for 10 MHz use, with Watchdog and Power-onReset both off.
The software was written using TASM. the shareware (Public Domain Shareware Library, Version 3.0 originating from Speech Technology Inc.) PIC programming software first made available when the EPE Simple PIC Programmer of Feb '96 was published. This software continues to be available from the editorial office on disk. and from our web site.

Whilst knowledgeable programmers will be able to translate the TASM coding to suit other programming software, such as MPASM, neither EPE nor the author can advise on this.

Pre-programmed PICs are also available from our regular programmed-PIC source. Magenta Electronics, again see Shoptalk for details.

The web site address (including subdirectory) is ftp://ftp.epemag.wimborne. co.uk/pub/PICS/PIColo

Readers who wish to know more about PIC programming should refer to the Back Issues page where previous EPE PIC-based project articles are listed and available as stated. You should also obtain PIC data books from Arizona Microchip, Unit 6, The Courtyard. Meadowbank, Furlong Road, Bourne End, Bucks SL8 5AJ. Tel: 01628851077. Fax: 01628850259.

## CONCLUSION

For the author. designing PIC-olo Music Maker was an interesting exercise in finding out how a PIC could cope with music making. As it turns out. an instrument like PIC-olo seems to be just about all that this particular microcontroller can handle. It is a powerful microcontroller in many applications. but sophisticated music making is not its forre.

PIC-olo is a lot of fun and will not only provide enjoyment, but could also offer
children early steps in understanding how to make music. It is not a precision musical instrument. but it was never intended to be. More elegant electronic musical instruments need much greater processing power and a lot more circuitry. Nonetheless, we believe you will enjoy PIC-olo.

The thought occurs. though. could another EPE PIC project be inspired by the Bournemouth Symphony Orchestra? Although the author would like to honour. say. the harp section. the project title HarPIC might be misunderstood!

## SHOP RHWALK with David Barrington

## Variable Bench Power Supply

The only item that stands out as likely to possibly cause sourcing problems for those undertaking the Variable Bench Power Supply project is the 1.2 V temperature compensated voltage reference type ICL8069. If it is unobtainable from your usual local supplier, it can be purchased from Maplin, order code YH39N.

It is important to use the specified PMOS op.amp type CA3140E as it is capable of handling the low input and output voltages associated with this circuit. It was found that most other "similar" op.amps will not work properly in this circuit. The CA3140E is generally available from most of our components advertisers.

The case must be an all-metal type as the base also acts as a heatsink for the Darlington transistor. Also, as the "heat-tab" is connected to its collector terminal the Darlington must be isolated from the case using a standard TO220 insulating kit, consisting of a washer and plastic bush.

The battery eliminator used to power the circuit MUST be a non-regulated 12 V d.c. type - no other type is suitable. Most mains eliminators offer a range of low output voltages, so make sure the selector switch is set to the correct setting and check that the tip of the plug is the positive voltage connection. The eliminator used with the model is an Altai 750 mA version which many of our advertisers may stock.

This project is built on the EPE Multiple Project PCB, this is available from the EPE PCB Service. code 932.

## PIC-olo Music Maker

It is essential that only the 10 MHz version of the PIC16C84 microcontroller be used for the PIColo Music Maker project.

The dual power op.amp. type L272 is listed by two sources, namely: Maplin, code UJ36P and RS outlet Electromail (\$01536 204555), code 635-167.

Tuming to the "keys", the pushbution switches used in the model are not the cheapest, but
cheaper ones used in an early prototype soon showed up defects after repeated use. As a result, it is recommended that constructors stick to the specified (push-to-make release-to-break) RS types. These are obtainable through their mail order outlet Electromail, codes 334-915 (black) and 334-886 (white).

For those readers who do not have their own facilities to program PIC chips, a readyprogrammed PIC16C84 can be purchased from Magenta Electronics ( 801283565435 ) for the sum of $£ 15$, including post and packing.

However, if you wish to do your own programming, the software listing (TASM) is available on a 3.5 in disk from the Editorial Offices - see PCB Service page for details. If you are an intemet user, it is available Free from our FTP site: ttp://ftp.epemag.wimborne.co.uk/pub/PICS/ PIColo.

The PIC-olo printed circuit board is available from the EPE PCB Service, code 164.

## Micropower PIR Detector - Control Board <br> The dual-in-line switches, used on the Control

 board for the Micropower PIR Detector, are described as being sub-miniature s.p.d.t. types and a quad (S1 to S4) and a single version are used here. These switches are carried by most component stockists. but, be warned, the minimum spacing between banks is 0.15 in . so a 4 -switch d.i.l. packáge must be used for S 1 to S 4 if it is to fit on the p.c.b.The BUZ11 power MOSFET came from Maplin, code UJ33L. Note that the suffix L should be quoted when ordering the BC184L transistor, as other versions have a different pin-out line-up.
The printed circuit board is available from the EPE PCB Service, code 163.

## Universal Input Amplifier

The NE5532 i.c. is classified as being "suitable for use in high quality and professional equipment", and is the preferred choice of op.amp for the Universal Input Amplifier project. This dual
low-noise op.amp should be widely available, but if problems locating it do arise, it is stocked by Maplin, quote code UH35Q.

The small printed circuit board is obtainable from the EPE PCB Service, code 146.


## Multimeter Offer

If you're looking for a good low-cost analogue multimeter (everyone should have one as well as a digital meter), you will most likely find just what you want from Squires Model \& Craft Tools. The Altai HM102BZ multimeter in our photograph is being offered to EPE readers at just $£ 14.95$ inclusive of VAT; postage within the UK is free.

If you think the meter measures up to what you are looking for, contact Squires Model \& Craft Tools, Dept. EPE, The Old Corn Store, Chessels Farm, Hoe Lane, Bognor Regis, West Sussex, PO22 8NW. (ঞ01243587009).

## PLEASE TAKE NOTE

## Infra-Red Remote Control Repeater

July '97
A suitable substitute for the BP103B phototransistor is the SFH300, which is available from RS/Electromail, code 585-220.

## PIC Digilogue Clock

June '97
Reverse the polarity of capacitor C1 in Fig.5. Notches in switches S1 and S2 align vertically. 1 f instability of the display occurs (e.g. erratic "rotation" of the hours l.e.d.s), decrease the value of R1 to 1 k .

Cut off pins 6 and 7 of X2 display di.I. holder (units of minutes - bottom right-hand two pins) ensuring that they cannot touch the p.c.b. track (this is an easier solution than cutting and rerouting the associated p.c.b. tracks).


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# RE-USABLE CDs AT LAST 

# A Stop-Press report from Barry Fox They've done it - achieved the reality of re-writable CDs for the consumer! 

HEWLett packard, Missubishi, Philips, Ricoh, Sony and Yanaha are launching the product consumers have been wanting for the last ten years. CD-ReW'ritable looks like an ordinary $C D$ but behaves like a giant floppy disc, with 500 times the capacity: The dise records and plays back any kind of digital data, including back-up files from a P'C, hi-fi sound or video. It can then be erased and reused.

CD-RW discs play back on CD-RW recorders or a new generation of CD ROM drives. But unheknownst to their owners, some recently purchased CDROM drives already contain "Multiread" circuitry which self-adjusts for CD-RW playback. Anyone buying a new ROM drive should now insist that it is Multiread capable.

Conventional CDs and CD-ROMs are pressed from plastics, with a spiral of pits in the surface, and covered with reflective material. The player reads the disc with a
laser beam whose reflection is affected by the pits.

Write-once CDs are pressed from plastics without pits, and coated with a dye which absorbs laser light during recording. The dye gets hot and permanently deforms into pit-like marks. The dise has a "pre-groove" which is formed during pressing and serves as an optical guide for the laser during recording.

## CRYSTAL GAZING

The new CD-RW discs are coated with an alloy of silver, indium, antimony and tellurium. In its natural state, the alloy is crystalline and retlects light efficiently: During recording, the laser heats spots on the surface to around $600^{\circ} \mathrm{C}$, so that the alloy meits. The spot then cools so quickly that the alloy does not have time to recrystallise. It "freezes" into an amorphous state which is much less efficient at reflecting light. So the readout laser sees the spots as pits.

## SEMICON INDEX

## IT was with pleasure that we recently

 received the latest edition of the Semicon Index. For many years we have benefitted from various editions of this authoritative source of semiconductor information and are pleased to receive the latest. If there is a more comprehensive single source, we have yet to learn of it.The Semicon Index comprises three volumes, each dedicated to a specific group of semiconductors. Volume One covers transistors, Volume Two details diodes and thyristors, and Volume Three covers integrated circuits.
Each volume is around 35 mm thick.
 with pages measuring about $175 \mathrm{~mm} \times$ 246 mm . Device listings are split to reflect general type categories, and in each category the devices are itemised in alphanumeric order. The essential characteristics for the devices are then presented in tabulated form, complete with a cross-reference to outline and pinout details displayed in a later section. A guide to each device's manufacturer(s) is also given.

Details of the manufacturers, complete with their distributors' details, are given in another section. A further section of each volume gives guidance on possible substitutions where appropriate. Surface mount devices are included.

Anyone seriously into electronics will find that once these volumes have had their data riches probed, you will never want to be without them.

Each volume is available separately and may be purchased soft-bound, or loose-leaf in a sturdy ring binder. Loose-leaf updating packs are released periodically to keep you abreast of the latest device introductions. Devices are never dropped from the Semicon Indexes and so you will have access to historical data as well as the very latest.

To find out more about these invaluable data source volumes, contact Semicon Indexes, PO Box 470, Lee, London SE12 8AF. Tel/fax: 0181 852-2309. E-mail: Semidex@ aol.com.

To erase the recording, the laser again heats the alloy but more slowly and to a lower temperature, which lets the alloy re-crystallise.
The coating can be erased at least a thousand times and is protected by a transparent lacquer which is sufficiently robust to let the user handle the disc. So CD-RWs do not need a protective caddy and can thus fit any conventional ( $i$ ) drive.
Existing CD-audio players and CD)ROM drives have a light sensor designed to read pressed or write-once dises which reflect 70 per cent of the laser light from the lands between the data pits, and around 30 per cent from the pits. The difference represents digital ones and zeros. Although it is possible to make an alloy which matches these optical characteristics, the recording laser must be very powerful to melt the alloy. The system is then too expensive for consumer use.

## NEW MULTIREAD STANDARD

Recently, Philips and HP agreed a standard for CD-RW with 20 per cent reflection from the alloy in its crystalline state and five per cent in its amorphous state. The recorder can use an inexpensive 10 mW laser. But the player must have a more sensitive sensor.

The new standard defines a Multiread player with automatic gain control in the circuit which amplifies the signal from the light sensor. If the disc is a pressed CD or CD-R, the amplifier gain is turned down; if the disc is a C.D-RW with lower reflection, the gain autematically increases.

The Multiread standard has now been endorsed by all the major manufacturers of $\mathrm{CD}-\mathrm{ROM}$ drives. Around one third of the latest models in the shops, known as $12 x$ drives which run at 12 times normal speed, already have AGC. Once makers have sold off their old stock, all new drives will conform. Some old ROM drives and even CD audio players may; by happy chance, have sufficiently sensitive optics to play CD-RW recording.

Philips' (ID-RW recorder will cost $\$ 900$, with 650 Mbyte blank dises costing \$25. Hardware and disc prices should soon fall to around half. The Philips recorder tooks like a (D) player and connects to a PC to copy any files on its hard disc. The control software already lets it copy music files. Philips expects standalone audio recorders "this year or next".

# Sinclair Tunes in Again 

## Alan Winstanley has the details

THE irrepressible British inventor, Sir Clive Sinclair, is at the forefront again with a gadget harking back to his origins in electronics, in the form of the imaginatively-named X1 Button Radio, which he dubs "the world's smallest radio". The size of a 10 pence coin, the miniaturised mono X1 in-ear device weighs in at just 15 grams and is powered by a lithium cell which offers up to a year's usage.

A special "sure grip" feature is incorporated into the custom ABS moulding, and it includes its own antenna. Sinclair's X1 Button Radio automatically seeks and tunes in to FM stations, using pushbuttons to scan the 88 to 108 MHz FM band, in the same way a car radio and some personal stereos search for and lock on to radio broadcasts. The X 1 is only available by mail order in the UK and is supplied complete with battery.
Back in 1962, the fledgling inventor, aged 22, formed Sinclair Radionics and marketed his first AM-band miniature radio, the Sinclair Micromatic, which retailed at 49 shillings and sixpence (under $£ 2.50$ ) in kit form. He soon established a reputation for miniaturised, novel gadgets utilising new technology.

The Sinclair name subsequently adorned a wide variety of sleek products, including the Cambridge calculator, slim-line audio products and speakers, the best selling ZX81 and Spectrum computers, digital test equipment, the Microvision pocket television, the Sinclair QL ("Quantum Leap") and Z88 computers, before he sold the brand name to Alan Sugar's Amstrad in 1986.

Sir Clive is better known these days for his electric transport accessories, notably the "Zike" electric cycle and "Zeta" electric drive add-on for bicycles. We are promised another major innovation from Sinclair later this year in the electric transport market.
Most of Sinclair's former products were the subject of stylish advertising campaigns for mail order kits, and were a regular feature in the pages of Everyday Electronics and sister magazine Practical Electronics at the time.



Not all of his products were successful, and reliability was sometimes a problem as he "pushed" the technology, but Sinclair's visions were still ahead of their time and pace setting, nevertheless. His remarkable inventive genius was rewarded with a knighthood in June 1983.

With the X 1 , maybe his creative yearning for electronics technology has awakened once again.

The Sinclair X1 Button Radio is available for $£ 10$ inc. P\&P by mail order only, from Sinclair Research Lid., Vector Services Division, 13 Dennington Road, Wellingborough, Northants NN8 2RL. Tel: 01933279300.

## B.A.E.C. NEWS

LAST time we received a British Amateur Electronics Club (B.A.E.C.) newsletter, we were dismayed that they appeared to be having difficulties finding a replacement for their retiring editor, Alf Denison. We are pleased to learn in the latest newsletter that all appears to have been resolved.

In a nutshell, a team of B.A.E.C. members is now jointly carrying out the necessary compilation, editing and production involved. Other members, of course, continue to supply the material.

The B.A.E.C. is a club which en ${ }^{x}$ courages the enjoyment of doing electronics and of learning about it. We have always supported it, believing in its aims. If you would like to be directly involved with like-minded electronics enthusiasts, join the Club!

For more information, contact the Secretary, J. F. Davies, B.A.E.C., 70 Ash Road, Cuddington, Northwich, Cheshire, CW8 2PB. Tel: 0106883742 . E-mail: john@fdavies.u-net.com.

## SINGULARITY

IN an age when it is customary to cram as many functions as possible into a single i.c. package, the developing news of an opposite concept comes as something of a surprise. The news, received from two companies independently, Texas Instruments and Fairchild, announces the larest single gate i.c.s to be released.

As most of you will be aware, it has been conventional for many years for functional gates such as AND, NAND, NOR etc., to be housed as four of a type in a single package. Now, in what seems to be an about turn, chips comprised of just one gate are finding a use where board space is at a premium. Normally, if one wants a single gate, the quad package is used and the other three gates ignored. But why take up extra space if you don't need to seems to be the concept now being catered for by i.c. manufacturers.

The gates from Fairchild and Tl are all surface mount devices and cover the functions NAND, NOR, AND, OR, NOR, inverter and Schmitt inverter. Fairchild call their range the TinyLogic family (HST series); TI's form the MicroGate family.

Fairchild's web site is: http://www.fairchildsemi.com. Texas are at http://www.ti.com.

## ARE PCs TO REPLACE TVs?

## It may seem an heretical thought to avid programmers, but you could soon be using your PC to watch TV - Barry Fox explains

THe latest Homefront study, from electronics market research company Odyssey in San Francisco, shows how people are buying computers and using them instead of watching TV. But computers are now widely known to be difficult to use and sales to new customers are drying up.

Odyssey interviewed 4,500 consumers, across the country, and found that repeat purchases dominate the market. Only 32 per cent of consumers are now first time buyers, compared to 49 per cent a year ago. So home penetration now remains stable, with PCs in 37 per cent of US homes.
Odyssey's President, Nick Donatiello predicts that, "You won't see PCs in more than 50 per cent of US homes until the manufacturers figure out how to make them easier to buy, set up and use".

Odyssey's study also shows that 46 per cent of US households with computers (equivalent to 17 per cent of all US households) are now online. On average they spend $9 \cdot 4$ hours a week sending electronic mail or surfing the Internet.
By far the most successful Service Provider in the USA is America On Line, which now has 22 per cent of the market, compared with nearest rival, Compuserve, which has four per cent.
Particularly interesting is Odyssey's finding that 30 per cent of the PC users who surf the Internet say they are doing so instead of watching TV. Furthermore, 23 per cent of them are sending electronic mail, instead of mailing letters.

All this helps explain why compoter giants Microsoft, Compaq and Intel have now formed an alliance to develop and sell a new generation of PCs that will receive digital TV programmes and digital data transmitted over the airwaves. The announcement, made at the National Association of Broadcasters Convention in Las Vegas early in April, follows the decision, made last December by the Federal Communications Commission in the USA, on digital broadcasting.

The FCC has not defined a TV standard, instead it has set a range of digital standards, which range from current TV picture quality up to high definition pictures, with low cost receivers still able to receive high definition transmissions, and display them in standard definition.

The Microsoft/Compaq/Intel Alliance believes that adding digital TV reception to a PC will put only $\$ 100$ on the price. The first sets should be available in the second half of 1998 and the Alliance predicts that by the year 2000 all new PCs will be digital TV receivers.


BARRY FOX

## The world's first electronic computer, built in secret during World War II, has been recreated and outperforms the latest Pentium PC.

EVERY other weekend a group of volunteers welcomes visitors to a cluster of decaying huts in the grounds of a stately home near Bletchley, in Buckinghamshire. Despite what the British security services would wish, and thanks to the accidental publication of top secret documents in the US, the Bletchley volunteers can, for the first time, recreate the technology used during World War II to crack the codes used by the German military.

The surprise discovery, which raises questions on the best future for computing, is that the wartime technology can still outperform the latest Pentium PC.

Fifty years ago 12,000 people were working at Bletchley Park in three shifts round the clock. The Public Records Office has $320 .(00)$ messages which were successfully decoded. All the workers were bound by the Official Secrets Act and sworn to remain silent until the day they died. Some visitors are for the first time able to discover what it was their wives and husbands were doing there.

## LOONEY BIN

The locals referred to Bletchley Park as the "looney bin" because it was populated by oddballs, obliged to live a lie. Most of the staff did not understand the jobs they were doing. Each worked on a tightly targeted task, with no knowledge of how it fitted into the overall strategy. Mechanical engineers made precision wheels, electricians connected wires and clerks used two million punch cards a week to build an index.

Only a handful knew that they were building and using the world's first computer. That handful also understood the need for secrecy to continue long after the normal 30 years. The principles used at Bletchley were so far ahead of their time that they apply equally well to modem code-breaking. The application of new technology just makes them more efficient.

When the war ended. the equipment used by the codebreakers was all either spirited away by the security services, or physically destroyed on the orders of Winston Churchill. The blueprints were bumed. Churchill wanted to make sure that the Russians did not know how the British broke codes. He feared, correctly as it turned out, that a Labour Government would oust him, and believed that the Labour Party of the day had an uncomfortably close relationship with the Soviet states. Soon after, in his famous speech given at Fulton. Missouri (March 1946) Churchill wamed that "an iron curtain has descended across the Continent".

## WORKING REPLICA

Although Britain's spymasters, at the Government Communications Headquarters near Cheltenham, have now allowed the publication of numerous books on Bletchley, the authors have always been censored, either directly by GCHQ or indirectly by restricted information. Much of what has been written glosses over simple truths, replaces hard facts with complex maths or omits essential details.

Until 1996, no-one was allowed to build a working replica of Colossus, the first electronic computer, even though enough unauthorised diagrams had survived to prod the memories of those of the original design team who were still alive. GCHQ only relented because the US Security services published papers which the UK had given them during the war. Colossus is now built, with its 2.500 valves glowing, and paper tape drive whirring. at Bletchley.

GCHQ has not needed to stop anyone trying to rebuild the codebreakers" other machine, known as the Bombe. After Churchill's order to smash the equipment at Bletchley, all that remained was one fuzzy photo of a machine in a hut, some machine tool plans for unspecified components and a handful of wiring. This wiring had survived only because one of the workers who was ordered to smash Bombes with a hammer had broken the


The Colossus of Bletchley Park, showing its "front panel", and punched-tape rollers in the background.
handle on his suitcase, and needed something for a makeshift repair when he went on leave at the weekend. He found it recently in his attic.

Even the huts in which the equipment was built and used are at risk. They are now owned by British Telecom who no longer need them for training telephone engineers. So BT's accountants would like to earn $£ 10$ million for the company's shareholders by raising everying to the ground and selling the site for housing.

## ENIGMA

During the war, the Germans used two quite different coding systems to keep their radio messages secret. All operational radio traffic, such as orders to troops and ships, were fed through Enigma machines. These were clumsy to use so the High Command, Hitler and his generals, had a rather more user-friendly device made by electronics company Lorenz. The Allies knew how Enigma worked, but not how to crack the codes it used, which were changed every day. They did not know how the Lorenz machine was made so had to work that out before they could start on the codes.

Enigma was invented in Germany, in the 1920s. Patents gave full details of the mechnical design and the UK built a similar machine called TypeX. It looked like a mechanial typewriter and worked on the principle of substitution. I remember, during a time in the RAF as a service engineer, being taken to a securely guarded hut, told to sign the Official Secrets Act and shown a TypeX machine. As we left the hut, all the notes we had made to help us remember how to service TypeX, were taken from us and destroyed. Signing the Act was completely unnecessary. Within a couple of hours we had forgotten how the machine worked. Fortunately (for Britain) I was never asked to repair one.

Visiting the Bletchley Museum recently reminded me of what I had forgotten. When the operator pressed a key, a lamp lit to signify a different letter. Typing an A could give a $P$; then typing another $A$ might give a $Z$ : typing a $B$ could then give a $P$. or a $Z$. and so on.

The operator wrote down the signitied letter and when a whole message had been keyed in and the corresponding lamp letters noted, a Morse Code operator tapped out the now gibberish message for transmission.

At the receiving end, another Morse Code operator noted the incoming letters, wrote them down and passed them to an operator of another Enigma machine. Keying in the gibberish text. lit lamps to spell out the original clear text message. But this only



A side-ways view of Colossus. Its valves consumed 4.5 kW of power and were never switched off.
happened if both machines were set up in exactly the same way. and this was changed daily and was well nigh impossible to guess.

## WHEELS AND PATCHES

Each Enigma machine had at least three rotor wheels, like cogs, which contained wires that connected the keyboard to the lamps. All the wheels were differently wired and the operator had a choice of five. Each wheel had a inner and outer circle of 26 contacts. and the circles could be turned through 26 alphabet settings to change the internal connection paths.

Each time a key was pressed. the wheels moved relative to each other, continually changing the relationship between the keyboard and lamps. To make things even more complicated, the connections passed through a Siekker board, a patchwork of wires and pins like a miniature telephone switchboard.

A secret list told all operators which wheels to choose, which alphabet settings to use and in which order to place the wheels side by side. This was known as the "ground setting"

The operator then turned the wheels at random into a starting position or "indicator setting". This setting, signified by whichever three alphabet letters were aligned with a marker line, was transmitted "clear" for anyone to receive. The operator at the other end used this information to set his wheels in the same starting position: but it was valueless to anyone who did not know the ground setting.

Together, these confusions piled on confusions gave a total of 150 million million million possible combinations of connection paths. It would take anyone with an Enigma machine, but no knowledge of the ground settings, literally years to decode a message by the "brute force" method of keying it through the machine, again and again, each time with a different setting.

## SCHLUSSELZUSATZ

The Schlusselzusatz, made by Lorenz, worked like a teleprinter or telex machine. Instead of changing one letter into another (like Enigma), the Lorenz machine generated pseudo-random text characters which were then added to the original text. So the clear text message was hidden in a forest of gibberish characters.

Lorenz, which Bletchley people called Tunny, had a keyboard into which the German operator typed a message. The keyboard converted letters of the alphabet into the world standard 5-bit International Teleprinter Code, also known as Baudot code. Each digital word is made up from tive marks and spaces, in combinations which denote text characters. One word represents one character.

The 5 -bit words were stored as punched holes on paper tape, then broken down into five separate bits, and each bit passed through two toothed wheels, similar to the rotary contacts used in an ordinary Strowger telephone exchange. The wheels were connected in series, and each with different internal wiring paths. This changed the marks into spaces, and vice versa, or left them unchanged, depending on the wiring and position of the wheel. So each bit was changed, or left unchanged, Iwice. Two drive wheels turned the ten wired wheels through ever-changing combinations, to give $10^{19}$ code possibilities.

The wheel bank thus created a "key stream" of gibberish 5-bit


Banks of valve circuits; Colossus Mk2 used 2,500 valves.
code words. These were then added to the stream of orginal "clear" text character words, to produce an encrypted stream of very heavily disguised text.

When the receiving machine had matching wheels and the same ground setting, it generated an identical stream of gibberish which could then be stripped out to leave the original message.

As was the case for Engima, the relative positions of the wheels were set, to a secret menu, every day. Then, for each message. their starting positions were set to a new order. The codebreakers at Bletchley first had to know how the wheels were wired, then find the daily wheel pattern and finally the starting order used for each message. Only then could they build a replica of the key stream and strip it from the message. to reveal the clear text.

The British forces did not capture a Lorenz machine until the last days of the war. It was thus up to the Bletchely codebreakers to try and work out how Tunny worked, from intercepted messages.

## INTELLECTUAL FEAT

Mathematician Bill Tutte used a mistake made by an German operator in January 1942 to unravel the key stream for a single 4000 character message, subtract it from the gibberish stream and work backwards to deduce the wheel design. The task took him four months.
'Bill Tutte's work on Colossus was the greatest intellectual feat of the whole war" says Tony Sale. Director of the Bletchley Park Cryptology museum. "At Bletchley they knew nothing about the Lorenz machine and only six people ever understood the signficance of what Bill Tutte had achieved."


Interconnection wiring allowed circuit paths to be changed.
Tony Sale used to work for MI5. with Peter Wright, who later upset the government by writing a book about the security service. Sale then helped the Science Museum rebuild the Babbage Engine. the mechanical calculating machine which Charles Babbage designed but never finished. Three years ago he became fascinated by the computers used at Bletchley and the way the Lorenz mystery was unravelled.

The Lorenz operator had sent the same 4000 character letter twice. because there was a fault at the receiving end - probably because the operator had dozed off. Because he was lazy, the sender used the same Lorenz wheel settings, and thus the same encryption code. Worse still he did not send exactly the same text. Fed up at the prospect of keying it all in again. he shortened some words. for instance using "nmr" for "nummer". This gave the codebreakers "depth", at least two identical or similar messages, sent with the same coding settings. With depth they were able to strip the key stream from the original messages. Tutte then worked backwards from the key stream to deduce the way the wheels on the Lorenz machine were made.

## WORLD FIRST

As with Enigma, the Lorenz codes changed with the daily wheel settings. Even when the codebreakers knew how the Lorenz wheels were wired, it had taken them two months of trial and error, working through every possible relative position for the wheels, to decode a message. By then its content was worthless. and Britain's radio operators were intercepting thousands a day and sending them to Bletchley. What the codebreakers needed was a machine to automate the task. Colossus was the answer. It was the world's' first electronic computer.


Colossus operators at work - note the punched paper tapes running on the pulleys. built by the Post Office Research Station at Dollis Hill under the direction of electronics engineer Tommy Flowers.

Colossus grew out of a primitive machine, jokingly called Heath Robinson, which used two loops of punched paper tape. One tape stored the encrypted message. the other held a selection of possible keys represented by wheel settings derived from the text in a crib. The machine ran the tapes at 30 mph while reading them by shining light though the punched holes onto photocells. All the time the electronics looked for possible matches which might tell what postions the wheels had been in when creating the gibberish.

The Heath Robinson tapes stretched and got out of synchronism. Tommy Flowers then had the idea of using just one tape, to store the encrypted message, while storing the key information inside the machine. electronically. This was done with a plug board of connections and with thyraton valves, which remain in digital on and off positions after electrical switching. like modern RAM chips.

Colossus Mk 1 (with 1.500 valves) was working by mid 1943; Mk2 (which had 2,500 valves) was switched on 1 June 1944. The main difference was that Mk2 also had a refreshable memory, of more switched valves, that let it store some previous characters while processing new ones, and then compare the results.

## CRIBS

Alan Turing, another mathematician recruited to Bletchley from Cambridge University, saw that there was no point in trying a brute force attack on the codes. Messages would be out of date long before they could be read. Instead he tried to predict text that would be found at some part of the message. The codebreakers called these known text strings "cribs". It is this kind of process that the security services would still like to keep secret because the principles are as valid today as they were in the war.

One crib trick was for British bombers to drop mines at carefully targeted Iongitude and latitude in the sea. The German Navy would then transmit a message which somewhere contained the known grid references. The same technique was used by British spy ships, disguised as fishing trawlers, to prod Russian spy ships, also disguised as trawlers, into sending messages during the Cold War.

The Germans also used Shon Weather Codes, a lookup table of three-letter abbreviations for known weather conditions. If the weather was known, then the appropriate abbreviation would be buried somewhere in the coded message. Wireless operators used similar short codes to check tuning and report reception strength.

All round Britain, radio stations continually monitored the airwaves round the clock, writing down all the gibberish messages which the Germans transmitted. Although the wireless interceptors did not know it, what they wrote down was sent to Bletchley for analysis.

Colossus used Boolean logic to find wheel patterns that matched letters in the plain text crib with letters in the encrypted message. These wheel pattems thus might be the actual ones used to encrypt the entire message. On the other hand they might not. So Turing devised a system of probability scoring. Colossus continually printed out a list of numbers which represented the probability of a key being good for the whole message, rather than just a few characters.

Clerks then used a Bletchley-built machine which worked like the German Lorenz machine - a "virtual Tunny" - to check the key with the whole message. If it produced clear German, the key was the one used by the German operator. If most of the message came out as gibberish. with a only a few letters correct by pure


[^1]

Keyboard data operators at Bletchley. None knew what purpose the data served.
chance, the key was wrong. The operators then had to go back to Colossus and look for the next highest probability score.

Precursing modern parallel processors, like the latest Pentium MMX chips, Colossus checked five paper tape tracks simultaneosly, to generate five pulse streams, each running at $5,000 \mathrm{~Hz}$. Colossus then performed 60,000 calculations a second, on each of the five tracks, making a total of 300,000 calculations a second.

The $2,5(0)$ valves consumed 4.5 kilowatts of power. PO designer Tommy Flowers told the codebreakers to leave them all running all the time. Valves fail when they are switched on from cold and pass a surge of current.

## NOT FORGOTTEN

Tony Sale wants to ensure that Britain's remarkable technical achievements are not forgotten, as the original designers die. The US computer industry has made far more capital out of ENIAC, the Electronic Numerical Integrator and Computer. completed in 1946. ENIAC calculated the trajectory of shells in different wind and air conditions; it had 19,000 valves but was not completed until 1946.

Sale also believes that the work done fifty years ago has an important message for today's computer industry. A dedicated computer, like Colossus, performs one task extremely efficiently. A multi-purpose platform, like the ubiquitous PC , can perform a wide variety of tasks, none of them very well. To prove his point Sale has written a program for a Pentium PC which does what Colossus did. The Pentium still takes twice as long to come up with the right answer.

The orginal plans for Colossus no longer exist. because GCHQ told Tommy Flowers to burn them. But eight photos survived, along with a few circuit diagrams illegally kept by Bletchley staff.
"No-one believed we could do it", recalls Sale. But the Bletchley Park Trust, which rents the grounds from British Telecom, provided the hut rent-free. Sale spent $£ 6,(000$ of his own money and ex-codebreakers chipped in another $£ 2.000$. When this money ran out, British electronics company Quantel gave $\mathbf{£ 4 . 0 0 0}$ to buy the last valves.

But GCHQ was unhappy about the project.
"It was not the technology of the computers used to break the codes that worried the Government", says Sale. "It was the algorithms used by the codebreakers on those early computers. The work being done by Alan Turing and his team was so many light years ahead of its time that it is still being used in cryptoanalysis today. The Government saw it as a continuing security risk".

Until November 1993 GCHQ thatly refused to allow a rebuild. Sale then got "reluctant agreement" from GCHQ to let the public know what wires, resistors and valves the codebreakers were using. GCHQ released what information it said it still had, but this turned out to be very little. Sale was allowed to rebuild only the short-lived


A row of 4-digit electro-mechanical counters, five rows of patch sockets, and three rows of keyswitches.
Mark 1 Colossus, which had no memory. He was forbidden to connect any wires so that the machine could actually work.

## GIFT FROM THE GODS

Without telling anyone Sale quietly rebuilt the Mark 2 version. complete with memory, connected the wires and got the machine running. A gift from the Gods saved him from prosecution under the Official Secrets Act. In March 1996, the National Security Agency in the USA handed a bundle of 5,000 secret documents on Bletchley to the US Govermment's National Archives and Records Administration library in Washington. The papers contain working plans for Colossus. The NSA had them because the US Army sent a security detachment to Eastcote during the war.

Insiders are sure that the NSA would never have released its papers without permission from GCHQ. They suspect that the Cheltenham organisation only gave permission because whoever it was who dealt with the request, did not understand the significance of what they were doing. Now it is too late. The documents cannot be unpublished and they for ever lift the threat of action on Colossus under the Official Secrets Act.

## TURING BOMBE

Sale now wants to rebuild the electromechanical Bombe which Alan Turing, and fellow mathematician Gordon Welchman, designed to crack the Enigma codes. Again the wartime machine was a precursor to modern parallel processing, and far more efficient. When the Bombe's 15 minute search and match routine is simulated on a Pentium PC, it takes 18 hours.
"Unfortunately the world has gone for the general platform approach". says Sale. "It rules the world. But not always for the best".

The Turing Bombe had rows of contact wheels, mechanically and electrically analagous to the rotors of 36 Enigma machines. Together they worked like an electrical circuit tester. As the wheels


More patch connectors and switches, apparently associated with the counting procedure.
turmed, with a clockwork ticking sound (hence the nickname Bombe), they tried every possible connection path through the Enigma machine. Each Bombe tested 15.726 different paths every 15 minutes. registering those which connected to conlirm a possible match between the original letter keyed into the Enigma machine and the quite different letter that came up as a lit lamp. Operators tried each of the possible settings on a TypeX machine to see if it translated gibberish into a crib.

When the Bombe found a match with a predicted fragment, this did not necessarily mean that the setting was correct, and could decode the complete message. It precluded a raft of other settings. which must be wrong. This narrowed the choice for a further search. The Bombe also took advantage of a design flaw in Enigma. It never encoded an A as an A. or B as a B, and so on. Applying high level maths to this, ruled out some possibilities.

The first Bombe was built in Hut 11 in 1940. Many replicas were later installed in sites in Wavendon, Eastcote and Stanmore.
"The Bombe was a dedicated parallel processor of immense power" says Sale. "Once a connection was made current travelled down the wires at the speed of light. There is no general purpose platform that can match that speed ${ }^{\text {" }}$

But Churchill's destruction was total. No useful plans or wiring diagrams survived, not even some measurements to put a size on the machine seen in the one remaining photo. GCHQ insists that it has nothing on file.

Most of the components were made by British Tabulating Machines of Letchworth. now swallowed by ICL. The high speed relays, which had to switch every 20 milliseconds as the drums stepped, came from the Post Office. The components meant nothing to their makers. The Bombe only took shape at Bletchley when all the components were assembled and connected by the few people who understood the overall plan.

## BRITISH BOMB

Just before Christmas. Tony Sale visited the Washington library, while on a lecture tour in the US. There he found a file catalogued as the "British bomb". It turned out to be a complete description of the Turing Bombe, with 50 photographs of the original machine.
"We jumped a light year", says Sale who now has a full copy of the file. "We are already showing the photos to the "wrinklies", people who worked on the project, and it is triggering all manner of lost memories".

Publication of the Bombe papers also insures the Bletchley volunteers against any threat of prosecution under the Official Secrets Act.

But even with the US papers, the rebuild will take three years and cost at least $£ 150,(00)$. It must be done fast while there are still wrinklies left to remember.
Quantel has started the ball rolling with a gift of $£ 10,(000$, "We know the machines worked". says Quantel's Director of R and D. Paul Kellar, "because we won the war. We now want to see how they worked. The whole electronics industry owes its existence to the work done at Bletchley".

Quantel has a special reason to be interested in the rebuild. Quantel makes the special effects and editing equipment used by TV stations around the world and has stuck with an engineering policy which parallels Turing's. Colossus, like the Bombe, and ENIAC. was designed to perform a specific task, unlike today's computers which can be programmed to perform a wide range of tasks. Quantel's equipment uses dedicated electronics which perform one task only.

## ELDAT

Paul Kellar believes that the Bombe carries an important message for today's computer industry. As the hardware for standard platforms, like DOS. Windows or the Apple Mac, becomes more powerful. programmers write software that needs more power. more memory and more disk space. So it runs at snail's pace speed on anything but the latest machine.
"We call it bloat", says Kellar. "And bloat soaks up advances in technology. At Bletchley they lived from hand to mouth. The way they did it would still be the best way to do it today."
"If standard platforms had existed fifty years ago. some civil servant would have insisted that the Bletchley codebreakers use them. Then we would have lost the war and we would all now be speaking with German accents".

## ACKNOWLEDGEMENT

The illusirations used in this arvicle have been kindly supplied by
The Science Muscum. London.

# CIRCUIT SURGERY 

## ALAN WINSTANLEY

## This month, our regular "Surgery" deals with some obscure capacitor specifications, we also look at coin cell sizes, an alarm modification and answer a PIC query.

## Capacitor ESR

Have you ever noticed a large electrolytic capacitor becoming warm in use? In an ideal world. capacitors are. well, just capacitors, and an ideal capacitor would not do any "work" and would not dissipate heat. After all. in theory they merely store charge and have no resistance.

But, in reality, they have further characteristies which aren't always obvious when you think of these components as "ideal" devices. Sometimes you need to dig more deeply into the data!

Electrolytic capacitors are often used as smoothing (or "reservoir") capacitors in power supplies. One capacitor parameter you might see in a catalogue is its Equivalent Series Resistance or ESR. Maybe you will see alternative specs, including dissipation factor or ripple current given instead. These can give you interesting pointers regarding the performance of the capacitor.

The physical construction of an electrolytic capacitor results in it having an inherent resistance (as well as inductance) which causes it to dissipate $I^{2} R$ power in use, so it warms up. Therefore, this means that there is a limit as to how much "work" such a capacitor can be expected to do and still operate reliably at a given temperature. This becomes most relevant at higher currents (say, over a few amperes output).

Shown in Fig. 1 is an ordinary full-wave rectified power supply used with a stepdown transformer. Even if you don't feel the need to engage in heavy mathematics. there are several aspects which need to be borne in mind when picking a capacitor. even if the circuit is designed using a pretty standard combination of rules of thumb and educated guesstimates!

Firstly, the transformer secondary voltage may be higher when it's off-load. because output voltages are often quoted assuming a simple resistance at full load. Because of the transformer's regulation (or lack of it), the output voltage may be somewhat higher when a smaller load, or none at all, is placed across the output of the power supply.

Transformer regulation is calculated as:

$$
\frac{(\text { Voff load }-V \text { full load) }}{V \text { off load }} \times 100 \%
$$

Regulation specifications are not often quoted in mainstream catalogues. and it could be anything between, say, 7 and 33 per cent. depending on the transformer type. Let's assume 25 per cent for a small 6 VA p.c.b. mounting type: if it has a 10 V secondary (full load) rating, the actual off-load a.c. voltage could be 13.3 V or so.

After full-wave rectification, this voltage will be 1.414 limes higher, or about 19 V d.c. (Incidentally, the bridge rectifier should, therefore, be able to withstand twice this figure as a peak-inverse voltage.) Hence, the capacitor must be rated for the higher. off-load voltage.

If we're honest. when we're designing those simple. impromptu power supplies. a rule of thumb is simply to use, say, a $1000 \mu \mathrm{~F}$ or $22000 \mu \mathrm{~F}$ smoothing capacitor along with a favourite bridge rectifier and transformer, and away we go! It has.
after all, been done many hundreds of times before.

But. for heavier duty applications when things become more demanding, then those hidden specifications should be borne in mind. When shopping around. compare the ESR, ripple current and power factor ratings of the components on offer.

ESR will usually be quoted in milliohms: the lower. the better. Ripple current may be also quoted, indicating the maximum permissible current flowing "via" the smoothing capacitor at (usually) 100 Hz at $20^{\circ} \mathrm{C}$. although ripple at other frequencies may also be included (e.g. 10 kHz ).

Capacitors claiming to be high ripple current imply that they have a low ESR. and are better suited to heavy duty applications featuring high load currents. Other capacitor types may claim to have a low ESL (effective series inductance), as the spirally-wound foils of electrolytic capacitors produce an inherent inductance ( $L$ ) effect. Even the length of the lead-outs can have an effect on this, and the leads should be kept as short as possible. especially at high frequencies.

A final rating which you may sometimes see is that of dissipation factor or power factor. This is calculated by dividing the ESR by the capacitive reactance (a.c. resistance) at a particular frequency. Without getting into power engineering, you can use this figure when making comparisons of different types of capacitor: the lower. the better.

## Schade Graphs

When designing d.c. power supplies, probably the most accurate and elaborate design aids are the "'Schade" graphs, of which advanced electronics enthusiasts will be aware. These have been reproduced in some past data books (e.g. I have them in certain Texas and National books) and even though they were drafted in 1943. they are still held in high esteem today. (O.H. Schade. Proc. IRE. Vol. 31 ).

Schade performed a total analysis of the relationship between all aspects of a capacitor-input filter section, resulting in him publishing a number of complex

Fig.1. Regulation of a transformer determines the maximum voltage across the smoothing capacitor. (Assumes that the secondary voltage is quoted as 'full resistive load'.)
graphs which enable a designer to specify a transformer, rectifier and capacitor in half-wave and full-wave circuits.

Having used the Schade graphs. hobbyists can be forgiven for reverting to the time-honoured technique of using a $1,000 \mu \mathrm{~F}$ capacitor together with a suitable transformer and bridge rectifier for all their low power projects!

However, at least when you're next checking through catalogues, you'll now have more of an insight into those capacitor specifications.

Finally, for the benefit of beginners, we would re-iterate that it is extremely dangerous to misuse electrolytic capacitors. It is safe to use a capacitor with a voltage rating higher than the one specified in the parts list, but it is potentially hazardous to utilise a lower-voltage device. You must always observe their maximum voltage rating.

Even more importantly, because electrolytics are polarised. you must always ensure that they are inserted into circuit the right way round. The polarity of electrolytics is always very clearly marked, so be sure to inspect them closely and double-check prior to powering-up for the first time.

Similarly ensure that you insert bridge rectifiers the correct way round. because sometimes this too could result in the smoothing capacitor being accidentally reversed. Failure to observe any of these warnings could result in serious damage or injury.

## Burglar Alarm Modification

June 97s Circuit Surgery included a simple burglar alarm project which used a transistorised loop to trigger a thyristor. This provides a "latching" alarm which continues to sound even when the triggering signal is removed. Ben Dias of Bristol asked if it could be adapted to provide a further delay:

I'm interested in adapting it for one of my projects. I need the Normally Closed loop to reset itself after a delay of about 5 to 10 seconds if it is closed again in other words, the thyristor would only fire after a break of 5 to 10 seconds on the N.C. loop. Could you suggest a modification?

Straightforward, this - simply add, say. a $100 \mu \mathrm{~F}$ electrolytic capacitor to the circuit (Fig.3, p. 390) in place of capacitor C1. Also increase the value of R4 to, say. 47 kilohms. This will slow the response right down and (rule of thumb time) introduce a delay whose RC time constant is 4.7 seconds.

In practice, because of the leakage and tolerance of the electrolytic, expect variations of the timed period in individual cases. It will also affect the operation of the Normally Open loop, however, as it introduces a delay on that too.

## Coin Cell Batteries

Recently, when I enquired at my local branch of a well known chemist for a replacement "coin cell" for,my electronic organiser (which, in the event. conked out one week later). it brought forth the response. "Do you mean a coin holder?" Um, not quite.

Anyway. Tables 1 and 2 are reference table for the plethora of coin cells.

Table 1. Popular Silver Oxide Cells

| IEC <br> Ref. | Height <br> $(\mathbf{m m})$ | Dia. <br> $(\mathbf{m m})$ | mAh <br> typ. | Varta <br> Ref. | Duracell <br> Ref. |
| :--- | :--- | :--- | :--- | :--- | :--- |
| SR41 | $3 \cdot 6$ | $7 \cdot 9$ | 45 | V391 | D392 |
| SR42 | $3 \cdot 6$ | $11 \cdot 6$ | 100 | V344 |  |
| SR43 | $4 \cdot 2$ | $11 \cdot 6$ | 120 | V386 | D386 |
| SR44 | $5 \cdot 4$ | $11 \cdot 6$ | 170 | $V 357$ | D357 |
| SR45 | $3 \cdot 6$ | $9 \cdot 5$ | 78 | $V 394$ |  |
| SR48 | $5 \cdot 4$ | $7 \cdot 9$ | 70 | $V 393$ | D393 |
| SR54 | $3 \cdot 1$ | $11 \cdot 6$ | 80 | $V 389$ | D389/390 |
| SR55 | $2 \cdot 1$ | $11 \cdot 6$ | 43 | $V 391$ | D391 |
| SR57 | $2 \cdot 7$ | $9 \cdot 5$ | 42 | $V 395$ | D395/3999 |
| SR58 | $2 \cdot 1$ | $7 \cdot 9$ | 22 | $V 362$ | D361/362 |
| SR59 | $2 \cdot 6$ | $7 \cdot 9$ | 30 | $V 397$ | D397 |
| SR60 | $2 \cdot 1$ | $6 \cdot 8$ | 16 | $V 364$ | D364 |
| SR62 | $1 \cdot 6$ | $5 \cdot 8$ | 8 | $V 317$ |  |
| SR63 | $2 \cdot 1$ | $5 \cdot 8$ | 12 | $V 379$ |  |
| SR64 | $2 \cdot 7$ | $5 \cdot 8$ | 16 | $V 319$ |  |
| SR65 | $1 \cdot 65$ | $6 \cdot 8$ | 13 | $V 321$ |  |
| SR66 | $2 \cdot 6$ | $6 \cdot 8$ | 27 |  | D377 |
| SR67 | $1 \cdot 65$ | $7 \cdot 9$ | 20 | $V 315$ |  |
| SR68 | $1 \cdot 6$ | $9 \cdot 5$ | 23 | $V 373$ |  |
| SR69 | $2 \cdot 1$ | $9 \cdot 5$ | 30 | V370 | D370/371 |
|  |  |  |  |  |  |

button cells. silveroxide and Lithium batteries, call them what you will. If you see something curious like $\cdots 9,5 \times 3,6{ }^{\circ}$ imprinted on the electronic calculator or watch in question - it's simply the continental way of writing the battery's dimensions in millimetres!
The tables quote dimensions of most common types, and in compiling them 1 checked the Duracell and the marvellous Varta sites on the World Wide Web (www.varta.com and www.duracell.com). It's also worth pointing out that it's much easier to use these cells on a home-brew p.c.b. these days, now that board-mounting holders for coin cells are available from the major mail order suppliers.

The "SR" prefix means silver oxide (1.55V). An "LR" pretix infers an alkaline ( 1.5 V ) type. but the dimensions would be the same. Mercury oxide ("MR") batteries are apparently being phased out for environmental reasons.

Note that it is always extremely important that coin cells are kept away from babies and children: if swallowed. a chemical reaction will produce a caustic solution, and such cases should always be treated as a medical emergency.

## Which PIC?

A question cropped up from a reader concerning the choice of PIC microcontroller. Joseph Zammit of Malta asks:

I wemt to bus some PIC'InC84s hut the ones I have seen advertised have an "-04." suffix athached to the part number. Is there anything different? Secondly. I know quite a lot of theory about electronics but l'm a bit rusty when it comes to the constructional aspects. Since I need to budget well. how can I boost my confidence?

John Becker, our Technical Editor, advises that the "-()4" devices are what we
might call the "standard". They will operate at clock speeds up to 4 MHz . The "- $10^{\circ}$ " types will operate at clock speeds up to . 10 MHz . The PIC data books give more details of the two versions. Their pin-outs are identical.

As far as the practical aspects are concerned. first you could check out my own five-part series Build Your Own Projects which commenced in the November 1996 issuc. Robert Pentold's periodic Techniques - Acrually Doing It series also provides practical hints and tips for constructors.
My advice is always to get some straightforward projects "under your belt" to gain confidence and practical experience, prior to tackling more advanced subjects where there is the risk of running into difficulty if you've been too ambitious, too soon. That alone could damage your contidence irreparably.

Preferably, purchase the printed circuit board from the EPE PC'B Service rather than make your own. The use of p.c.b.s eliminates a huge scope for wiring errors.

The other main subtle causes of problems are transistor substitution errors (especially if the pinouts differ from the original). component value crrors. and problems with soldering technique.

- If you have any queries or comments. please write to Alan Winstanley. Circuit Surgery: Wimborne Publishing Lid.. Allen House. East Borough. Wimborne. Dorset. BH 21 IPF, United Kingdom.


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# ANDY MILLAR 

## A versatile little front-end amplifier with an impressive performance.

T Is unfortunate that the sound quality of many items of low cost PA and recording sound equipment are let down by the performance of their input amplifiers. This is because the demands placed on a PA or recording input amplifier are really quite heavy. Ideally it should be able to cope with input signals ranging from the quietest microphone to the output of a CD player, whilst having low distortion and low noise at all times.

The circuit described here is designed to replace the input stage of a mixer or pre-amp and will provide a gain adjustable over a very wide range with low noise. It is also a straightforward task to assemble several of these circuits into a simple mixer, as will be shown later.

## HIGH GAINS

The most critical part of a high gain low noise amplifier is the choice of amplifying
component. Despite the vast quantities of new op.amps which have been launched over the past few years. the single 5534 and dual 5532 still seem to be by far the best general purpose audio op.amps for the money in terms of gain range available and input noise.

On the downside is their d.c. performance, which is pretty poor compared with more modern devices, and so some care was required in the design of this circuit to prevent d.c. offset problems. However, the noise performance, even of the noisier 5532, is still very good. At full gain the Universal Input Amplifier circuit will have an output noise of about -60 dBu over the audio band.
Since the maximum output level with $\pm 15 \mathrm{~V}$ power rails will be about 20 dBu . this gives a signal-to-noise ratio at maximum gain of 8()dB - plenty good enough for Rock-and-Roll. At minimum (unity) gain the noise will be at about -96 dBu


Fig. 1. Full circuit diagram for the Universal Input Amplifier.
giving a signal-to-noise ratio of 116 dB , rather better than digital!

The Universal Input Amplifier circuit diagram shown in Fig. 1 provides a voltage gain continuously variable from one ( 0 dB or unity) suitable for tape machine, CD, and some musical instruments, to $1000(60 \mathrm{~dB})$ suitable for low sensitivity microphones. Almost any music signal will fall somewhere between these two extremes. The frequency response is flat to within 0.5 dB between 20 Hz and 20 kHz .
One of the most difficult parts of designing an amplifier with a wide range of gain settings is to achieve a sensible law or "feel" to the control. The human ear is sensitive to logarithmic changes in level, that is if you keep doubling the level of a signal it will sound as though it is increasing in equal steps, which is why we use the dB scale for measuring audio levels.

An ideal gain control would vary the level steadily in dB, but it turns out that for most circuits this requires a "reverse log" potentiometer (pot.). Although reverse log pots. are made the author has never found a retail source for them.


Fig. 2. Gain laws for the amplifier stages.

## CIACUIT DESCRIPTION

The Universal Input Amplifier circuit (Fig. 1) uses a combination of two different gain configurations to give a reasonably good gain law - see Fig. 2 graph. To achieve this, the gain control is split into two stages around ICla and ICIb. Note that VRla and VRIb are connected such that if their wipers (a) come off their tracks, as tends to happen when pots get old and dirty, there is still a feedback path for the op.amps to help prevent unpleasant bangs and crackles.

Another reason for splitting the gain between two stages is so that neither op.amp is overworked. If distortion, noise, and offset are to be kept under control then the gain that is required from an op.amp must always be considerably less than that available.

The gain available from an op.amp at any particular frequency is given by the gain-bandwidth product, always clearly given in the op.amp data, which for a 5532 is 10 MHz . The op.amp will have its lowest gain at the highest frequency.

For audio equipment 20 kHz is normally taken as the highest operating frequency, so to find the gain available in the op.amp we divide the gain-bandwidth product by 20 kHz , giving us $10 \mathrm{MHz} / 20 \mathrm{kHz}$ which is 500. A good rule of thumb is that the gain of the circuit should never be more than one-tenth ( $1 / 10$ ) of the gain available, so we should keep the gain of each stage of this circuit below 50 (34dB).

In fact, the highest gain required in each stage is $32(30 \mathrm{~dB})$. This leaves plenty of gain available in the op.amp to counteract its internal distortion and noise.

Capacitors C2 and C4 keep the d.c. gain of each stage at unity to prevent large offsets, whilst C3 and C5 prevent oscillations. Capacitors Cl and C 6 a.c. couple the input and output respectively.

Resistor RI sets the input impedance. It was found that 47 kilohm ( 47 k ) is suitable for most signal sources, but for an input that will be used with a Guitar this should be changed to 470 kilohms. Note that contrary to popular belief both "low impedance" and "high impedance". microphones can be fed into an impedance of 10 k or higher with no problems.

The value of the output resistor R6 depends on the circuit application. Normally this would be a 100 ohm resistor, which ensures the circuit will not oscillate when a capacitive load. for example a screened cable, is connected to its output.

However, by fitting a 10 k resistor in this position it is possible to make a simple mixer by feeding the output of several Universal Input Amplifier circuits together as described later.

## INPUT SCREENING

The input connector type will also depend on the application, however jack type connectors are recommended as these are available with an extra contact to "short out" the input when the mating jack plug is removed (see Fig. 3). With other types of connector the high input impedance of the input may pick up unwanted signals (particularly mains hum) if the gain is set high when the input is removed.

The input connections should be made with screened wire and the screen of the input connector must be connected to the 0 V connection of the p.c.b. for lowest noise operation. If the input jack socket is a plastic bodied type or otherwise insulated from any "grounded" metalwork then the other 0 V connection should be made to the power supply. If the screen of the input socket will make a connection to the power supply through the metal chassis then no other 0 V connection should be made to the board as this would make an earth loop. which could pick up hum.


Fig. 3. Circuit symbol and wiring to "switched" input jack socket.

## POWER SUPPLY

A suggested optional Power Supply circuit diagram is shown in Fig. 4. Although it shows dual plus and minus 15 V power rails, the circuit will operate from any dual supply rails from $\pm 18 \mathrm{~V}$ down to about $\pm 5 \mathrm{~V}$. This will normally allow it to use the existing supplies when wired as a replacement into an existing piece of equipment.

The power supply should be well smoothed. preferably with voltage regulators. Due to the 5 mA current drain. it is not recommended that the circuit be run from batteries.


Fig. 4. Suggested dual voltage power supply circuit diagram.

COMPONEVTS

## INPUT AMP

Resistors

| Resistors |  |
| :---: | :--- |
| R1 | 47 k (see text) |
| R2 | $1 \mathrm{k5}$ |
| R3 | 2 k 7 |
| R4 | 100 k |
| R5 | 51 k |
| R6 | 10 k (see text) |

See
TALK
Page
10k (see tex)

## Potentiometer

VR1 47 k dual rotary carbon, linear

## Capacitors

| C1 | $1 \mu$ polyester |
| :--- | :--- |
| C2 | $33 \mu$ radial elect. 35 V |
| C3, C5 | $22 p$ disc ceramic (2 off) |
| C4 | $22 \mu$ radial elect. 25 V |
| C6 | $10 \mu$ radial elect. 63 V |
| C7, C8 | 100 n disc ceramic (2 off) |

## Semiconductors

IC1 NE5532N dual low noise op.amp

## Miscellaneous

SK1 6.35 mm ( $1 / 4 \mathrm{in}$ ) plastic bodied jack socket, with break contacts
Printed circuit board available from EPE PCB Service, code 146; metal case, size to choice - see text; 8-pin d.i.l. socket; screended cable; multistrand connecting wire; solder pins (6 off); solder etc.

## MONO MIX AMP (Optional)

## Resistors

| R7 | 10 k |
| :--- | :--- |
| R8 | $100 \Omega$ |

All 0.25W $5 \%$ carbon film

## Potentiometer

VR2 10 k rotary carbon, log.

## Capacitors

C9 82p disc ceramic
C10 $10 \mu$ axial elect. 63V
C11, C12 100n disc ceramic (2 off)

## Semiconductors

IC2 TL071CN low noise op.amp

## Miscellaneous

Stripboard, size 11 strips $\times 10$ holes approx; 8-pin d.i.l. socket; multistrand connecting wire; solder pins (5 off); solder etc.

## POWER SUPPLY (Optional) Capacitors

C13,
C14
$220 \mu$ radial elect. 35 V (2 off)
C15 to
C18 100 n disc ceramic ( 4 off )

## Semiconductors

| REC1 | W005 1.5A bridge rectifier |
| :---: | :---: |
| IC3 | $78 \mathrm{~L} 15+15 \mathrm{~V} 100 \mathrm{~mA}$ |
|  | voltage reg. (see text) |
| IC4 | $79 \mathrm{~L} 15-15 \mathrm{~V}$ 100mA |
|  | voltage reg. (see text) |

## Miscellaneous

| S1 | d.p.d.t. mains toggle switch |
| :---: | :---: |
| T1 | mains transformer: 230 V |
|  | a.c. primary; $15 \mathrm{~V}-0 \mathrm{~V}-15 \mathrm{~V}$ |
| FS1 | 300mA secondary |
|  | 0.5A anti-surge fuse, with |
| holder |  |

If you do need to provide an additional power supply then use the one in Fig. 4, this will power up to six Universal Inpu Amplifier circuits with a mix amplifier (Fig.7). For more circuits replace the $78 \mathrm{~L} 15 / 79 \mathrm{LI} 5$ regulators with $7815 / 7915$ types, change the mains transformer to one with a 1 A rating, and change the values of capacitors Cl 3 and Cl 4 to $470 \mu \mathrm{~F}$.

## CONSTRUCTION

Details of the Amplifier printed circuit board (p.c.b.) component layout, wiring and full size underside copper foil master are shown in Fig. 5. This board is available from the EPE PCB Service, code 146.

Commence construction by mounting the 8 -pin d.i.l. socket for the op.amp, these i.c.s do occasionally fail, in particular they may become noisy. Also, this will allow you to check that the voltage rails are the correct way round before plugging in the op.amp - even highly experienced engineers get this wrong sometimes!

Follow this up by inserting the resistors and capacitors, taking care with the polarities of C2, C4 and C6. Also, don't forget the single wire link.

With the p.c.b. layout given, the board is fixed to the chosen metal enclosure, size will depend on set-up finally selected, using the pot. mounting bush and nut of the Gain control VR1, which, being a dual device, will provide plenty of support for the small p.c.b. If the circuit is built using another method, such as stripboard, then care should be taken to prevent oscillation by keeping the pot. connections as short as possible and the input and output of the circuit well separated.

## TESTING

Testing the circuit is simply a matter of connecting a signal source to the input with the output connected to an amplifier


Fig. 5. Input Amplifier printed circuit board component layout and full size copper foil master pattern.



Fig. 7. Circuit diagram for a Simple Mono Mixer.

Fig. 6 (above). Linking the Universal Input Amplifier into an existing system.
and speaker - remembering to start with the Gain control right down (anti-clockwise). A suitable signal source for testing low gain settings would be a CD or cassette player, whilst the high gain settings would be best tested with a microphone.

If the Universal Input Amplifier is used to replace an existing input stage then some investigation will be required to find the best place to tap the signal in. This will probably involve tracing out the existing circuit, which 99 times out of 100 will have the form of Fig. 6 - if it doesn't then you may require expert help!

The existing coupling capacitor should be removed and the output of the Universal Input Amplifier connected to the


Fig. 8. Suggested stripboard component layout and wiring for the Mono Mixer.
"output" side (right) of the capacitor. The signal input feed must be completely isolated from the old circuit.

Altematively, if your equipment has a "Line" audio input you could build the Universal Input Amplifier into a separate box and connect its output to the "Line" input. The screen of the output connector should then connect to the 0 V of the Universal Input Amplifier.

## MONO MIXER

To make the circuit into a simple selfcontained Mono Mixer, you will need as many Universal Input Amplifier circuits as inputs are required, with a 10 k resistor being used for R6 as mentioned earlier. The outputs of all the Universal Input Amplifiers are then simply wired together and connected to the input of the single i.c. Mono Mixer circuit shown in Fig. 7. Potentiometer VR2 will give a Master Volume control if required.

A stripboard component layout for the Mono Mixer is given in Fig. 8. "Busbars" of tinned copper wire can be used to join the outputs and the supply rails of the Universal Input Amplifiers to simplify wiring as shown in Fig. 9. Again, the 0V connection should NOT be made to the power supply if it will connect through the metal front panel and input connectors.


Fig. 9. Wiring together several Input Amplifier boards for connection to the Mono Mixer stripboard.

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# MEXPRESS - EAS FOR ENEMEERS 

## ROB MILES

## Examining the latest in data manipulation and presentation software offered by Quickroute systems.

MEXPRESS is officially described by software distributors Quickroute Systems as "BASIC" for scientists and engineers. The wording on the MExpress box describes it as a "Technical Fourth Generation Language with Flexibility and Power". In this review, we examine the program, what it can do, and why it may be so useful to engineers. First. let's look at some maths!

## MATHS AND MEXPRESS

If you have a lot of calculations to perform, you will probably want to use a calculator, possibly a programmable one; you may perhaps resort to a computer and maybe a spreadsheet in which you can work out totals and averages, plot graphs and a whole lot more.

However, there are many tasks which engineers and mathematicians would like to perform with ordinary numbers that spreadsheets cannot do easily, and this is where software tools like MExpress come in. MExpress, though, is not a "superspreadsheet". instead it might be regarded as a tool on the next level up.



Fig.1. Circuit diagram for a simple RC network

A number is a single value. perhaps, the voltage across two terminals, and you can manipulate simple numbers with MExpress. A vector, though, is a ser of values, perhaps the voltage across a capacitor as it charges. MExpress can accept a range of values and then treat then as a single vector. Take for example, the circuit in Fig. I

When the switch is closed, the capacitor will charge up via the resistor, and there is an equation which, for any given time, will give us the voltage across the capacitor.

The numbers in lig. 2 represent the instantaneous charging voltage on a $1 .(1 \times 6 \mu \mathrm{H}=$ capacitor in series with a lks? resistor when connected across a $1(K)$ volt source.

The voltages were calculated al quarter second intervals. starting at time ecro, and were then input to MExpress as a vector.


Fig.2. Data for the voltage across a charging capacitor.

You can certainly use it to perform calculations, but you can also apply it to build programs which are able to perform very complex tasks on large amounts of data. These programs can then be embedded into other systems and "do the maths" for them.

Whilst there are already tools of this kind available and engineers have been using them for some time. the promise of MExpress is that it will not only deliver what other programs do, but do so at a much-reduced cost. It also has a few extra tricks up its sleeve.

## VECTORS AND MATRICES

Numbers are the raw data that calculators and spreadsheets work with, and MExpress can indeed work with ordinary numerical data. The real power of the program appears when you start playing with vectors and matrices.

The last instruction plotedtcap): tells it to plot a graph. Fig. $3^{3}$ shows the result.

This is the curve we would expect. with the rate of change slowing down as the voltage on the capacitor approaches the supply voltage. The values conld. in this instatse. have been worked out using a spreadsheet. but, to give you an idea of the power of MExpress, consider the data shown in Fig.t.


Fig.3. Resultant exponential curve. plotted as a 2-dimensional graph.


Fig.4. More complex treatment of multiple vectors.

A single statement has been performed and that has created a new vector, "Fred", and then made a third vector, "Jim", by adding "Fred" and "cap" together. This shows that vectors can be treated as single variables, and played with accordingly.

The technique can now be expanded to handle matrices, and this is where the fun really starts!

A vector is treated by MExpress as a special kind of matrix: one having a single row. A matrix is a set of values of a particular size. Matrices can hold many kinds of data: you can even regard the screen of your computer as a matrix, with a number giving the colour and brightness of each of the arrays of dots (pixels) on it.

MExpress lets you work with matrices in the same way that calculators let you handle simple numbers. You can add them. subtract them and multiply them together.

In addition, there is a range of built-in mathematical tools which can be used to analyse and process matrices, as well as displaying them graphically. This means that you can directly manipulate this form of data without having to write a program specifically to handle the complicated variables.

## PROGRAMMING

It is possible to write scripts, which are programs that MExpress will execute for you. For example, rather than manually work out each value in turn for the capacitor in Fig.I, the five-line program in Fig. 5 was typed in and used instead, resulting in the ligures shown below the program. It looks a lot like BASIC.

Programs can be saved in files and you can create libraries of functions that can be used in even more demanding applications.

Once you get the hang of programming you can go on to produce full-blown Windows applications which have buttons. pointers, sliders and all the gadgets which Windows users seem to like!

The example shown in Fig.6. is an implementation of a calculator using MExpress. At this point, we have probably come full circle!

If you use the Developer version of MExpress, you can create freestanding programs that can then be distributed without needing to make extra royalty payments to the designers of MExpress.

You can also use the system to generate components that can be made part of "C" programs. In this way you can integrate the mathematical abilities of MExpress in your own software.

Programs can additionally import and export audio samples in the standard Windows . WAV format, so that you can
perform signal processing and analysis using it (although you'd need a pretty fast machine to do this in real time).

It is also able to manipulate bitmaps and images, so that you can perform image processing, or just enhance the appearance of your programs.

## INSTALLATION

MExpress requires an IBM-compatible PC running Windows 3.1 or later. However. since MExpress is a 32 -bit program, you will obtain the best results if you run under Windows 95 or NT, particularly if you want to use the Developer version.

It is, perhaps, regrettable that MExpress is supplied on three floppy disks. Whilst it is impressive that something of this power and complexity can be fitted onto only
three disks, it is a nuisance to keep swapping them during installation.

Furthermore, the designers have saddled the program with a rather clumsy key mechanism, which updates a special file onto the distribution disk each time you install or remove the program.

This can not only be annoying, but any damage to your initial floppies might mean that you could have difficulties when moving the program from one machine to another. You might also get problems when you back up and restore the key files.

Quickroute Systems say they will replace damaged disks if you send them the originals, but even so, if the software had come on a CD-ROM, as so many packages do these days, life could have been easier.

In fact, the program was transported over a number of machines during the review test and without problems, except that the Install program made rather a hash of modifying a particular autoexec.bat file. You do have the option. though, to make the changes manually, or to make no changes at all - which seemed to work.

Once running, the program did everything the manual said it would, and behaved itself during testing with no nasty crashes or hangups.


Fig.5. User-written MExpress script.


Start
Fig.6. Windows Calculator application written in MExpress.

## FLAVOURS

There are two flavours of MExpress, the Standard and the Developer versions. The Standard version costs $£ 99$ plus postage and VAT. The Developer version costs $\mathbf{£ 2 9 9}$ plus postage and VAT. There is a 30-day money-back guarantee.

The Standard version interprets the programs that you write, which means that it looks at each line of the program as it reaches it while running, figures out what is required and then does it.

The Developer version produces programs that are compiled (by itself) while being written. Thus, when run, the system does not have to spend time interpreting each line as it occurs. This means that programs can run several times faster.

You would really need the Developer's version if you wanted to make freestanding programs, and it is also useful if you need MExpress programs to run as quickly as possible.

Note that to use the Developer version, you also need a " $\mathrm{C}++$ "' compiler, the Microsoft or Borland versions being the ones that are recommended.

The bottom line is that, unless you specifically want speed, or want to give your programs away, the Standard version will probably suit you.

## IN CONCLUSION

MExpress is a good product. To go back to the description "Technical Fourth Generation Language with Flexibility and Power," it seems possible to agree with most of it ; MExpress is undoubtedly a technically-orientated tool for engineers.

Whilst the meaning of "Fourth Generation" is, perhaps, ambiguous when it is applied to programming languages, in this case, it probably means that you can write programs which are very Windowslike in operation, down to event-driven buttons and sliders.

MExpress is probably as flexible as a programming language can be, with the added benefit of its mathematical abilities. It is unlikely you would write a word processor using MExpress, but you can construct design. simulation or analysis programs with this tool.

It provides similar facilities to other systems already in the marketplace, but its price makes it highly competitive, and the ability to make freestanding executable programs when using the Developer's version is a great asset.

The manual is well written and comprehensive - and no errors were spotted! It will not teach you to program, nor will it make you a mathematical genius, but it will tell you how to use the system in a comparatively painless way. There is also a useful set of examples and demonstration programs.

Future versions of MExpress will be able to make dynamic link libraries for use with Microsoft Windows. These "DLL" files are the key to how Windows applications actually integrate together, and this should make it even easier to integrate MExpress into existing programs.

Quickroute have also developed a tool


Fig.7. Demonstration of 3-dimensional data display.
to aid porting of Mallal, Level $\$ M$ files to MExpress, which will become available in beta-release form free from their WWW site in the near future.

MExpress is a highly cost effective software package that will enable you to perform complex data analysis and presentation functions, and to create custom design and analysis tools. If this is what you are looking for, it is strongly recommended that you examine it. It is also great fun to play with?

## MEXPRESS DEMOS

The screenshot in Fig. 7 shows MExpress in action from the demonstration package that is provided with it. It illustrates a graph containing multiple representations of the same data, in this case a matrix containing height information. Note also the buttons on the
screen, which activate functions in the program to perform the designated tasks.
Compiled versions of MExpress demonstration programs have been placed on the EPE FTP site: ftp://ftp.epemag.wimborne.co.uk/pub/ software/mexpress.zip
For more information about MExpress, contact Quickroute Systems Lid., Dept. EPE, Regent House. Heaton Lane, Stockport. SK4 IBS. Tel: 0161 476 0202. Fax: 01614760505.

E-mail: info@quicksys.demon.co.uk.
Web: http://www.quickroute.co.uk.

## THE REVIEWER

Rob Miles is a lecturer in the Department of Electronic Engineering, University of Hull. E-mail: r.s.miles@e-eng.hull.ac.uk. He thanks Dr. Tony Wilkinson for also assessing the software.

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John Becker addresses some of the general points readers have raised. Have you anything interesting to say? Drop us a line!

## PIC-TICK TIMING

## Dear EPE

I restore and repair clocks and recognise in your PIC-a-Tuner (May '97) a possibly economical method of timing clocks. However. to do this it would be necessary to read frequencies of IHz (as in the case of a grandfather clock). Can the software be modified to do this?
J. J. McClure, Omagh, N. Ireland

It could be modified if you are familiar with PIC programming (l can'l offer to do it for you). But, allhough it can be done, in all honesty, it would not be the ideal way to achieve what you want. PIC-a-Tuner is very accurate for musical tuning. but there is a wider degree of acceptable tuning range in music than there is in horology. A fraction of a Hertz difference in musical tuning accuracy is unlikely to be noticed, especially in the very low frequency range. Clock timing, though. requires far greater accuracy (as I know you know!). A fraction of a Hertz inaccuracy for a clock can mount up to several seconds of significant deviation over several months, or even weeks.
To tune a clock to the required accuracy using counting techniques, a digital readout of the time periods should be used, not an analogue bargraph as used in PIC-a-Tuner. You would then need to use a circuit which could count the number of "system" clock pulses occurring between each clock tick. and the greater the number. the greater the accuracy of the count.
Crystal controlled PICs can do the task, up to a count rate of. perhaps, at least ten times less than the crystal frequency (the control command instructions processed by the PIC to do the counting task slow down the sampling rate). It would be critical, however, that the circuit which detects the sound of each clock tick should rapidly respond to each tick and produce a pulse with a very fast leading (or trailing) edge. This edge needs to trigger the counter routine at identical points in the changing amplitude of the tick sound; not such an easy task for a comparatively slow changing audio source.

A more accurate pulse could be generated in the case of grandfather clock by using optoelectronics, the pendulum repeatedly swinging between a sharply focussed light source and a sensor, so creating sharply defined electrical pulses. Even that, though, could be subject to error due to the slowness of the pendulum; in this case an averaging technique would have to be used, taking a given number of ticks over a given period of time, and averaging the total microsecond (or even nanosecond) count assessed by the PIC. It's all more complex than PIC-a-Tuner is designed for.

There is, though, a non-counting technique that could be used, requiring a dual-trace oscilloscope. frequency generator, small amplifier and a microphone. Sel the frequency generator to exactly $/ \mathrm{Hz}$. Monitor its output on one scope channel, selting the sync trigger to this channel. Connect mic to amplifier. and the amplifier output to the scope's second channel. Set the scope sampling rate so that one pulse of the 1 Hz siggen signal can be clearly and stably seen. Arrange the mic and amp controls so that the clock ticks can be seen on the scope at a reasonable amplitude.

Observe at which point on the screen each lick pulse is being displayed (the pulse need not be particularly "clean" - as would be needed for a counting technique). At each tick. the pulse will change its position relative to the 1 Hz pulse trace. If the pulse appears to be moving to the left. the ticks are occurring too rapidly (the
clock is "fast"): if they move to the right. the clock is "slow". Adjust the clock's tick rate unil the tick pulse appears to be stationary. The clock is then accurately set. For clocks ticking at more than one pulse per second, set the sig-gen frequency accordingly (e.g. 2 Hz for two ticks per second).

JB

## RAILWAY SHORTS

## Dear EPE

I have taken EPE since it began, but mainly due to lack of time I find I never make the larger projects, plus the fact that I have found no use for them.
Because my other hobby of a Garden Railway takes much of my time. I would like simple projects like timers, flashers. light level controls, proximity switches, speed controllers for small motors, noise makers. temperature level controllers, and so on. Don't tell me I can get all these from books (and from back issues of EPE. JB). I do already if required, but I like to make different "simple" projects, Simple, in this sense, means fairly easy and quick to make and use in my situation. I do already buy your p.c.b.s and find this helps.

Eric Goodley. Doncaster
Between Jan '90 and Jun '97 we have published four railway-type projects (plus numerous related discussions in Interface). 25 timers, three flashers, 35 other lights projects, 18 switching designs. four motor controllers. countless noise makers, seven temperature controllers. and the "so ons" probably run into hundreds! These are, hopefully, what's helped to keep you with us over the years. Nice to hear from you.
On a personal note. let's hear from readers in favour of larger projects; all of us authorldesigners love designing, coming up with an idea and seeing how it can be achieved electronically (or just in sofiware on a PC). but for many of us it's the more complex design challenges we prefer - something to get teeth and brain cells into. Please. people, encourage us with some supporiive replies!

## BULLY FOR BULL (AND OTHERS)!

Dear EPE
Reading your piece on Bull Electrical (Innovations, Jun '97) has prompted me to tell you of a drill speed controller kit I bought from them. This was based on thyristor circuitry, which is "old hat" today, but this was in "62 or '63 and was fairly advanced for its time. What's more, it's still in perfect working order. Even older was the multimeter kit that I bought from Bull's (albeit under one of their former names). This was possibly ' 59 or ' 60 , but someone borrowed it and found it so useful that they forgot to retum it.

I was also one of Greenweld's first customers, giving them one of their first big orders. for about 100 "Talking Book" machines, and that was around 30 years ago.
My progress in electronics owes a lot to these two firms who have made it possible for those of limited means to get some hands-on experience of electronics which they could not otherwise afford. We should all be grateful to them.

Alan J, Gamble. Burscough. Lancs
Despite the (now retreating) recession. there are still a fair number of electronics companies whose history goes back in time and to whom many are grateful. Being EPE staff, I can't mention my own favourites, but we'll be pleased to hear about other readers' Company accolades. Many newer companies are also selling similar quality and service trends for the current generation.

JB

PIC OR CHOOSE?
Dear EPE
As an avid reader of EPE since 1987. I enjoy the projects and articles (although not being in the UK some flavour is lost for me). I have a suggestion/problem that might revive the constructional heydays:

I get the impression that the market in microcontrollers is fairly biased to the PIC and its derivatives. Seeing that the projects published (and the technology used) are largely beyond your control, why not generalise the part of the project involving the microprocessor? This will mean that the project can be built by, for example, an enthusiast of the 8051 and its derivatives, or any of the other curren microcontrollers available. More generic info in the article will assist the constructor to get the project going without having to relive all the development problems the original designe had!

## Louis Kirstein. Johannesburg, South Africa

A stunned intake of breath and astonishment from yours truly! (Half an hour later:) It's no the electronics that takes the design time in a microprocessor/controller based project, it's the software writing!

In a sense, any $\mu$ controlled project can be regarded as a generalised electronics base for which a variety of $\mu$ controllers could be used. But, and this is a big BUT, each $\mu$ controller type could well have different languageldialect requirements and interface facilities. There is no way that most readers could take an elec tronics base and a detailed program flow-chart and readily write their own software to suit the electronics presented in the magazine. Such projects have to be tailored to suit specific processors for which proven software is also available as part of the published article.

Even experienced programmers can have dif ficulty translating between languages: expecting readers to create their own software from flow charts would be unrealistic. It's not just the concept of the software which is important. it's the implementation through the commands available for a particular processor
As an example. I've been writing machine code for various projects and uprocessors for around 20 years. One of the projects (about 1989) was a bike computer based on a 6502 processor, plus Eprom. interface chip and separate memory: It might seem, therefore. that my PIC-Agoras Wheelie Meter, which I designed to replace it, would have required a mere translation from 6502 to PIC. Not so! The end products are largely identical in function. but because of the significantly differing command structures of the two processors, a total soflware rewrite was required. Apart from the fact that I knew what the design could and should do, it was as though the 6502 design had never existed
No, by their very nature, published $\mu \mathrm{C}$ projects must be designed for specific controllers.

Finally, we do have control over what designs we publish!

## ENGINEERING RECOGNITION (2)

Continuing to share with you the anonymous document circulating as multiple generation photocopies (Readout. June '97), another way to recognise engineers is by their attitudes towards gadgets:

- To the engineer, all matter in the universe can be placed into one of two categories:

1. things that need to be fixed, and
2. things that will need to be fixed after you've had a few minutes to play with them.
'Engineers like to solve problems. If there are no problems handily available, they will create their own problems. Normal people don't understand this concept; they believe that if it ain t broke, don't fix it. Engineers believe that if it ain't broke, it doesn't have enough features yet. To the engineer, the world is a toy box full of sub-optimised and feature-poor toys."

Mind you, an engineer is also likely to observe that life is too short to bother with mend ing things; why waste time on the obsolete when there's so much else that needs to be invented?

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## AUDIO FREQUENCY METER INTERFACE

INN RECENT Interface articles we have considered a number of test instruments based on a PC, and we continue in the same vein this month with an Audio Frequency Meter Interface.
This is a function that could probably be achieved largely in software, either by counting the number of pulses in a given period of time or by measuring the period of one cycle. Either way, it would probably require a machine code routine to handle things with adequate accuracy, but these are interesting approaches that I will investigate in the future.
The current design relies largely on hardware to do the work, with the computer providing the display and control logic. The interface is still quite simple, and it is designed to connect to a bidirectional parallel printer port. It should also work properly with any coniputer port that can provide eight input lines plus two handshake outputs and one handshake input.
However, the software will obviously need some adjustment for operation with a different input port. The 8 -bit resolution of the system means that it will not achieve the same degree of accuracy as an expensive digital frequency meter (d.f.m.), but it is still adequate for many purposes, and is at least as good as an analogue frequency meter.

The block diagram of Fig. 1 shows the general scheme of things used in this frequency meter. It operates on the normal pulse counting method with gate periods of 10 milliseconds, 100 milliseconds, and one second. In conjunction with an 8 -bit counter, these provide full scale frequencies of $25.5 \mathrm{kHz}, 2.55 \mathrm{kHz}$ and 255 Hz .
The gate periods are controlled by a


Fig. 1. The Audio Frequency Meter Interface block diagram.
monostable which has three switched resistors to provide the three gate times. The monostable is activated by way of a handshake output of the printer port. A trigger circuit is used ahead of the gate, and this provides a pulse output signal that is compatible with the logic circuitry regardless of the input level or waveform.

A minimum input level of about 150 millivolts r.m.s. is needed in order to operate the trigger circuit and produce a reading.
The counter is a 9-bit type, but only eight bits are used to provide readings. The ninth bit is read by a handshake input and is used to provide a warning to the user if the counter overruns.

A handshake output is used to control the reset input of the counter and this must be activated to clear the counter before a new reading is taken. The basic sequence of events when taking a reading is for the counter to be reset, the
monostable to be triggered, and then after a suitable delay the reading is taken.

The delay is provided in software and is a little over one second on all three ranges (i.e. slightly longer than the longest gate time). The ninth bit of the counter is checked repeatedly while the count is in progress, and a suitable warning produced if this bit is set.

## The Circuit

The circuit diagram in Fig. 2 shows the trigger, gate, and monostable stages of the Audio Frequency Meter Interface. Transistor TR1 is used as an emitter follower buffer stage which provides the unit with an input impedance of about 250 kilohms.

Capacitor C3 couples the output from the buffer stage to a simple trigger circuit based on transistors TR2 and TR3. This drives the signal gate which is one of the four 2 -input NOR gates in IC1, a



Fig.3. The circuit diagram for the counter stages. Only nine of the fourteen stages are actually used.

CMOS 4001 BE . The other three gates of this package are left unused.

A high control signal to pin 2 of IC1a blocks the output signal at pin 3, and a low control level enables the input signal to pass through to the output. The signal undergoes an inversion through ICla but this is of no practical consequence.

Component IC2 is a CMOS 4047BE astable/monostable, which in this circuit is connected to operate as a positive edge-triggered monostable. The duration of the output pulse is governed by capacitor C5 and whichever of the three presets (VR1 to VR3) is selected using switch S1.

The three presets provide the unit with its three measuring ranges, with the $255 \mathrm{~Hz}, 2.55 \mathrm{kHz}$, and 25.5 kHz ranges being provided by VR1 to VR3 respectively. Each preset must be adjusted for good accuracy on its range, and a source of calibration frequencies is therefore needed in order to set-up this project ready for use.

A crystal calibrator having low frequency output signals is ideal, but a calibrated audio signal generator or function generator should provide signals of adequate accuracy. Preferably, each calibration frequency should be more than 50 per cent of the full scale frequency for the range it will be used to calibrate.

The circuit diagram for the counter stages appears in Fig.3. Two CMOS 4024 BE 7 -stage binary counters are used in series to provide a 14-stage counter, but only the first nine stages are actually utilised.

The outputs of the first eight stages drive the data inputs of the computer's printer port via current limiting resistors R10 to R17. These resistors are needed because the data lines of the printer port will default to behave as outputs at switch-on, which results in two sets of outputs connected together.

These resistors are high enough in value to prevent damage to either set of outputs, but are low enough in value to ensure that they do not have a detrimental effect on the circuit's performance.

Bit 9 of the counter is read by the busy handshake input of the printer port and the reset input of the counter is driven by the initialise handshake output.

## The Software

The GW BASIC/QBASIC listing shown in Listing 1 provides a frequency readout in Hertz. It prints the range in use on the screen, and the required range is selected by pressing keys 1,2 , and 3 . The program is set to range 1 initially (the 255 Hz range).

The range must be set separately on the interface and on the computer, but it would be possible to use an extra pole on switch S 1 to indicate the range in use to the computer. This type of thing has been covered in previous Interface articles.

After some initial setting up, line 50 sets the data lines of the printer port as inputs (see Interface Feb. '97 for more information on bi-directional printer ports). The next three lines then generate the pulses which reset the counter and trigger the monostable.

A delay of about 1.2 seconds is then provided with the aid of the TIMER function, and this gives the hardware time to take a reading. At the same time, bit 9 of the counter is repeatedly checked by line 120, and the program exits the loop if an overload occurs and this bit is set.

The routine at line 360 onwards prints a warning message on the screen, provides a delay of 1.2 seconds, and then loops back to line 60 where a new reading is commenced.

If no overload is detected, the program checks to see if a range selection key has been pressed and, if an appropriate key

Listing 1: Audio Frequency Meter Program (GW BASIC/QBASIC)

```
5 REM FREQUENCY METER PROGRAM
6 REM REQUIRES PC WITH
    BI-DIRECTIONAL PRINTER PORT
10 CLS
20 LOCATE 7,30
30 PRINT "RANGE 1 "
\(M=1\)
    OUT \&H37A,32
    OUT \&H37A, 33
    OUT \& \(837 A, 37\)
    OUT \& \(\mathrm{H} 37 \mathrm{~A}, 33\)
    T! = TIMER
    \(T!=T!+1.2\)
10 X! = TIMER
120 IF \((\operatorname{INP}(\& H 379))<128\) THEN GOTO
    360
130 IF X! < T! GOTO 110
150 A\$ \(=\mid\) NKEY\$
160 IF AS \(=" 1 "\) THEN GOSUB 240
170 IF \(A \$=" 2\) " THEN GOSUB 280
180 IF AS \(=\) " 3 " THEN GOSUB 320
190 LOCATE 5,30
\(200 \mathrm{~F}=(\) INP \((\& H 378))\)
\(210 \mathrm{~F}=\mathrm{F}^{*} \mathrm{M}\)
220 PRINT F "HERTZ
230 GOTO 60
240 LOCATE 7,30
250 PRINT "RANGE 1"
\(260 \mathrm{M}=1\)
270 RETURN
280 LOCATE 7,30
290 PRINT "RANGE 2"
\(300 \mathrm{M}=10\)
310 RETURN
320 LOCATE 7,30
330 PRINT "RANGE 3
\(340 \mathrm{M}=100\)
350 RETURN
360 LOCATE 5,30
370 PRINT "OVERLOAD "
\(380 \mathrm{D}=1\)
390 T! = TIMER
\(400 \mathrm{~T}!=\mathrm{T}!+1.2\)
410 X! = TIMER
420 IF X! < T! GOTO 410
430 GOTO 60
```

has been pressed, the program goes into one of three subroutines that change the range message printed the on-screen.

The value assigned to variable " $\mathrm{M}^{\prime \prime}$ is also altered. After the range checking routine the counter is read, the returned value is multiplied by " $\mathrm{M}^{\prime}$ ", and the frequency in Hertz is then printed on the screen. The program then loops back to line 60 and starts taking a new reading.

A continuous stream of readings are therefore taken and displayed at approximately 1.2 second intervals. There is no built-in provision to break out of the program, but the usual Control-Break combination will bring the program to a halt.


Fig.4. Connection details for the printer port. The connections to the port are via a 25-way male D-type connector.

## Constructional Project

# MICROPOWER P/R DETECTOR 

## ANDY FLIND

## A three module integrated intruder deterrent. The PIR is a self-setting, stand-alone, battery powered module for boats, caravans, garages or garden sheds.

As Mientioned last month, the construction of an effective intruder alarm system requires more than just the $\mu$ PIR Detector. A means of adjusting sensitivity is often needed, together with a timer for setting the alarm duration when triggered. A facility for resetting this timer is usually necessary. and of course a power output stage is needed to operate a siren, flashing beacon or other warning device.

For use with the $\mu$ PIR Detector unit (last month) these control functions should use very little current so that a complete battery-operated system can be built for use in remote locations where mains power is not available. Fortunately. using CMOS components it is possible to construct a Control circuit that draws no current at all in the "standby" condition. Power is only required during operation following a signal from the Detector.


## pULSE COUNTING

An effective way to adjust the sensitivity of a PIR detector is to use a "pulse counting" technique. A simple PIR circuit such as that shown last month. responds to changes in the level of infra-red radiation. producing a series of pulses as the intruder's body moves around the detection area. These pulses are counted by the circuit and. should a sufficient number occur without a reasonably long interval between them, the alarm is triggered.

There are two main reasons why sensitivity may need to be reduced in this way. One is that an alarm that responds to a single pulse may be triggered by all sorts of spurious inputs. caused by animals, birds or even insects crossing its field of view. The other is that the installation may cover an area where, perhaps, the


Fig. 1. Circuit diagram for the Micropower PIR Controller.

IC3 to go low. When it does so, it holds the clock to IC2 low via diode DI, so that the circuit remains in the "operated" condition.

When the input is not active, transistor TRI is "off" and capacitor C2 is charged by resistor R5. It takes about seven seconds to reach a high enough value to reset IC2, so a gap of this length is required to restart the count or to reset the circuit once it has operated.

## OUTPUT SELECTION

Three outputs are available from this part of the circuit. IClc inverts the output from IC3 to give a Continuous Output which goes high on operation and remains high until IC2 is reset. This is combined with the input through NAND gate ICIb and passed through the time constant of capacitor C3 and resistor R6 to the inverting gate ICla, to generate a brief pulse at the Pulsed Output for each further input pulse from the detector after the counter has operated.

This "pulsed" output is the one that is normally used for triggering the timer to sound the alarm. A Direct Output that goes high for each pulse from the detector is also available for readers wishing to monitor the system or experiment with it.

## TIMER CIRCUIT

The output timer is built from the 4001 B quad NOR gate IC4, and the 4060 B counter, IC5, which has an internal oscillator. Gates IC4a and IC4b are connected to form a "set-reset" flip-flop.

A positive voltage applied to capacitor C5, either from ICla through diode D4 or from an external input connected to point $F$. results in a pulse at pin 6 of IC4b which sets it to the state where its output pin 4 is low and IC4a output pin 3 is high. The positive voltage from pin 3 turns on TR2, an inexpensive power MOSFET which can switch several amperes if the power supply can deliver it. via diode D5 and resistor R12.

All positive input pulses are also applied to input pin 8 of IC 4 c , resulting in negative pulses from this gate and posilive ones from IC4d which reset the counter to zero each time. When IC4c pin 8 returns to the low state with a low also set on pin 9, the Reset input to IC5 becomes low which allows its internal clock to run and counting commences.

If there are no further input pulses, following a period set by the values of the clock components $C 6$ and R11. the output from IC5 pin 3 goes high. This resets the flip-flop, which then turns off

TR2 and resets IC5 ready for further triggering.

A positive voltage applied to pin 1 of IC4a at any time will also reset the flipflop to turn off TR2. Holding this point positive prevents the circuit from operating to provide a "disarmed" condition.

An input, such as a switch, that is either positive or floating can be applied directly to input point $E$, but one that has an active low state such as a CMOS output must be connected through point $D$ so that diode D3 will allow resistor R9 to take pin 1 of IC4a high. Diode D3 also allows the "disarm" connections of several boards to be connected in parallel.

## USEFUL FEATURES

This timer circuit has some useful features that may not be immediately obvious. The timing period is easily set by changing the value of resistor R11. which gives about a minute for each 33 kilohms of resistance. Timing begins from the end of the last input pulse, so it will not "timeout'" whilst the intruder remains within range of an associated PIR detector.

The Disarm inputs always override the signal input so TR2 is always "off" whilst one of these is active. Capacitor C5 ensures that it will time-out if a fault
results in a permanently high input. In the "standby" condition it draws no current at all, so prolonged use from battery supplies is possible (unless it is triggered very frequently!)

Although the complete board can be used to make an effective alarm with a single PIR input. additional inputs are provided for building up a more complex system where this is required. Extra control board(s) are needed for this, but usually only part of the circuit will be needed for the additional features.
As triggering is effected by positive pulses through diode D4 with the "pulldown" resistor R7. multiple inputs can be connected so long as they consist of pulses applied through diodes to point $F$. For a second PIR detector only the counter would be required, and the output from D4 of this could be connected to this input to use the existing timer and power stage.

To add a second timer with a longer period, perhaps for use with a doorswitch or a glass-break detector, only the timer part of the circuit is required as the arrangement of D5 and R13 allows the second timer to use the output stage of the first, simply by connecting the points $G$ in parallel.

The circuit power supply is available from points $C$ and $H$ for use by external
circuits. although the current available is limited by resistor R14. Multiple Disarm connections can be connected to points $D$ or $E$. though this is not really advisable as one of them might inadvertently be left active.

## CONSTRUCTION

Despite the many features and the flexibility of this circuit, it is compact and simple to construct. As with the $\mu$ PIR Detector. care should be taken to avoid contamination of the p.c.b. from excessive handling as some of the impedances used are fairly high. This applies especially around components R5 and C2

Precautions to prevent damage by static electricity should also be observed as all the i.c.s are CMOS types. The use of d.i.l. sockets is advised for these.
All components for the $\mu$ PIR Control System are mounted on a single-sided printed circuit board (p.c.b.) and the component layout and full size copper foil master are shown in Fig.2. This board is available from the EPE PCB Service. code 163. The usual construction procedure of fitting resistors and diodes. followed by the smaller capacitors, then the i.c. sockets, di.i. switches and the electrolytic capacitor C 9 should be followed.


Fig.2. Printed circuit board component layout and full size copper foil track master pattern for the $\mu$ Power PIR controller.

The value of 33 kilohms (33k) shown for resistor R1I gives the timer a period of about a minute but this may easily be altered. For example. 330k will give about 10 minutes.
The switches S! to S 5 are described as "sub-miniature s.p.d.t. dual-in-line", and are available in banks of 1, 2 or 4 . For this circuit, a bank of four is used for SI to S4 and a single one is used for S5. Be warned. the minimum spacing between banks is 0.15 in . so two " 2 ' s " cannot be used to replace the " 4 ", they won't fit on the board!
Internally they consist of two switches physically coupled together and closing in opposite directions. In this design, the lower one of each pair is on and so "active" when moved towards IC2. Each switch is marked with two dots to indicate which is "on" for the current slider position.

It is. of course, possible to omit the switches and fit wire links instead, but this would reduce the ease of adjustment.

## COWPONEVITS

$\mu$ PIR Controller

| Resistors |  | See |
| :---: | :---: | :---: |
| R1, R13 | 10 k (2 off |  |
| R3, R4, R7 | 22k (3 off) |  |
| R R6, R8, | 10 M | Page |
| R11 | 33k (see |  |
| R12, R14 | 1008 (20) |  |
| Capacitors |  |  |
| C1, C3, to C7 | 100n resin ceramic | -dipped ( 6 off) |
| C2 | $1 \mu$ polyes | ter layer |
| C8 | $1000 \mu / 16$ | $\checkmark$ radial ctrolytic |

Semiconductors
D1 to D5 1 N4148 silicon signal diode (5 off)
D6, D7 1 1 4003 silicon rectifier diode (2 off)
TR1 BC184L non silicon transistor
TR2 BUZ11 power MOSFET
IC1 4093B CMOS quad Schmitt NAND gate
IC2 4024B CMOS 7-stage ripple counter
IC3 4068 BCMOS 8 -input NAND gate
IC4 4001B CMOS quad NOR gate
IC5 4060 B CMOS 14 -stage ripple counter with internal oscillator

Miscellaneous
S1 to S4 sub-miniature s.p.d.t. dual-in-line switch, p.c.b. mounting, quad
S5 sub-miniature s.p.d.t dual-in-line switch, p.c.b. mounting, single.
Printed circuit board available from EPE PCB Service, code 163; optional case, size and style to choice - see text; 14 -pin di.i.l. socket ( 4 off); 16 -pin di.I. socket; strip of aluminium for heatsink, see text; multistrand connecting wire; solder pins; solder etc.

Approx Cost
Cuidance Only
excluding case


Fig.3. Basic system connections.

In the prototype MOSFET TR2 was fitted with a small heatsink made from an Lshaped piece of aluminium. However, subsequent tests have shown that this is not really necessary as the "on" resistance of the BUZII is so low that even when passing a couple of amps it is unlikely to dissipate more than a couple of hundred milliwatts. Most sirens use much less than this, the one used by the prototype draws around 300 mA .

## TESTING

Providing construction is carried out with reasonable care, it is unlikely that problens. will be experienced with this circuit. However, if it does malfunction, a logic tester is the best instrument for investigating the circuit, though a meter could be used instead.

The pulse counter stages should be checked first by inserting ICI, IC2 and IC3 in their respective sockets and the circuit powered-up. It will operate from any voltage between 6 V and about 15 V , although it is really intended for use with a 12 V supply.

For testing, a microswitch can be connected between points $A$ and $B$, this being preferable to a pushbutton as it will normally have less contact "bounce". Switches S 1 to S5 can be set for a low count of, say, " 5 ". They have a binary weighting of 1,2,4,8 and 16. so "5" means d.i.I. switches SI and S 3 on. all others off.

Various points can be checked as the microswitch is operated. Each time it is closed the Direct Output should go high. On releasing it for the fifth time (if this value has been set) the Continuous Output should go high and remain high for about seven seconds.

The Pulsed Output may be difficult to see as the pulses are brief, but pin 4 of ICl can be easily observed. It should be high until the preset number of pulses is reached. then go low each time the input switch is released. Following the final pulse it will remain low until capacitor C3 charges sufficiently to reset IC2.

An additional check that may be useful is that if SI to S 5 are all "off". all the


Left to right: $\mu$ PIR Detector, Control Board and Disarm/Reset Board.
inputs to IC 3 should be high, so output pin 13 should be low and pin 10 of ICI should be high. If the input is then left open, 1 Cl pins 11,4 and 3 should be low. If the input is taken low, pins 11 and 4 should go high.

Testing of the Timer section can be carried out by inserting IC4 and IC5 and applying brief positive inputs to " $F^{\prime \prime}$ (Start) and ${ }^{\bullet} D{ }^{\bullet}$ (Stop). Once again, a logic tester is the best tool for locating problems around the Timer circuit.

Operation of the Clock in IC5 can be checked at pin 9 of this i.c. This oscillates at a relatively high frequency whilst timing is taking place. around 130 Hz for a one minute output period, and remains low at other times.

It should be remembered that when the timer is triggered by the pulse counter through diode D4, about seven seconds will have to elapse before the full count can take place again. Until this time has passed. if the timer is reset, it will be triggered by just one input pulse to the counter.

## CASE DETAILS

Like the $\mu$ PIR Detector, this board can be fitted into any case of the constructor's choice, though a fairly large one containing a pack of eight "C" or " $D$ " cells and perhaps even the alarm siren itself may be the hest arrangement. The less external wiring there is, the less chance of an intruder managing to disable the system by damaging it.

The siren used by the author is a "Miniature Piezo Siren" which takes 300 mA of supply current and is quite loud enough for most applications. Connections to make a simple and effective system are shown in Fig.3.

The Disarm switch could be concealed in some way, and may be an ordinary s.p.s.t. switch of any type or perhaps a reed switch. The only difficulty is making it inconspicuous enough whilst still accessible from outside the protected premises.

If it is inside, it may be difficult to unlock and enter before the pulse counter triggers the alarm. There is also the minor difficulty of resetting the system into the "Ready" state without setting it off whilst locking up and leaving.

Next month, the concluding article in this series will present a neat solution to these problems when we discuss the Disarm/Reset system.

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## IR Camera Shutter <br> Trigger

pictorially
NSPIRED by professionals" photographs of birds in flight. I decided to have a go myself and attempt to take dynamic photos of the same subject. The resultant circuit of Fig. I is an interrupted beam type of trigger design which operates the shutter of motordriven cameras at the appropriate moment to photograph the bird

The system comprises an infra-red transmitter and a receiver which drives the camera shutter mechanism for a short trigger period.

The transmitter is based around ICI, a 75.55 astable driving an IR emitter I.e.d. DI. It has a $10 \mu \mathrm{~s}$ "on time" and $200 \mu \mathrm{~s}$ "off time" at a frequency of about 4.8 kHz .

The receiver is formed around IC2. a Sharp ISIU60 IR remote control receiver (available from RS Components or Electromail. Code 577-897 - A.W.) in a 3 -pin housing. Received IR pulses appear at the output of 1 C 2 and. being negative-going. are rectified and smoothed by D2. R5 and C3.

The resulting d.c. voltage is then compared against a reference voltage of half the supply rail. derived by Rt and R7. A 7611 op-amp (IC3) acts as a comparator whose output is low whenever there is no signal
(i.e. when the IR beam is broken by the bird in flight).

In order to provide a consistent shutter triggering signal. the comparator's output is coupled by C5 5 IO IC4. a 7555 monostable which is triggered when IC3 output goes low. and which times for a period of about $30(0) \mathrm{ms}$.

The output of IC4 drives a solenoid (L.1). or alternatively fires the camera's remote release directly. via the MOSFET power transistor TR2. The solenoid must be mechanically linked to a remote manual shutter release. and may need to the adapted to suit.

When IC4 times out. a negative-going trigger signal is sent via C7 to IC.5. another


Fig. 1. Circuit diagram for an IR Camera Shutter Trigger.

7555 monostable with a period of approximately 50 seconds. IC5 drives transistor TRI and this holds IC4's reset pin at 0V. disabling it from timing in the interim period.

The purpose of this is to provide a delay timer which prevents any further operation of the shutter solenoid/trigger (and wasting film) when the flash gun is recharging; I.e.d. D3 illuminates to indicate that resetting is in progress. After 50 seconds or so, the system is ready to take another photo.
A split rail supply voltage is derived from a series of batteries, as also shown in Fig.I. A 12V 6Ah lead acid battery, B1, powers the flash units. A 5 V rail for the transmitter and receiver is provided by four 1.2 V 600 mA Ni Cad cells (B2 to B5) in series. These are also placed in series with B1 as shown, and the resulting 17 V d.c. supply was found to be sufficient to power the solenoid.
The circuit could, perhaps, be adapted with more ingenuity to provide a method of timelapse photography for other applications.

Stephen Browne,
Carrickfergus,
Northern Ireland.


Fig.2. Camper's Night-Light controller.

## Camper's Night-Light - for comfortable camping

G
olnc on a camping holiday with very young children often means some form of night-light is required. However, it's impractical to leave a lamp switched on throughout the night. and so I designed the circuit of Fig.2, which is a sound-operated night-light that gradually fades in brightness to save batteries.
This circuit will cause a low voltage bulb to shine at full brightness for approximately 12 minutes, and then cause it to gradually fade out. A dimming lamp is also less likely to wake a dozing child than being suddenly being plunged into total darkness. However,
the lamp can be switched on again by making a sound, such as a child's cry or the clicking of fingers - much more convenient than fumbling around for an on/off switch in the dark.
A miniature electret microphone. MICI. with integral f.e.t. amplifier, is used to monitor sound. Transistor TRI amplifies any sounds, and the relatively low values of coupling capacitors make the circuit quite insensitive to low frequencies. e.g. quiet talking after "lights out". but responsive to high-pitched sounds (a clicking of the fingers up to two metres away!).
The amplifier's output triggers ICI, a 555
monostable, with R4 and C3 providing a 12 minute delay. Via DI and R5, its output charges C4, turning on the transistors TR2 and TR3. This completes the circuit to LP1. a 6 V bulb, which illuminates.
When the 555 times out, C4 maintains the base current but slowly discharges through the transistors to 0 V and gradually dims the bulb. Dl prevents the capacitor from discharging back into the 555 output pin. The overall current consumption is approxinately 5 mA when the bulb is not illuminated.

Peter Bush,
Colchester, Essex.

## Car Electrics Tester - bryun of om oime

THE DESIGN shown in Fig. 3 was evolved to enable some basic tests to be made on a car's electrical system, producing an audio tone when in use. It has two basic functions:

1. With two test leads plugged into SK1 and SK3. a "go/no-go" test for the presence of voltage may be made, e.g. at the pins of a bulb holder or a fuseholder.
2. If SK1 and SK2 are used, a continuity tester is formed to aid the testing of bulbs, fuses, wiring, etc.

The circuit is simply an audio oscillator form around a 555 astable. its frequency set by R1, R2 and C1. The oscillator drives a small loudspeaker (LSI) having an impedance of 35 ohms or more. Four diodes. DI to D4, are used as a bridge rectifier on the input supply. making the circuit immune to reverse voltages.

All sockets are standard 4 mm types for use with ordinary test leads. Power is supplied by a miniature 12V "No.23A" cigarette lighter battery (Maplin JG9IY).

David Allen,
Chellenham, Glos.


Fig.3. Car Electrics Tester circuit.

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# THE GREAT EXPERIMENTER <br> <br> A short history = Part Four <br> <br> A short history = Part Four STEVE KNIGHT 

## Although poorly educated, Faraday began to solve the mysteries of magnetic induction and capacitance, laying firm foundations for future theoreticians.

TIIIS MONTH we turn to the man who was arguably the greatest experimental physicist of them all, Michael Faraday. Like many of those before him, and many of those contemporaneous with him, he came from a poor family and had a meagre upbringing along with three other children, of which he was the third.
His father, a Yorkshireman of frugal parentage. was a skilled blacksmith by trade having at that time what might be called an elementary education, followed by an apprenticeship. His mother was without any form of education, but what she lacked in literacy she made up for in love and care for her offspring so that, in spite of their poor circumstances, the children were as well looked after as their straitened situation allowed.


Michael Faraday (1791-1867)

So this was the atmosphere in which Michael Faraday spent his childhord and carly adolescence. He did not show any particularly ability for, or promise of a scientific career: he seemed just an average kind of lad. and as he himself said later: "My education at a common day-school was of the most ordinary kind. consisting of little more than the rudiments of reading. writing and arithmetic. My time otherwise was passed at home and in the streets"
The extraordinary thing about the achievements of his later career was that he knew practically no mathematics beyond the arithmetic he had picked up at the common dav-school. He once remarked that he was not a mathematician in any shape or form and had never made a mathematical calculation in his life. except for one occasion when he had cranked the handle of a primitive calculating machine.
11 may have been that his self-confessed ignorance of mathematic: made him the great practical experimenter that he became: we can only surmise. Experimenters there have been who. applying their mathematics for the meaning of a theory. have successfully reached their goal by the test of experiment to that theory: the works of Ampere and Ohm are examples of this.

Faraday. however. did not have this approach: he had an instinctive feel for what was a plain physical truth. and rarely did he fail to hit the nail squarely on the head. This innate ability he applied to the testing ground of his laboratory work. nearly always with remarkable success. The mathematical foundations of his discoveries were left to later workers. particularly James Clerk Maxwell of electro-magnetic wave theory, who produced a treatise on the electrical achievements of Faraday a few years after the latter died in 1867 .

## EARLY YEARS

When he was thirteen. Michael was apprenticed to a book seller and book binder. initially as an errand boy. This work brought him into contact with books and he wasted no time in studying some of these as they passed through his hands.
Among the books he read in this way were a number on chemistry and the section on Electricity which he discovered in the Encychopedial Britannica. This period sparked his interest in science.
In 1812, when he was twenty-one and completing his apprenticeship with the book binder, his slender resources prevented him from gaining more information and instruction than he could obtain simply by reading broks. He soon grew tired of the humdrum routine of the work he now found himself doing for a new employer, as most of us do at one time or another, particularly as he now had an ill-tempered boss and long hours.

At this stage he had the temerity (as it no doubt was) to write a letter to Sir Humphrey Davy of the miners' safety lamp fame. who had been seen by Michael at


Sir Humphrey Davy (1778-1829)
a series of lectures at the Royal Institution through the kindness of a bookshop customer who appreciated the embryonic physicist's enthusiasm for science. With his letter, Michael also included the notes he had made of Davy's lectures.

Davy showed some immediate interest in the letter's appeal and the ability that Michael had displayed in the taking of the lecture notes and the observations he had appended to them. He (Davy) asked one of the former managers of the London Institution (who was a descendent of the great diarist, Samuel Pepys) what he should do about Faraday's appeal.
"What should you do?", replied Pepys. "Put him to wash bottles and keep the laboratory tidy. If he is good for anything he will do these jobs straightaway: if he declines he is good for nothing".

But Davy felt that Michael deserved something better than being a mere handyman, and he sent Faraday a letter explaining that he would do the best he could to find him a worthwhile job somewhere within the scientific establishment.

## LUCKY APPDINTMENT

A particular piece of luck now came Faraday"s way. The position of a laboratory assistance happened to fall vacant shortly after, and at the Royal Institution, to boot. Davy put Faraday's name forward for the position and as a result Michael was appointed to the job at a meeting of the managers of the Royal Institution held on the first day of March, 1813. So Faraday began his famous career.

His main work at the early stages of his new employment was to assist the lecturers and to maintain the apparatus in good condition, which probably included the washing of a few bottles! Faraday quickly established himself to the full satisfaction of the Institution members, and it was not long before he was assisting with minor experiments, initially in chemistry, and in acting as a note copyist for Davy himself.

When Davy set out in October 1813 on an eighteen month European tour, he invited Faraday to accompany him as a general assistant: this tour gave Faraday an irregular but valuable scientific education, together with the privilege of meeting many of the great names of European physicists who were in later years to become his own close and admiring friends.

Over the next ten years, Faraday established himself as a physicist of a very high order, performing experiments on the alloys of steel (of which "he was accustomed in later years to present his friends with razors made from them''), followed by researches into the liquefaction of gases.

He was made a Fellow of the Royal Society, and in 1825 was promoted to the position of Director of the Laboratories. One of his first acts in this new capacity was to organise Friday evening meetings of those members who were interested in science, and these meetings soon developed into those weekly discourses which became a hallmark in the activities of the Institution, and do so to this day as television entertainment round about Christmas time.

Faraday married a Miss Sarah Barnard in 1821, in a ceremony which was, by his own wish, a quiet and simple affair. So he just married the lady and took her home to his
rooms at the Royal Institution. It turned out to be a long and happy union.

## ELECTRO-DYNAMICS

Out of all the literature Faraday produced over the period we have covered above, his greatest work was the series of "Experimental Researches into Electricity". where he described his many discoveries, among which were the principles of electromagnetic induction in 1831, the laws of electrolysis in 1833 and the effect of a magnetic field on the rotation of polarized light in 1845.

Our main interest in this short series will lie in the first of these headings, though we will first have a look at an experiment in electro-dynamics which is the fundamental feature of the electric motor.

The study of electro-dynamics is, along with his work on electromagnetic induction, accepted as being Faraday's greatest contribution to electrical science. We have already looked at the work of Christian Oersted in which the magnetic effect of an electric current was discovered, so leading to the knowledge that there was a close relationship between these two phenomena: and to that of Andre Ampere's extension of the Oersted effect, culminating in the discovery of the electromagnet.

Faraday, from about 1830. effectually took over the study of electromagnetism from Ampere and was successful in making an experiment which had previously been attempted, without success, by a colleague at the Royal Institution and which foreshadowed the electric motor.

Oersted's experiments had demonstrated that a magnetic needle in the vicinity of a current-carrying conductor tends to move so that it is at right-angles to the conductor. This, as we now know, is due to the fact that an isolated north pole tends to move along the "lines of force" (as Faraday visualized in his field theory of magnetism) of a magnetic field in which the pole is placed. This is illustrated in Fig. I.


Fig.1. The movement of an isolated $N$ pole at points such as $P_{1} P_{2}$ etc. will be along a line of magnetic force.

It is important to realise here that the lines of force do not have an identity as such; they do not "flow" in any way such as an electric current (of electrons) tlows in a conducting medium. It is simply that an isolated pole (if we could get one) would move along a particular route to the oppositely signed pole.
It was general knowledge at the time, and Faraday was aware of it, that the magnetic field surrounding a current-carrying conductor took the form of concentric circles; you have probably illustrated this for yourself by arranging a wire to be vertical and passing through the centre of a horizontal piece of card. see Fig. 2.


Fig.2. The magnetic field lines surrounding a current-carrying conductor.

Then if iron filings are sprinkled on the card and the edges gently tapped, the filings will arrange themselves in concentric circles which indicate the direction in which the magnetic force is acting at any point. An isolated pole, placed in this field, would move around one of the circles if free to do so.

Now Faraday argued that since an action and a reaction are equal and opposite, then if an isolated pole tends to move around a current-carrying wire, there must be a tendency for the wire to rotate around a magnetic pole: it was simply a matter of relative motion. depending upon which element was free to move and which was fixed and unable to move. Faraday's experiment to see if his argument was sound is shown in Fig.3.


Fig.3. Faraday's experiment on the mutual rotation of a wire carrying current and a magnet.

He arranged a container of mercury $A$ so that it formed a part of an electric circuit made up of battery $B$ and the connecting wires. A magnet $M$ was fixed centrally and vertical in the mercury container, and a portion of the wire, $a-b$, forming the circuit was pivoted at $a$ so that it was free to move in any direction about the pivot point. Hence the apparatus formed a practical test of the surmised rotation of a free conductor $a-b$ round a fixed pole which existed at the upper end of the magnet and acted effectually as an isolated pole.

The experiment was a complete success and a story goes that on seeing his apparatus work, Faraday danced about in childlike excitement exclaiming "There it goes. there it goes". He later repeated the experiment with the wire fixed and the magnet moveable, with similar results.


## Faraday's rotation apparatus.

## ELECTROMAGNETIC INDUCTION

Faraday's most far reaching discovery happened in about 1831 when he demonstrated that electricity could be produced from magnetism; we know this effect today as electromagnetic induction.

Although the American physicist Joseph Henry discovered electrical induction independently of Michael Faraday and constructed the first electromagnetic motor, the achievements of Faraday are in no way diminished. The unit of inductance, the Henry, was named after the American.

What was the general nature of the investigation as Faraday would most probably consider it? From the work of earlier experimenters he would know that a currentcarrying wire is surrounded by a magnetic field; he had already made use of this fact in the construction of his electric "motor". Additionally, he knew that Ampere had shown that two conductors placed side by side and each carrying a current, would attract or repel one another. Why should it not be possible, then, for a current of electricity to set up (or induce) a current in another conductor brought into its vicinity?

What Faraday was in effect asking was why it should not be possible to produce a current from magnetism, seeing that Oersted and Ampere had demonstrated the opposite effect.

At the beginning of his research into this possibility, he arranged two wires in close proximity to each other, as Fig. 4 shows; one wire he connected to a battery $B$ so that this


Fig.4. Faraday's first attempt at producing an induced current.
first wire carried a current, and the other he connected to a galvanometer $(G)$ so that he could detect the presence of a current llowing in this second wire.

He obtained no result in spite of a number of changes in the disposition of the wires and the strength of the current. He abandoned this line of enquiry for a number of years because much of his time was then taken up with research into the properties of optical glass: when he returned to the problem again in 1828 he still failed to get any sort of result. It was a classical case of being so near and yet so far.

It was not until 1831 that Faraday met with success, and what followed was probably the most outstanding achievement of his entire career. We recall that Ampere had shown that a solenoid carrying a current behaved exactly as a bar magnet, and that if a rod of iron was placed in the solenoid, the rod became magnetized.

Building on this foundation, Faraday made an important change in the form that the solenoid took. He obtained a soft iron ring or toroid, about 150 mm in diameter, and around each half of the ring he wound coils $P$ and $Q$, as Fig. 5 depicts.

Coil $P$ was connected to a battery $B$ and switch $S$ : coil $Q$ was connected to gal-


Fig.5. The experiment from which Faraday discovered electro-magnetic induction.
vanometer (i. On pressing the switch, Faraday observed that the galvanometer needle swung completely around a number of times before coming to rest in its initial position. On releasing the switch, the deflection of the needle was repeated but in the opposite direction.

Although what he had actually done was to invent the toroidal transformer, we must forgive Faraday for not immediately appreciating the significance of what his experiment was saying to him; it appeared that the magnetism set up by the battery current in coil $P$ was the cause of the momentary current which appeared in coil $Q$, but why did it flow only at the moments when the current in coil $P$ was started or stopped?

He wrote in his notes "I think I have got hold of a good thing, but cannot say. After all my labours it may be a weed that I pull up instead of a fish".
To verify that it was indeed the magnetism of the iron core which produced the electricity, Faraday repeated the experiment using a non-magnetic material in-place of the iron. The galvanometer deflection again occurred but the movement of the needle was greatly reduced. This satisfied him that the magnetization was the responsible agent and that the greater the magnetic field strength, the greater was the induced current. and consequently the magnetism produced the electricity.

He wrote about this experiment: "When the contact was made, there was a sudden and very slight effect at the galvanometer and there was also a similar slight effect when the contact with the battery was broken. But while the voltaic current was continually passing through the one helix. no galvanometer appearance nor any effect like induction upon the other helix could be perceived, although the active power of the battery was very great".

Faraday now did a number of experiments replacing the magnetism that he had so far derived from a battery supply of



Fig.6. A further demonstration of electro-magnetic induction.
current with a permanent magnet. In one of these he wound a coil around a short rod of soft iron and connected its ends to a galvanometer as we see in Fig.6.
He then took two bar magnets and placed them so that opposite poles were in contact with the ends of the iron rod. When the contacting ends of the magnets were brought together (or separated) at point A. the galvanometer needle was deflected, the effect being as in the earlier experiments. not permanent but a momentary "kick" each time the bar magnets were separated or brought together.
From this, Faraday had thus demonstrated the converse of Oersted's effect: Oersted had found that a magnet was influenced by an electric current; Faraday that an electric current could be generated by a magnet.
In another experiment, Faraday plunged a bar magnet directly into a solenoid and found that the galvanometer deflected as the magnet entered the coil and again when he withdrew it, but again there was no deflection when the magnet was stationary.
It now became clear to him that the vital factor for the production of an induced current in any given conductor was that there had to be a relative change of conditions between the inducing field and the current generating circuit and that any change in conditions followed from a movement of one part relative to another.
This fact had to follow. of course, from the principle of the conservation of energy. No induced current could be obtained from a stationary field or a stationary magnet; it doesn't matter whether the magnet moves relative to the coil, or the coil to the magnet. If things were otherwise and an electric current (and hence electrical energy) could be generated by simply placing a magnet at the centre of a solenoid, then sitting back to enjoy the free output, all the trials and tribulations of the "perpetual motion" addicts would be over, and electricity bills would have never been invented.
To get energy out of a system (in the case of the induced current here) energy has to be supplied to the system. This supplied energy comes from the work done in establisting the exciting field or from the muscle power in pushing a magnet into or pulling it out of a solenoid. We do not get any "free" energy.

## LENZ'S LAW

A Russian born physicist, Heinrich Lenz. besides being credited with the effect of temperature on electrical resistance, supplemented in 1834 the work of Faraday by pointing out what is now known as Lenz's Law: that is, in every case of induced
current flow due to a relative change in the magnetic conditions. the direction of the induced flow is always such that it tends to oppose the change in the conditions which are bringing it about.

When we consider Lenz's Law, we have to realise that what is actually induced in a solenoid directly or in a winding adjacent to the field (what we now call the "secondary" winding in transformer action) by the change in conditions somewhere in the system, is an e.m.f. (electromotive force); this e.m.f. causes a current to flow in the winding if the circuit is completed through a galvanometer or whatever.

Lenz's Law therefore applies only to induced currents and these can only flow in a closed circuit. If you push a magnet into a solenoid, you have to provide a little more muscle power when a galvanometer is connected to the solenoid than you do when the solenoid is unconnected.

Faraday also discovered for himself how to determine the directions of induced voltages and currents, but he did not express his results so neally as Lenz.

## FARADAY'S LAW

Faraday had the insight to see that the induced e.m.f. appears no matter whether the change in the magnetic field strength is provided by moving the coil or by moving the magnet relative to each other. Although much of the required mathematical treatment of Faraday's work was carried out by Clerk Maxwell. the resulting law is known as Faraday's Law of Magnetic Induction. It can be expressed as:

## Induced e.m.f.

=change in the field density (or flux) time the change takes place
$=$ the rate of change of the field density
So the faster a magnet is plunged into a coil, for instance, the greater is the induced e.m.f.

A negative sign is usually given to this expression as a consequence of Lenz's law which indicates that the induced e.m.f. acts against the direction of the inducing agent.

## FURTHER RESEARCHES

Not all of Faraday's achievements were associated with electromagnetism. After some years investigating electrolysis and the analysis of compounds by use of an electric current (with which many of us are no doubt familiar), he turned to electrostatics and demonstrated that the electricity derived from friction (on various machines) was identical with that obtained from the voltaic cell. and from this he turned his attention to electrostatic induction.
Charles Coulomb, the French physicist. had in 1777 shown that Newton's law of inverse squares for gravitation was as true for electric and magnetic attractions and repulsions, something that Ampere also demonstrated for the force acting between parallel wires. Coulomb had assumed from his experiments that the force acting between two charges was also, as Newton believed for gravity, a case of "action at a distance" and that the intervening medium had no effect on this.
Faraday did not see eye to eye with this argument, saying that the medium


Fig.7. Electrical force field between oppositely signed charges.
between the charges (which he called the "dielectric") did have an influence on the force. Action at a distance implied that the mutual attraction between charges took place along a straight line, but his own experiments had shown him that the force lines were curved, after the fashion of Fig.7.

This sketch reminds us of the field around a bar magnet where charges are replaced by poles, but with the important difference that the electrostatic lines of force terminate on the surfaces of charged bodies whereas magnetic lines form closed loops. Electric force is taken to act from the positive charge to the negative, so that if an isolated positive charge is placed in the field it will tend to move towards the negative charge.

Faraday experimented with concentric metal spheres and parallel plates in search of a verification of his suggestion about the effect of the dielectric medium on the force between electric charges, and of the mechanism of the charge induced in an insulated uncharged body by the presence of a near-by charged body.

In his own words: "The question may be given thus: suppose an electrified plate of metal $(A)$ is suspended in air, and $B$ and $C$ two exactly similar plates are placed parallel to and on each side of $A$ at equal distance and insulated. $A$ will then induce equally towards $B$ and $C$, that is, equal charges will appear on these plates". His experiment is illustrated in Fig.8.


Fig.8. Faraday's capacitor experiment.
The induced charges appear on $B$ and $C$ because the lines of force from $A$ terminate on their surfaces, in just the same way as a magnet will induce magnetism in a near-by piece of iron. Faraday goes on: "If in this position of the plates some other dielectric than air, such as shellac, be introduced beiween $A$ and $C$, will the induction between them remain the same?"'
He answered this question by
constructing two identical plate arrangements (what we now call a capacitor), in one of which he placed a dielectric, the other containing air only, as shown in Fig.9. When both systems were charged by the same potential difference, Faraday found by experiment that the charge on the one containing the dielectric was greater than that on the other, that is, the field between the plates was increased by a factor $k(k>1)$ by the introduction of a dielectric.

In this way Faraday discovered the property which he termed the "dielectric constant" of the medium and which we now call the permittivity; the ability of a conductor to hold a charge is increased by the presence of a solid dielectric, hence it exhibits a greater "capacity". So did Faraday's work lead us to the capacitor.

Faraday died on August 25th 1867 at the age of 76. John Tyndall, who was the friend and biographer of the great experimenter, in his book Faraday as a Discoverer said of him: "Underneath his gentleness was the heat of a volcano. He was a man of excitable and fiery nature; but through high discipline he had converted the fire into a


Fig.9. The effect of a solid dielectric is to increase the electric field by a factor $k$.
central glow and motive power of life, instead of permitting it to waste itself in useless passion."

Faraday is remembered in the unit of capacitance, the Farad. You will find his portrait on the back of a $£ 20$ note.

## PART FIVE

In next month's concluding article. we
cok at the work of William Thomson whose pioneering rescarches led to the advent of wireless telegraphy.

## ACKNOWLEDGEMENT

The illustrations used in this article
have been kindly supplied by
The Science Museum. London

# Ohm Sweet Ohm 

## Max Fidling

## Mouse Trouble

Flicking through some of the many colour prints which were on sale in a local pictureframing gallery, up popped - lo and behold - "Mouse Trouble" - a cartoon print from the 1944 Tom \& Jerry movie of the same name! This immediately struck a chord with my latest death-defying attempts at electronic maintenance, back at the shack.

You see, readers, over the last few months, far from retiring into my workshop never to be heard of again. I have in fact been tussling with technology. What with Pentiums, RAMs, ROMs, floppies, mice, software, hardware, firmware. liveware, and all that jazz, I fear I have joined the ranks of my babbling computer friends.

Even the most resistant of my friends seem to have got this computer bug too, so eventually I had no choice but to relent and purchase a personal computer. So far, the main benefit has been that I can now print the Boss's shopping lists at 600 dots per inch, in a variety of those typeface things. Somehow, the list is longer, too.

## Royal Command

The past few months have seen intense activity Chez Fidling because it isn't easy to get to grips with these things if all you've ever known has been a 1947 "Royal" typewriter with sticking letters (or missing altogether the letter " P " flew off one day) and a ribbon which was permanently stuck at half mast. printing half red, half black as a consequence.

Always one to keep up with the elite, however. the Boss had decided that the Fidlings would join the computer age, and all I had to do was pay for it. The dining room table was thus buried under half a hundredweight of menacing machinery.

Aided by my know-all computer-literate

10-year-old nephew (swine). I had persevered. and eventually 1 managed to make progress with this word-processing malarkey.

At my command. the printer churned out all sorts of colourful invitations, adverts for the local Women's Institute meetings, not to mention more stuff for the local vicar and his fund-raising activities, notably the Reverend's awe-inspiring Church Bells Mobile Disco. Great things. computers.

## Rogue Rodent

At least they were until one day, the mouse started to behave in a most unrodent-like way. Try as I might. I could not get the pointer to move on the television screen. It was stuck firmly at the bottom. No amount of cajoling, muttering, threatening it with the cat or anything would free the darn thing. The pointer would not point.

There was nothing in the "Help" screens about Pointers. Sticking. There was nothing for it. I would have to operate on the deceased mouse using my trusty multi-purpose colour coded screwdriver set. which was always poised above the bench in the shack. ready for action. So Piddles the cat and I skittered down the garden path and into the workshop. accompanied by the said Sticking Mouse.

On the underside of the dead mouse were some screws - nothing high-tech about that. I thought! - and so these were off in a twirl. Typically, one of them rolled onto the floor and under the bench never to be seen again. but there was nothing wrong that a big self-tapping screw wouldn't cure. I told the cat. as I proceeded to dissect the electronic. decidely-dead mouse.

The fault soon became apparent. because the innards were glued solid with what resembled dried marmalade, probably a souvenir from the breakfast table! A quick polish with a soggy rag soon removed the desiccated condiment.


## Blasted Mouse!

This left me with the problem of how to dry out the soggy mouse. as 1 twirled the rotating innards freely. waiting for inspiration. No problem! I told Piddles. who was munching a rubbery marshmallow (our favourite candy) on the floor. I spotted my can of Air Duster, which was used mainly for knocking bluebottles and wasps senseless and splatting them on the window during these summer months.

1 grabbed the can. and gave the mouse a quick blast. The innards rotated gratifyingly. as I squirted the opto-isolator slotted wheels a bit more. They whizzed round at a dizzying speed, though Piddles, who'd seen it all before. was duly unimpressed. One more blast for luck. I thought. and I jabbed the button hard down!

This produced an awe-inspiring jet of gas of supersonic proportions which obviously caught the electronic rodent unawares - as it blasted the mouse clean out of my hand and smacked it against the wall behind the bench!

The mouse fell to the bench with a clatter. looking decidedly the worse for wear. Holding it up by its cable. I tutted as I picked over its broken circuit board with my screwdriver. So much for computer technology! Now. where's my hot melt glue gun and looW soldering iron?


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Sottware programs for the EPE projects marked above with an asterisk (*) are available altogether on a single 3.5 inch PC-compatible disk, or as needed via our Internet site. The same disk also contains the following additional software: Simple PIC16C84 Programmer (Feb '96). The disk (order as "PIC-disk") is available from the EPE PCB Service at $£ 2.75$ (UK) to cover our admin costs (the software it self is free). Overseas $£ 3.35$ surface mail. £4.35 airmail. Alterna tively, the files can be downloaded free from our Internet FTP site ftp://ftp.epemag.wimborne.co.uk


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## SURFING THE INTERNET

 NET WORK
## ALAN WINSTANLEY

## FTP Et AI!

EPE Nel Work is our monthly column which updates readers having Intemet access with the latest news about our World Wide Web site (http://www.epemag.wimborne.co.uk) and our FTP site (ftp://ftp.epemag.wimborne.co.uk). This month's PIC-olo project is at ftp://ftp.epemag.wimborne.co.uk/pub/PICS/PIColo.

Regular readers will recall last month's large addition to our site in the form of the fully fledged demonstration version of the CD-ROM Parts Gallery + Electronic Circuits \& Components - An Introduction.

Follow the link on our Home Page, or FTP (file transfer protocol) it directly from our FTP site at /pub/software/eccdemo.exe $(4.2 \mathrm{Mb})$. The demo will be there until the end of August, after which you can fetch it from the site of Matrix Multimedia (http://www.matrixmultimedia.co.uk).

With large FTP transfers like this, I prefer the full control of "proper" FTP software as found in my favourite Procomm for Windows. The transfer is intelligently handled, in that you can specify a variety of configuration options to cope with any crashes which happen during the transfer.

If, for example, the line drops during an FTP download using Netscape, you have to start again from scratch and overwrite the existing file: however, decent FTP software will enable you to camy on where you left off, instead.

The Nel Work page is the most popular of all our web site pages and something new is the Net Work A-Z Inder (../netwkaz.htm) of all the Net Worked links I've provided in the past year. Through the miracle of Windows Cut and Paste, all the URLs to which I've linked are now also available in strict alphabetical order - enough to keep even the most ardent surfer occupied for ages! Remember that I welcome details of any URLs you find interesting yourself.

## Junk, and More Junk

You know the feeling as a large window envelope crashes onto your door mat - six lucky numbers visible through an opening, and your name in big letters? Yes, "junk" mail has been a fact of life for many decades, as a myriad of paper passes over your breakfast table en route to the waste paper bin.

Mailing lists are now much more refined and "intelligent" these days. though, and in the UK, one way of cutting down on the latest offers from the Reader's Digest and others is to contact the Mailing Preference Service (Freepost 22. London WIE 7E7, incidentally).

The service is there more to stop direct mailers from wasting their resources sending unwanted mail, rather than trying to preserve your sanity.

While we're on the subject, junk phone calls are also a bane, so try the Telephone Preference Service, 6 Reef House, Plantation Wharf, London. SWII 3UF. Both services will help reduce unsolicited contact from firms you've never dealt with.

If only such a free service were available for Intemet users! An interesting comparison: up to 75 per cent of my inconing E-mail at any one time can be unsolicited junk mail. However, my colleagues back at HQ in Wimbome never receive any at all! How so, I wonder? Well. the answer seems to be Usenet or New's.

There are some $25,(000$ newsgroups available to anyone having the software (an off-line newsreader) capable of "subscribing" to them. News, in spite of its name, has nothing to do with curtent affairs, bulletins or news tlashes. Each newsgroup is a topical chat area and in theory relates to one particular subject which is defined in the newsgroup's charter.

When you think about the millions of E-mail messages openly circulating within newsgroups (and news archives), each message contains something which is a prized commodity in the junkmailing community - your active E-mail address! Your address is contained in the header of the message, and it may be in the signature (.sig) too. Usenet is scoured by "bots" which are specially trained to sniff out E-mail addresses in news messages circulating the globe.

Junk mail then stats to drift in from nowhere (nowhere.com, anyway) - often along the subject lines of "Earn $\$ \$ \$ \$$ ! Not MLM!'" (Multi Level Marketing): "Teach Your Baby To Swim with Our New Video!’: "Sell Cosmetics from home!'": "Cut $\$ \$ \$$ from your AT\&T Phone Bill!'": ‘Hi! End all your money worries instantly!! ’; and it goes on.

Some Intemet Service Providers will suspend the account of any customers who send UCE - Unsolicited Commercial E-mail or bulk E-mail - like this, so the so-called "spammers" who "spam" this garbage across the planet hide behind strange E-mail addresses such as anvbody@nowhere.com.

It is an American menace, it can be intensely annoying when you're busy dealing with your E-mail, it is likely to get worse, and it costs you connection time and money.

Replying with an expression of your annoyance may be fruitless because of the dummy E-mail address set up by the spammer. In fact, if anything, it just proves that they ve hit the target and that your address is indeed active!

The same may be said if you send the standard "Remove" (me from your mailing list) message: you're likely to attract even more junk. The most irritating junk mail is probably that which has a meaningless but intriguing subject - such as "Hi!"" or "Hello again!". Some E-mail has been received which is of a deeply offensive and disturbing nature. but this is, thank fully, extremely rare.

Junk mail presently defeats the most refined of kill-file rules, so it's seemingly impossible to block it. though I did notice quite a reduction when I stopped posting to Usenet for a time. Interestingly, Usenet users are increasingly fighting back by forging their own E-mail addresses too: it is not uncommon these days to see a posting by, say, bob@no.spam.mycompany.com, thus frustrating any attempt for a mail-bot to send him some junk mail.

Human beings know to remove the "no.spam" from the address if replying by E-mail. Unfortunately, my own version of this ruse is currently unprintable.

## Hot Links

Here are this month"s selection of notable links - and ready-made for you to click on the Net Work page of our web site. Do share any worthwhile electronics sites with us and you'll receive a mention on our web pages.

To tie in with this month's Circuil Surgery, don't forget to visit the excellent Varta Batteries site at http://www.varta.com. More battery-related info is at Duracell New Products \& Technology Division http://www.duracellnpt.com/techref.d and http://www.car-go.com (Car-Go Inc.)

An on-line US version of the Mailing Preference Service anti-junk E-mail site - which costs US $\$ 19.95$ to subscribe to - is maintained by Donna Troy Enterprises at http://www.spnt.com/~d_troy/page2.html.

E-Lab Digital Engineering Inc. manufacture several highly-functional i.c.s. (they say here) for use by the "electronics design community". More news on http://www.netins.net/showcase/elab, worth checking for the Java applet moving l.e.d. display alone!

Information on educational software for teaching electricity and electronics (at $\$ 395$ per licence), with downloadable demos, can be checked out at http://etcai.pair.com. A comprehensive Emulator Page for Spectrum. Commodore C64. BBC. Atari 2600. NES. Sega etc. is at Legion's Emulator Page on http://www.tcp.co.uk/~legion/emul.htm whilst an Amiga emulator page is also under construction at Crasher's Home Page http://www.tcp.co.uk/~marios.

Sinclair's name pops up again: to find out what a Sinclair ZX80/81 looked like and how to build one out of standard components, you just must go to the ZX80/81 Hardware Page at http://www.babytalk.demon.co.uk/zx80/zx80.html.

Join me next month for more Net Work. My E-mail address is alan@epemag.demon.co.uk. My Hoine Page is http://ourworld.compuserve.com/homepages/alan_winstanley.


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