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VOL. 12 NO. 9 SEPTEMBER 1983


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Mere are all the girls? This was one question raised at the SEDAC '83 Presentation Ceremony last June. For although two school-girls were amongst the students of the twelve-finalist teams, no girls' school, as such, had entered this National Competition for schools sponsored by Mullard and this magazine.

Handling a soldering iron and pliers might be considered an inappropriate pursuit for girls-until one remembers their older sisters who in their thousands are employed in industry assembling electronic equipments.
Perhaps the computer has more appeal to the younger members of the fair sex? But here we discover an equally depressing situation. Girls, it is proved by national statistics, are not so eager as boys to get their fingers onto computer keyboards and four times as many boys as girls take A-level computer science. Strange again, when one considers the similarity between typewriters and computers from the users stand-point, and the overlapping that already occurs in the business office where secretaries and typists are becoming involved quite naturally with word processors and even microcomputers.

This lack of interest in computer science by girls is indeed serious and it is worrying national bodies such as the Engineering Council, the Equal Opportunities Commission and the Microelectronics Education Programme. Naturally there are exceptions to the rule, and we are pleased to show students of one London girls' school carrying out experiments with a computer, see Computer Aided Experiments, page 600.

Electronics and computers represent the two major growth areas in our economy-and significantly both areas are healthy despite the impoverished state of many older industries. The message for the future is there-but somehow it is not being received, or at any rate acted upon, by all those entrusted with the education of our children. It's the old story: the humanities all too often take precedence over the sciences. Many SEDAC finalists told us that the initiative to enter the Competition came from themselves and much (sometimes all) of the project work was done outside school, the school authorities taking a belated interest only when their students had won through to the final stage.

We do not have to accept passively this situation. Self-help is after all a virtue strongly advocated by our government, and it can be applied very effectively here. Wise and responsible parents do counsel their children in career matters and those who follow electronics, whether professionally or as a hobby, will appreciate the opportunities arising from electronics in general and microcomputers in particular. A home study course in electronics could initiate a youngster into the "mysteries" of the subject and infuse genuine interest leading to more extensive study.

Our twelve-part series Teach-In 84 will meet this need very satisfactorily. We commend it to all parents, and of course to the younger people themselves. Teach-In 84 starts next month. It could make a lot of difference to the job prospects of lots of young people due to enter the labour market in the next few years.


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We cannot undertake to engage in discussions on the telephone.

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THE Stylus Organ is basically a conventional instrument which covers two octaves (including sharps or flats), one octave either side of middle C. It has a switchable vibrato oscillator, the use of which gives a more "rich" and pleasant sound. An unusual feature which can further enhance the sound, is a simple envelope generator circuit, and this gives a fast attack time with a decay time which can be varied from a fraction of a second to several seconds.

With a long decay time, the envelope shaper has little effect in normal use since changes from one note to the next occur before the volume of each note decays significantly. The unit then operates as an "ordinary" stylus organ. With a fairly short decay time, the character of the sound produced changes completely, and a sort of percussive sound is obtained.

The unit has a built-in amplifier but requires an external loudspeaker having an impedance of 8 ohms or more (although there is room to fit a small internal speaker). Construction is quite simple due to the use of a single printed circuit which forms the "keyboard" and accommodates all the components apart from the controls and the battery.

## SYSTEM DESCRIPTION

Fig. 1 shows the Stylus Organ in block diagram form. A voltage controlled oscillator (v.c.o.) is used to generate the basic sound, and the "keyboard" provides a series of 25 voltages which give the v.c.o. 25 different operating frequencies thus generating the required two
octaves of notes. A low frequency oscillator can be used to vary the v.c.o. control voltage very slightly so that notes are modulated either side of their normal frequency, and this gives the vibrato effect.

The output of the v.c.o. is fed to the amplifier output stage via a voltage controlled amplifier (v.c.a.), and under quiescent conditions the control voltage to the v.c.a. is zero so that the v.c.o. is cut off from the amplifier. However, the stylus is connected to a pulse generator circuit which produces a brief pulse each time a new note is played, and after shaping to produce a gradual decay over the desired period of time, this pulse is fed to the control input of v.c.a.

Thus the v.c.a. couples the tone signal straight through to the amplifier at the beginning of a new note, but it then



Fig. 2. The complete circuit diagram of the Stylus Organ. Note that the keyboard consists of 25 preset potentiometers (VR1 to VR25) although only three are shown.
stabilised 5 V supply so that there is no discernable tuning drift, and once the unit has been initially tuned no further tuning adjustments should be required.

Transconductance operational amplifiers are also ideal for use in v.c.a. circuits, and IC $2 b$ is therefore used as the v.c.a. R13 couples the output of the v.c.o. to the inverting input of IC $2 b$, and in conjunction with R15 this resistor also forms a negative feedback network which sets the maximum voltage gain of $I C 2 b$ at unity. R17 is the output load resistor for the transconductance op-amp IC2b and R15 is used in series with the amplifier bias input to give voltage rather than current controlled operation, and it also limits the bias current to a safe level.

TRI is used as an emitter follower buffer stage which boosts the input impedance at the control input of the v.c.a. from about 10 kilohms to several megohms. This is essential as the pulse shaper circuit has an output impedance which can be as high as a couple of megohms, and without TR1 excessive loading on the pulse shaper would occur.

IC 1 is used as the pulse generator. The non-inverting input of IC 1 is biased to the negative supply rail by $R 4$, but the inverting input is given a small positive bias by R2 and R3. The output of IC1 is therefore low under quiescent conditions. When the stylus is connected to one of the keyboard presets this produces a small positive pulse which is coupled by $C 2$ to the non-inverting input of IC 1 . This momentarily takes the non-inverting input to a higher potential than the inverting input so that the output briefly goes fully positive. A large but very brief pulse is therefore generated at the output of IC 1 at the beginning of each note.

This pulse is fed by way of R5 and D1 to C3, and the latter is rapidly charged to almost the full supply voltage. C3 discharges more slowly since D1 prevents it from discharging through the low impedance of R5 and the output stage of IC1. Instead C3 discharges through the high resistance of VR26 and R6, and VR26 enables the decay time to be adjusted from about 0.2 s at minimum resistance to around 5 s at maximum resistance.

## VIBRATO AND AMPLIFIER

The vibrato oscillator and output amplifier stages of the unit operate direct from the 9 V battery supply and not via the 5 V regulator. IC4 is a dual op-amp and this is used in the standard triangular/square-wave oscillator configuration. In this application, a triangular waveform is ideal as it gives a smooth variation in the operating frequency of the tone generator, and the square-wave output from IC4 is therefore ignored. R21 and C9 couple the triangular signal to the stylus, and Sl can be used to disconnect the oscillator when the vibrato effect is not required. If desired, the depth
of the vibrato can be changed by altering the value of R21. A lower value gives increased depth, a higher value gives a weaker vibrato effect.

The amplifier uses a TBA820M audio power amplifier i.c. (IC5), and this gives an output power of up to about 500 mW r.m.s. into an 8 ohm impedance loudspeaker. A high impedance loudspeaker could be used, but the maximum output power available would be substantially reduced.

VR27 is the volume control and this also acts as the input bias resistor for IC5. R25 and C10 form a simple lowpass filter which attenuate the higher harmonics on the output from the v:c.o. and give the unit a more musical sound. R26 controls the voltage gain of IC5, and the specified value gives a modest but more than adequate gain of about 28 dB ( 25 times). C13, C15 and R27 are needed to aid good stability. S2 is the on/off switch and this is ganged with volume control VR27.

The quiescent current consumption of the circuit is about 10 mA , but the current drain can rise to several times this figure when the unit is used at high volume.


Fig. 3. The fult-size artwork of the p.c.b. This board is available from the EE PCB Service, Order code 8309-03.


## PRINTED CIRCUIT BOARD

Details of the printed circuit board are shown in Fig. 3, and this is constructed using the normal techniques. In order to give the completed unit a neat finish great care should be taken when applying the resist to the "keyboard" area of the board. Using rub-on transfers there should be no problems in producing good results, and reasonable results should be obtained using a resist pen if the outline of each "key" is drawn in with the aid of a ruler and then the outline is carefully filled in. In other respects the printed circuit board is straightforward. Alternatively, the board is available from the EE PCB Service, Order code 8309-03, see page 565 .

Note that ICI is a cmos device and needs the normal handling precautions for mos devices. If a CA3130E is used for IC2 this can be fitted into an 8 -pin d.i.l. i.c. socket, and it should not be fitted into place until all the other components have been mounted on the board. The layout is shown in Fig. 4.

If CA3130T (TO-99 metal can) version is used, this could be soldered into place using a soldering iron having an earthed bit, and again it should be the last component to be fitted to the board and it should be handled as little as possible. Be careful to connect IC1 properly as it has the opposite orientation to IC4 and IC5, and it could be destroyed if the unit is switched on while it is connected incorrectly.

## COMPONENTS Mis




Fig. 4. The printed circuit board assembly and front panel wiring diagram. Note that the wires must be long enough to reach the front panel when the p.c.b. is mounted to the top of the case (see photograph over page). IC1 is shown as an 8 -pin d.i.I. device but the TO-99 metal can version can also be used.



Oscillograph showing the rapid attack and slow decay provided by the envelope shaper. The section of the trace on the extreme left shows the rapid decay of the previous note when the stylus has been removed before the signal has decayed naturally.


The track-side of the p.c.b. showing the keyboard area. This is polished but not coated as good electrical contact is required with the probe tip.


Fig. 5. Layout of the two octave keyboard. VR1 tunes note $C_{1}$ and so on to VR25 tuning $C^{1}$

## CASE

A Verocase of outside dimensions $205 \times 140 \times 75 \mathrm{~mm}$ makes a convenient housing for this project. The three controls and the two sockets are mounted on
the front panel, and the printed circuit board is mounted on the underside of the top panel with its copper side facing upwards. A cut-out measuring $150 \times 28 \mathrm{~mm}$ is made in the top of the case and the

View inside the case top. Note that the p.c.b. is spaced slightly off the case with four additional nuts on the mounting screws.

circuit board must be accurately mounted so that the "keyboard" is accessible through this cut-out. This cut-out can be made using a fret saw, a coping saw, or a miniature file.

If an internal loudspeaker is required, there is sufficient space to fit a miniature type (up to 58 mm in diameter) on the front panel in place of SK2, or a larger type on the base.

## ADJUSTMENT

The output of the Stylus Organ is coupled to any loudspeaker having an impedance of 8 ohms or more, and a fairly efficient loudspeaker (such as used with small music centres and similar equipment) is ideal. The output will also drive any normal type of earphone or headphone. A test prod is probably the best thing to use as the stylus, but it is also possible to use a small jack plug, a wander plug, or something of this nature.

The only adjustment required is to set the 25 preset potentiometers to give the appropriate notes. As mentioned earlier, the keyboard covers an octave either side of middle C , and is tuned using a piano or similar instrument to provide reference notes. The presets can be adjusted in any order. A layout of the keyboard is shown in Fig. 5 and the corresponding frequencies of the notes are given to aid tuning if using a frequency meter. When tuning the unit, adjust VR26 for maximum decay time (that is, fully clockwise) and switch out the vibrato effect.

After the instrument has been in use for some time it will probably be found that there is a noticeable change in frequency during each note, and this simply indicates that the battery voltage is becoming inadequate under maximum loading, and that a fresh battery is required. A PP6 9V battery is adequate, but if it is likely to receive a great deal of use and a low impedance loudspeaker is being used, it would probably be more economic to use a larger battery such as a PP9 type. There is plenty of space in the case to accommodate a larger battery.


## BY A.A. CHANERLEY b.sc. M.sc.

THIS article describes the construction and use of a general purpose 8 channel, 8 -bit wide Analogue-to-Digital Converter, tailored here for use with the Research Machines 380Z microcomputer. It will be used as the basis for the first six parts of our Computer Aided Experiments feature, see page 600.

Providing software is developed, it is possible to use this ADC with other computers fitted with a user port by connecting the appropriate cable connector.

## ANALOGUE-TO-DIGITAL CONVERSION

The computer, mainframe or micro, can only respond in any meaningful fashion to digital signals which are transmitted in either serial or parallel form. The 380 Z has two ports for input and output signals. One is the printer port, and the other is the input/output user port; both are sockets at the back of the 380 Z .

The signals they receive are in parallel form, eight input lines, each line capable of being in either a high state (logic 1) or low state (logic 0). The collective states of the eight lines, for example, 10101111 are called the binary word or byte. The maximum value of a byte is 255 in decimal corresponding to all eight lines high (11111111) or 0 in decimal corresponding to all eight lines low ( 00000000 ). Any intermediate combination of the "bit" pattern will correspond to a number between 0 and 255 ; for example, 00000011 is decimal 3.

In physical situations quantities such as temperature, pressure, light intensity and sound intensity vary in a continuous manner, altering gradually in response to some external event. Before a computer can measure any of the above quantities they must first be converted into an electrical signal.

This is possible by using a transducer. A microphone is a suitable transducer for converting the changing sound intensity from a persons speech into an electrical signal which changes exactly in step with the sound intensity.

This is only the first step, however. The computer will only respond in a meaningful fashion to digital signals and therefore it is necessary to convert the gradually changing (analogue) electrical signals from the transducer into a suitable digital input for the computer to work on; this is done electronically by the Analogue-to-Digital Converter (ADC) described here. The complete set-up is illustrated in Fig. 1.

## CIRCUIT DESCRIPTION

The complete circuit diagram of the Analogue-to-Digital Converter (ADC) is given in Fig. 2. As can be seen, the component count is very low, using only two integrated circuits and a few discrete devices.

The main component, IC2, is a 7581 which is an 8 -bit 8 -channel memory buffered data aquisition system in cmos. It is capable of receiving eight separate analogue signals, each signal having a maximum value of +10 V per channel when used in the unipolar mode, and -5 V to +5 V when in bipolar mode.

The design described here is unipolar mode in order to simplify construction and keep cost to a minimum.

The 7581 i.c. has a multiplẹxer, digital-to-analogue converter (DAC), registers, a comparator and control logic all on the one chip. It stores each analogue channel in succession and stores the latest result in eight 8-bit ram locations which are onboard. Each channel is then selected by software.

Each channel takes 80 clock cycles for a complete conversion, or 640 cycles for all eight channels. With a 1 MHz clock, that is $80 \mu \mathrm{sec}$ per channel and $640 \mu \mathrm{sec}$ for all eight channels.

There is also a status pin (pin 12)
which goes low when a channel conversion is complete thereby giving interrupt facility for real-time applications. This function however is not included in this design and those interested in such applications should consult the technical data sheet for the 7581. The block diagram of the 7581 internal circuitry is shown in Fig. 3.

## POWER SUPPLY

Requirements for this chip are +5 V (supply) and -10 V reference, and a 1 MHz clock. The latter is provided by the two gates IC1a and ICIb with components R1, R2 and C1. IC1c acts as a buffer for this arrangement.

All power requirements are provided by the $380 \mathrm{Z},+5 \mathrm{~V}$ direct and -10 V derived from the -12 V available on the user port by means of Zener diode D1 with series limit resistor R3. When used with other computers these supplies may not be available and will need to be derived externally. Sufficient space exists in the specified case for such circuitry.

The 8 -bit digital output is connected to the 380 Z user port socket data-in lines DIØ to DI7 via a 25 -pin D-type connector PL1 where $+5 \mathrm{~V},-12 \mathrm{~V}$ and 0 V are also picked up. The user port also has
 providing the necessary decoded CS. The other four data-out lines are available to the user at SK 9 to SK 12 so that the computer may be used to control other equipment such as motors that may be required in A-to-D experiments for such purposes as transducer position and movement.

The digital outputs may be read by the 380Z through a PEEK (64511) statement, with the data-out signals controlling A0, A1 and A2 which select the appropriate ram location in the 7581 holding the experimental data, through a

Fig. 2. Complete circuit diagram of the 8-channel Analogue-to-Digital Converter configured for use with the RM380Z user port.



Table 2. Data-in pins on the user port

| PL1 Pin No. | Data Line No. | Decima! Value |
| :---: | :---: | :---: |
| 1 | D0 | 1 |
| 14 | D1 | 2 |
| 2 | D2 | 4 |
| 15 | D3 | 8 |
| 3 | D4 | 16 |
| 16 | D5 | 32 |
| 4 | D6 | 64 |
| 17 | D7 | 128 |

## DATA-IN

Table 2 shows the data-in pins which connect to the digital outputs of the 7581 on the circuit diagram, and constitutes the 8 -bit word (byte) which corresponds to the converted analogue data. All pins high gives $255(1+2+4+8+16+32$ $+64+128$ ); this is equivalent to the maximum analogue input of 10 volts. All pins low is of course 0 volts. Any intermediate bit pattern corresponds to intermediate analogue voltage between 0 V and 10 V .

These pins are read by the PEEK (64511) statement, which follows the appropriate POKE64511,XX command.
Fig. 3. Internal block diagram of the 7581 A-to-D converter i.c.

POKE64511,XX statement where XX takes on values from 0 to 7 depending on the analogue channel selected.

## TEST ROUTINE

$1 \emptyset$ POKE64511.8:REM clear data lines: GRAPH 1 : GRAPH $\emptyset$
$2 \emptyset$ INPUT $X: R E M$ select channel number
30 POKE64511,X:REM initialize selected channel
$40 \quad Y=\operatorname{PEEK}(64511):$ REM read value of digital output from 7581
$5 \emptyset$ PLOT $4 \emptyset, 5 \emptyset$, STR $\$(Y)+{ }^{\prime \prime}$ ":REM print data at grid ref $4 \emptyset, 5 \emptyset$
$6 \emptyset$ GOTO $3 \emptyset:$ REM go back for next value of data

## DATA-OUT

Table 1 shows the bit pattern associated with the data-out pins. Each pin is controlled by the POKE statements shown. For example, pins 5, 18, 6 are connected to A0, A1, A2 of the 7581 chip and these address the ram locations, corresponding to the analogue inputs, inside the chip.

So that POKE64511,00 selects analogue channel 0 with pins 5, 18, 6 all low. Then the statement POKE64511,01 selects the analogue channel 01 , with pin 5 high, and pins 18 and 6 low. POKE64511,03 statement selects channel 03 , with pins 5 and 18 high and pin 6 low, that is, 011 in binary which is 3 in decimal, and so on.

The logic levels for these pins, 6,18 and 5 for all eight channels are: 000,001, $010,011,100,101,110$ and 111 corresponding to channels $00,01,02,03$, $04,05,06$ and 07.

Pins $21,8,20$ and 21 correspond to the auxiliary outputs on the unit marked AUX1 to AUX4. Using the appropriate POKE statement these pins can be made high (or low) to activate external circuitry.

Table 1. Data-out pins on user port

| PL1 <br> Pin No. | Data Line No. | Bit Pattern | Basic Statement | To |
| :---: | :---: | :---: | :---: | :---: |
| 5 | Dø | 00000001 | POKE64511,2 | $A \emptyset$ |
| 18 | D1 | 00000010 | POKE64511, ${ }^{1}$ | A 1 |
| 6 | D2 | 000001.00 | POKE64511, $2^{2}$ | A2 |
| 19 | D3 | 00001000 | POKE64511,2 ${ }^{3}$ | CS |
| 7 | D4 | 00010000 | POKE64511,24 | AUX4 |
| 20 | D5 | 00100000 | POKE64511,25 | AUX3 |
| 8 | D6 | 01000000 | POKE64511, ${ }^{6}$ | AUX2 |
| 21 | D7 | 1.0000000 | POKE64511, ${ }^{7}$ | AUX1 |




## canstructionir

## CONSTRUCTION

The Analogue-to-Digital Converter is constructed on 0.1 inch pitch stripboard, size 37 strips $\times 33$ holes. This board size differs in strip count from that seen in the photographs as we have increased its size to provide a different board fixing method than that used in the prototype. This larger board requires a wider case than that seen and the next size up in the same style is specified in the components list.

The analogue inputs are made in 4 mm panel mounting sockets. The four sockets labelled AUX are not essential. They provide access to user port signal lines DO4 to DO7 and were included by the author for work he has in mind for the future. There is space in the case for fitting further sockets or circuitry if the user so wishes, perhaps from another outlet from the 380 Z that could be useful in A-to-D experiments.

The layout of the components on the topside of the stripboard is shown in Fig. 4. A number of breaks in the copper strips on the underside need to be made. Use a spot-face cutter or a small ( 3 mm ) diameter twist drill to make these breaks. Drill the four board fixing holes as shown to suit the fixing hardware. We suggest 6 mm high snap-fixing plastic stand-off pillars which require a 3.8 mm diameter hole in the board with a 5 mm diameter hole drilled in the case. Use the drilled board as a template to drill pilot holes in the case base and then enlarge to 5 mm . In this way, the two sets of fixing holes are


The completed prototype with separated case sections.
correctly aligned and so exert minimal strain on the board.

Continue by mounting the i.c. sockets followed by the numerous link wires. You should use plastic covered single core wire or tinned copper wire sleeved at positions where the conductors run close. Next solder in the resistors, capacitor and Zener diode paying attention to orientation of this device.

Table 3. 3802 user port pin assignment details

| Pin No. | Function | Pin No. | Function | Pin No. | Function |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | DI | 11 |  | 21 | DO7 |
| 2 | DI2 | 12 |  | 22 | -12 V |
| 3 | DI4 | 13 |  | 23 | OV |
| 4 | DI6 | 14 | DI1 | 24 |  |
| 5 | DOQ | 15 | DI3 | 25 |  |
| 6 | DO2 | 16 | D15 |  |  |
| 7 | DO4 | 17 | DI7 |  |  |
| 8 | DO6 | 18 | DO1 specified |  |  |
| 9 | $+5 V$ | 19 | DO3 |  |  |
| 10 | +12 V | 20 | DO5 |  |  |

Fig. 6. View of the connectors on the $380 Z$ microcomputer rear panel (not to scale) clearly showing the user port socket with pin numbers.


## CABLES

The ribbon cable connecting the board to the sockets and PL1 was soldered directly to the board tracks. Another idea is to use Veropins for this connection which will then allow the board to be positioned in the case before wiring up. Fit the Veropins now if you choose this method. Thoroughly check your construction before proceeding.

Prepare the lid of the case to accept the 4 mm sockets SK1-SK 14, see Fig 5. When fitted, wire up SK 1 -SK 8 to the board. Ribbon cable (8-way) was used for this in the prototype. Connect GND sockets to the board using stranded plastic covered wire. The easiest way to run the ribbon cables out of the case is to file three small recesses in one of the case end edges (lower section): two at $10 \times$ 2 mm and one at $5 \times 2 \mathrm{~mm}$. When the lid is finally assembled, the lid will grip the cable firmly. It would be a good idea to fit self-adhesive ribbon cable grippers about 10 mm below the recesses to align the cables with their respective outlets.

Fit the i.c.s in their sockets paying special attention to orientation and then push the board onto the plastic stand-off pillars which you have already positioned in the case.

There are two ribbon cable groups that connect the board to PL1. Solder sufficient lengths of 8 -way and 7 -way cable to the board and a similar length of 4-way cable from SK9-SK12 to reach the user port via PL1. Feed the cables out through their recesses after being fitted in their grippers.

Identify each of the wires and, with reference to Fig. 6 and Table 3, connect them to PL1 to complete the assembly. Screw on the lid and the unit is ready for testing.


Fig. 7. Simple test circuit for checking out the ADC unit. Use the "Test Routine" software given earlier.

## TEST CIRCUIT

A simple test circuit which consists of a 9 V battery whose voltage is divided by a 5 kilohm potentiometer is shown in Fig. 7. By twirling the wiper and thus altering the voltage at the wiper, the value of the data byte printed at grid reference 40,50 on the v.d.u. will be seen to vary between 0 and 230 , corresponding to 0 V and a maximum of 9 V in this case; 255 is for a maximum of 10 V , so the scale factor is easily calculated ( $=9 / 230$ ).

The equation giving the relationship between the input voltage and the binary output is:

$$
V=k N
$$

where $V=$ analogue input voltage $N=8$-bit binary output $k=\mathbf{a}$ constant
In this particular case

$$
N=255
$$

when $V=10 \mathrm{~V}$
and therefore the value of

$$
k=10 / 255
$$

This, of course, can be written into the programme, so that the software will plot the actual value of the voltage in line $5 \emptyset$ of the Test Routine rather than the decimal equivalent of the data byte.

If the above test was successfully carried out, you now have a useful instrument for use with a 380 Z microcomputer in the science laboratory for many experiments. With suitable software, experimen-

# COUNTER WTELACENCE <br> <br> BY PAUL YOUNG 

 <br> <br> BY PAUL YOUNG}

## Ion Tonic

I would like to begin by thanking the readers who were kind enough to write to me on the subject of lonisers (see May ' 83 issue). Their letters were all most enlightening, but the letter I found the most useful, came from Mr. C. Hyde of Beds.

With his letter he kindly enclosed the circuit of a simple apparatus which would give an indication as to whether the negative lons were flowing, see Fig. 1. This could be mounted on a small piece of wood.


The bare wire at one end is held between the thumb and index finger, and the bare wire at the other end is brought near the source of the negative lon emission. The neon lamp will flash intermittently, and the closer it gets to the source, the faster it flashes.

It could be constructed for a few pence and is particularly useful for gauging when the needies from which the lons flow need cleaning. I can confirm that the needies do become corroded in time and then the flow slows down. The best way to rectify this is to polish them with some fine emery cloth or sandpaper.

One reader told me that the Cockcroft Walton circuit tended to produce too much Ozone and therefore some of the commercial machines use a different circuit. I must confess 1 have not noticed it myself, although it always seems to be present when there is a high voltage discharge. It is a rather penetrating pungent smell which tends to irritate the membranes of the nose and throat.

Another reader successfully disposes of the "Placebo" argument by pointing out that negative lons have been proved beneficial to animals. The thought had never struck me before, that it is useless giving a placebo to an animal.

Lastly a reader from Jersey pleads for a simple constructional article for an loniser. Well, Mr. Yates, I have some good news for you. I have it on the highest authority that you can expect to see such an article in Everyday Electronics in the next two to three months.

tal results may be collected, stored, plotted and analysed. For ideas of its use see Computer Aided Experiments, page 600, a new series starting this month.

## Wide Interest

The other day, I was idly contemplating the difference between two contrasting professions, the Antique Dealer and the Electronic Engineer. Now before everyone starts muttering, "What on earth does old Paul Young know about Antiques?" I will have you know that my aunt had an Antique shop in North Road, Highgate Village, for many years. She also had a stall in the Caledonian Market every Friday. Now please keep this to yourselves, I wouldn't want it broadcast, but I sometimes used to look after the stall in the hope of extra cash.

All this is by the way, but to me the interesting thing is this. The field of Antiques is fairly limited. Even so, in the case of my own relations, and no doubt this was repeated throughout the trade, they decided there was so large an area to be covered that the only satisfactory way to achieve it was by specialisation. Consequently, my aunt was an expert on China, my uncle Pictures, my cousin Furniture.

By contrast, the field of Electronics is expanding in all directions at the speed of light. I, and even colleagues who are considerably younger are almost overwhelmed by the speed of progress and the magnitude of the task of keeping abreast of modern practice.

I think all of us, have long realised, that the only hope is specialisation in a very narrow sector, and this, dare I say it, could become extremely duil and boring. I am glad to note that Everyday Electronics is not proceeding along these lines, but keeping its interests wide and varied.

Long may this policy continue. All the same, there are times when I wish I had taken up Antiques.


## Comptalk

While industry's attention has been firmly focused on "Office of the Future" systems, Maplin Electronics have just introduced, what they call, "Shopping of the Future" for its customers. It is now possible for orders to be placed directly with their computer by means of a customer's home or personal computer.

All the customer needs, apart from his home computer, is a modem kit and interface unit. A modem kit, £39.95, and a ZX81 interface, £19.95, are available now. Before long, it is hoped, that most popular microcomputer systems will be able to join the electronic shopping club.

Apart from the "interrogation" equipment, the prospective customer will need to be equipped with a Maplin catalogue product code number, a credit card number plus a conveniently sited telephone.

Having dialled directly into the Maplin computer system, the user will be able to access the stock file and place his order. The user's screen will display stock availability, the current price and total amount to be charged to the credit card account.

For more details of the Maplin Computer Shopping Service write to: Maplin Electronic Supplies Ltd., Dept EE, PO Box 3, Rayleigh, Essex SS6 8LR.

## Constructor's Aid

Sometimes we hear from readers who, having seen an article in our pages, rush out purchase the components and then Find, through one reason or another (usually lack of time), that they are unable to complete the project. Then there is

## EE PRINTED CIRCUIT BOARD SERVICE

Printed circuit boards for certain EE constructional projects are now available from the EE PCB Service, see list right. These are fabricated in glass-fibre, and are fully drilled and roller tinned. All prices include VAT and postage and packing. Remittances should be sent to: EE PCB Service, Everyday Electronics Editorial Offices, King's Reach Tower, Stamford Street, London SE 1 9LS. Cheques should be crossed and made payabie to IPC Magazines Ltd.
Readers are advised to check with prices appearing in current issue before ordering.
the reader who has built his project but has difficulty in getting it to work.

When we receive letters from readers in this vein, we have to write back regretting that we are unable to accept and "troubleshoot" individuals models. We can only advise, from the information contained in the letter and about circuits published in EE, as best we can but this can only be in general terms as we cannot take into account the standard of construction.

Most faults are usually traced back to bad soldering, missing links or neglecting to make all the necessary cuts in copper strips. Always check the wiring against the circuit-we are not immune against errors ourselves.

We have just heard that Webb Logic Systems have spare capacity in their workshops and are prepared to undertake the task of sorting out readers' problems with any published design. They claim they will build, test and/or repair any partially or completed projects.

The average cost for the complete assembly of most projects is in the region of $£ 10$ to $£ 20$. But they do reserve the right to make exceptions to the costing in cases of (in- their assessment), complex and poorly submitted models.

We have not seen the service in action, but feel sure that readers will welcome this facility. For more information readers should write to: Webb Logic Systems Ltd., Dept EE, Gainsborough House, 15 High Street, Harpenden, Herts AL5 2RT.

CONSTRUCTIONAL PROJECTS

## Distress Beacon

The instrument case for the pocket version of the Distress Beacon is available direct from Lascar Electronics Ltd., Dept EE, Module House, Whiteparish, Salisbury, Wilts.

The NiCad PP3 equivalent batteries were obtained from Ambit. Suitable equivalent are also stocked by various advertisers.

Other components likely to prove difficult to locate are the mercury switch, Xenon tube, diac and triac. The Xenon tube and transformer, diac and triac are available from Maplin Several advertisers stock mercury switches and it should be possible to adapt these for this design.

## A/D Converter

The D-type connector for the $A / D$ Converter is available from Bi-Pak, Maplin, Electrovalue, Cricklewood and Rapid Electronics.

The 8-channel 8-bit analogue-to-digital i.c., type 7581, is stocked by RS Components, Order code 303:545. RS Components will not supply to the general public but must be ordered through a local recognised stockist.

## Stylus Organ

The semiconductor devices appear to be the only item that could cause any purchasing problems for the Stylus Organ.

The dual transconductance amp. type LM13600N, seems to be orily listed by Cricklewood, Maplin and Rapid Electronics. However, all components for this project are stocked by Maplin.

## Microcomputer Interfacing Techniques

As far as components for this month's constructional projects, for the Microcomputer Interfacing Techniques series, go we cannot foresee any problems for the Signal Conditioning Amplifier. However, the ZN449 only seems to be available from JEE Distribution Ltd., Dept EE, 43 Stathville Road, London, SW18 40X.

## Computer Aided Experiments

The photodiode, with integral amplifier, used in the Computer Aided Experiments was obtained from RS Components and carries the stock number, 308-067. This device is only available from a recognised RS dealer.

We do not expect.any component buying problems for the Traffic Light Simulator and the Voltage Dualiser projects.

| PROJECT TITLE | Order Code | Cost |
| :---: | :---: | :---: |
| Eprom Programmer, TRS-80 (June 83) | 8306-01 | £9.31 |
| Eprom Programmer, Genie (June 83) | 8306-02 | £9.31 |
| Eprom Programmer, TRS-80 \& Genie (June 83) | 8306-03 | £1.98 |
| User Port Input/Output M.I.T. Part 1 (July 83) | 8307-0.1 | f4.82 |
| User Port Control M.I.T. Part 1 (July 83) | 8307-02 | ¢5.17 |
| Storage 'Scope Interface; BBC Micro (Aug 83) | 8308-01 | £3.20 |
| Car Intruder Alarm (Aug 83) | 8308-02 | f5.15 |
| Electronic Die (Aug 83) | 8308-03 | £4.56 |
| High Power Interface M.I.T. Part 2 (Aug 83) | 8308-04 | ¢5.08 |
| Pedestrian Crossing Simulation M.I.T. Part 2 (Aug 83) | 8308-05 | £3.56 |
| High Speed A-to-D Converter M.I.T. Part 3 (Sept 83) | 8309-01 | £4.53 |
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# DISTRESS BEACON <br> FOR MARINE USE <br> PLUS A POCKET VERSION FOR FELL WALKERS \& MOUNTAINEERS 



MAN OVERBOARD at night is a distressing experience both for crew as well as victim. Time is the essence, and quick positive action essential in order to avoid loss of life.

With the Distress Beacon mounted in an accessible position, and easily located by the small flashing light on the lamp housing, all that needs to be done is to toss it overboard. Immediately on hitting the water, the beacon is activated, emitting a powerful flash of light and providing a reference point both for swimmer and helmsman. A spiral rescue pattern working inwards to the beacon should soon effect a speedy rescue.

The beacon is totally sealed to prevent water entering, and is powered by nickelcadmium cells, inductively charged from an external circuit, thus eliminating leakage or corrosion problems associated with connectors.

Constructors may prefer to use a casing of their own design, however, details are included of the moulding techniques as used in the original, and those that have had no previous experience in glassfibre moulding may be delighted to discover how easy it really is using readily available moulds from off any supermarket shelf.

## POCKET BEACON

For fell walkers, climbers and hikers, a pocket version of the Distress Beacon is also described. It is designed to fit into a calculator-type case and can be attached to an anorak or rucksack.

So if the user should become lost in darkness or mist, or fall injured and be unable to return, the Beacon will attract the attention of the rescue party.


The circuit description for this version is identical with the exception of the battery charger section, as this is unnecessary, for the Pocket Beacon can use standard alkaline cells in place of ni-cads. The constructional details are given in a separate section.

## CIRCUIT DESCRIPTION

A single transistor oscillator based around TR1, working at a frequency of 20 kHz converts the 8.4 V battery supply via the secondary winding (S) of T1 and voltage doubler formed by D7, D8, C9 and C10 to a peak rail voltage in the order of 350 V under no load conditions (see Fig. 1).

This peak voltage is, in reality, never achieved since current is constantly being bled via R5 to charge capacitor C11. At the same time, current is drawn from the h.t. supply through R6 and R7 to charge C12, and also through R10 to charge C13.

When the potential across C12 reaches a point approaching 80 V (by which time C11 has charged up to 240 V ) the diacs (D9, D10) conduct, providing a trigger pulse to the gate of the triac, CSR1.

The triac turns on and shorts C13 to ground, and the rapid discharge through the primary of the trigger transformer (T2) induces a 3 kV pulse in the secondary, which is fed to the glass envelope of

## SPECIFICATIONS

Power
Supply BEACON

## Supply:

Drain
Drain:
Standby: (Upright during daylight) 0.5 mA
Inverted: (Day or night) xenon supply ( B 1 ) zero mA. (B2) l.e.d. flasher supply 0.5 mA

Xenon Approx. every 7
Flash rate: seconds over a period of 2 hours on a fully charged battery
Energy 0.5 Joules
Output of
Light:

## CHARGER

Supply: 12 V d.c.
Current $\quad 60 \mathrm{~mA}$ nominal
Drain:
Frequency 6.5 kHz
of
operation:
Charge . 5 mA nominal rate:
Efficiency: 10\% max.
the xenon tube (LP1). The normally nonconductive xenon gas is sufficiently energised by the high voltage pulse into a state of conduction, and the full charge held by C11 is able to discharge through the column of gas causing further excitation of the xenon atoms and the intense emission of light energy.

Once C11 has been discharged, the h.t. rail is reduced to zero volts, at which
no longer operate under daylight conditions. To test the completed unit, it is therefore necessary to mask PCC1 with a piece of opaque tape.

When the beacon is not in use, it is hung upside-down. An l.e.d. (D6) located at the top of the lamp housing, and run by the LM3909 (IC2) produces regular flashes of light, making the beacon easy to find at night should an emergency oc-

point the triac reverts to a nonconducting state. The charging sequence of C11, C12 and C13 begins to commence once more, and the cycle of events described repeats continuously until the power source is removed.

## DAYLIGHT INHIBIT

The light dependent resistor (PCC1) placed across R4 reduces the base bias of the transistor TR1 to a point where it can
cur. Once thrown into the water, the unit will float the correct way up and the internal mercury switch ( S 1 ) will connect the battery to the xenon flasher circuit and, providing the internal battery has been charged, flashing will commence within 20 seconds.

## CHARGING THE BEACON

While the beacon is hanging in the inverted position, an energiser is placed into


Printed circuit boards: EE PCB Service, Order code 8309-04; raw materials for casing (see separate list); small case for charger, $35 \times 35 \times 20 \mathrm{~mm}$; battery connector; $7 / 0.2 \mathrm{~mm}$ wire; small-bore aluminium tube; spacer, 15 mm long; eyehook; foam pads; silica gel in muslin bag; FX2238 ferrite core plus bobbin (for T1); silicone rubber; assorted screws (non-corrosive); 40 s.w.g., 36 s.w.g. and 30 s.w.g. enamelled copper wire.

## Transformer (T1) <br> Windings

Primary 12 turns 30 s.w.g. enamel wire (marked P) Feedback 3 turns 30 s.w.g. enamel wire (marked F) Secondary 240 turns 36 s.w.g. enamel wire (marked S)

Wind first the primary and feedback windings alongside each other. Mark the start and finish of each winding, and cover this first layer with one turn of the paper tape. Next wind on the secondary. Care must be taken to lay the windings neatly, otherwise the bobbin will not enter the cores. When complete, finish off with one layer of paper tape to hold the windings in place and add fine bore plastic sleeves to the start and finish of the three windings. Assemble the bobbin and cores holding them together with a screw and nut, and immerse the transformer in hot paraffin wax.

## Inductors

Energiser (L1)
250 turns $40 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. wire wound over a length of 10 mm on a $20 \times 6 \mathrm{~mm}$ diameter ferrite rod (Fig. 6)
Receptor (L2) 220 turns of 36 s.w.g. wire wound over a length of $10 \overline{\mathrm{~mm}}$ on a glass-fibre tube (Fig. 7)
a receptor. In the charger, a 555 timer running at a frequency of $6 \cdot 5 \mathrm{kHz}$ provides pulses that are fed to the energiser coil, L1, producing an alternating magnetic field.

This field induces an alternating current in the receptor coil, L 2 , which is rectified to produce the d.c. potential necessary for charging the ni-cads. An l.e.d. (D5) will light, indicating that charge is being transferred into the beacon.

## CHARGER CIRCUIT

A 555 timer (IC1) running in the astable mode at 6.5 kHz provides pulses which are fed via C3 to the energiser coil, L1. The signal from L1 passing through the beacon shell induces a resonant field in the tuned circuit formed by the receptor coil L2 and capacitor C4. The voltage produced across L2 is further increased by the doubler network formed by D3, D4, C5 and C6 to provide the charging potential required to the ni-cads B1 and B2.

The in-line l.e.d. (D5) indicates that charge is passing into the batteries, which for the cells specified will be reached in a little over 24 hours. The nominal charging rate for the ni-cads is 15 mA - which is the maximum permissible current once the cells reach full charge.

Since the charger unit described here delivers well below this figure, it may be left on charge for considerable periods without risk of damage to the batteries. This ensures that the beacon is always at peak operational performance. A very slight warming of the energiser coil is normal when the charger is in use.


## CIRCUIT CONSTRUCTION

Few problems should be encountered in the circuit construction, providing miniature components are used wherever possible in order to save space and weight. Perhaps the most tedious job is cutting out two identical discs of circuit board, 65 mm in diameter. A complete set of p.c.b.s for the marine version of the Distress Beacon is available from the $E E$ $P C B$ Service, Order code 8309-04.

The transformer (T1) winding details are given under the components listings, and as can be seen from the first p.c.b. layout (Fig. 2), connections into the p.c.b. 1 will be found to be straightforward. The secondary windings marked G and S have to be connected to p.c.b. 2 with short, well insulated leads, before the two boards are screwed together.

The two low-voltage windings are connected as follows: Feedback (finish), Primary (finish), Feedback (start), Primary (start), as viewed from left to right on the component overlay drawing.

Ensure that the mercury switch (S1) is mounted in such a way that battery, B1,

# DISTRESS BEACON 



Fig. 2. The component layout and full-size artwork of p.c.b.1. Note that D6 is mounted at the top of the Beacon housing. Battery B2 is located on the space in the centre of this board.


Fig. 3. The component layout and full-size artwork of p.c.b.2. The xenon tube, LP1, is mounted onto stiff wire supports in the holes indicated (see also Fig. 4).



Fig. 5. The full-size artwork and component layout of the battery charger p.c.b. This fits into a small enclosure into which D2 is mounted.
is switched on only when the beacon is in the upright position. Note also that the light dependent resistor (PCC1) is not mounted on the printed circuit board, but is located as shown in Fig. 4 in the upper half of the beacon casing.

The battery, B2, that operates the l.e.d. flasher should be mounted on p.c.b. 1 and soldered into position. The current consumption of IC 1, the LM3909, is so low that a button-sized ni-cad may be used with a considerable weight saving.

As shown in Fig. 4, the two circuit boards are mounted on top of one another, and the screw that secures the potcore of T1 should be of sufficient length to pass through both p.c.b.s in order to bolt the two boards together. A plastic support drilled to take a selftapping screw at each end should be positioned at a convenient place on the opposite side of the two boards in order to keep them parallel. Ensure that none of the components on p.c.b. 1 are touching the printed track of p.c.b.2.

Three in-line holes at the centre of p.c.b. 2 mark the position of the xenon tube supports which also serve as power connectors. A strip of brass 50 mm long and 0.8 mm thick was used as the central positive supply support, and a length of stiff wire cut to the correct length and with a 90 degree bend at the top end for the negative supply support. The trigger supply wire should be well insulated to minimise voltage losses en route to the glass envelope.

The l.e.d. (D6) supply which runs to the top of the lamp housing was positioned through a fine bore aluminium tube obtained from a model shop. The purpose of this tube is to keep the l.e.d. supply neatly stored away from the high voltage wires supplying the xenon.

## TESTING

On completion of the circuit boards p.c.b. 1 and p.c.b.2, connect up the battery, B2, and check that the l.e.d. flasher is operational. Next, with the PPC1 masked with opaque tape or temporarily disconnected, connect B1 and check that the current drain is between 40 and 50 mA . Current in excess of this probably indicates a fault in the oscillator or h.t. winding of $T 1$.

With a testmeter across C11, the voltage should steadily rise to 240 V d.c., by which time the xenon should have fired. If no voltage exists across C11, disconnect R5 and check to see if there is a voltage across C10 in the order of 300 V d.c. If not, check that the diodes D7 and D8 are properly positioned, and that the h.t. connections between the two boards are sound.

Should the voltage across C11 reach 240 V without the xenon firing, the circuit should be switched off before the voltage rating of the capacitor is exceeded. Check the triac (CSR1) connections, trigger transformer and polarity of the xenon supplies.

## CHARGER CONSTRUCTION

Construction of the charger p.c.b. is straightforward, and the layout is shown in Fig. 5. The prototype was housed in a miniature "Twinings Tea" sample caddy, reduced in height to measure $20 \times 35 \times$ 35 mm . Any case of similar dimensions can be used.

Considerable care must be taken, however, in winding the coils L1 and L2 in order to minimise losses.

## ENERGISER COIL

Following the dimensions given in Fig. 6 , wind one turn of thin sticky paper onto a 20 mm long by 6 mm diameter ferrite rod. Starting 2 mm from one end of the rod, wind on 250 turns of 40 s.w.g. enamelled wire in neat layers over a length of 10 mm of rod.

Referring to Fig. 6A, pass the energiser feed wire through the base of a small plastic cap (as used to seal pill tubes). Slip in place a short length of plastic sheath and strip and tin the ends of the wire.

Prepare a small quantity of resin, and pour into the plastic cap as shown in Fig. 6B. When the resin has set, position the wires so that they are parallel and able to accommodate the diameter of the ferrite rod which should be placed in position by means of a further blob of resin (Fig. 6C).

When sufficient time has elapsed for the resin to harden, solder the coil ends to the feed wire, and pour a further quantity of resin into the cap to cover the first three millimetres of the ferrite (Fig. 6D).

Lastly, the exposed length of ferrite and coil windings have to be covered in a regular layer of resin. One way to do this is to find a short length of plastic tube of 8 to 9 mm internal diameter, coat the inside with a layer of resin, and coat the ferrite and coil.

Slip the plastic tube over the ferrite until the resin has set hard, and then carefully remove both the plastic tube and the plastic cap. The energiser coil will have a regular finish as in Fig. 6E.

## RECEPTOR COIL

Find a former with an outside diameter of no more than 0.5 mm greater than the

Fig. 6. The step-by-step construction of the energiser coil, L1, shown in cross-section. Note that the windings must be completely enclosed in resin.



Fig. 7. The constructional details of the receptor coil, L2. Note that it should be tested before bonding to the Beacon shell.
diameter of the completed energiser (disregarding the enlarged region where the supply lead enters). Clean and polish the surface with furniture polish, and apply a thin regular layer of gel coat. When the gel has hardened, apply three turns of fine glass matting and resin (Fig. 7). When set, ease out the former and cut off the ends to form a tube 15 mm in length.

Using a waste fragment of glass-fibre from the beacon casing, cap one end of the tube as shown by applying gel coat to both surfaces, thus effecting a waterproof seal.

Starting 2 mm from this join, wind onto the tube 220 turns of $36 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. enamelled wire in compact layers over a distance of 10 mm . Cardboard stops glued into position on the tube will locate the winding within these limits.

At this stage the coil should be checked before it is permanently bonded against a hole drilled through the beacon casing. Space inside the beacon is very limited and care should be taken to select miniature components for the receptor board which, only if space permits, should be bonded to the receptor coil, otherwise it will have to be located elsewhere inside the beacon shell. The layout of the receptor coil p.c.b. is shown in Fig. 8.

For testing purposes, temporarily link receptor coil (L2) to receptor board and
link batteries B1 and B2 in circuit observing correct polarity. Place energiser coil into the receptor tube and connect charger board to a 12 V d.c. supply. D5 should light indicating that charging has commenced. Check that current drawn by the charger board is between 40 and 60 mA , and that the charging current into the battery is $5 \mathrm{~mA}, \pm 1 \mathrm{~mA}$.

VR1 on the charger board should be set for maximum resistance, that is, 100 kilohms. Reduce this value slightly to see if the efficiency of coupling is improved. When the best position for VR1 has been found it may be replaced by a resistor of fixed value.

Excessive currents drawn by the charger board may be trimmed by a low value resistor placed in series with the supply-typically 20 to 30 ohms. Further adjustments to compensate for the variation in the properties of the inductors L 1 and L2 may be effected by slightly altering the value of the tuning capacitor C 4 .

## BEACON CASING

Glass-fibre is not in itself waterproof, and thus the outer surface of the beacon is coated in a layer of gel coat, which is obtainable in a variety of colours. You will find that most small boat repair yards will not only be willing to supply gel coat


Fig. 8. The component layout and full-size artwork of the receptor coil board (p.c.b.3). Note that the components are mounted onto the track-side. D5 is remotely mounted in the Beacon as shown in Fig. 4. The receptor coil, L2, is wound onto the former as shown in Fig. 7 and connected to the position indicated.
in the small quantities required, but will also supply you with the rest of the materials for moulding the beacon, providing that you are suitably equipped with an assortment of clean jam jars.

A catalyst has to be mixed in with both gel coat and resin in order to initiate the hardening process. The catalyst contains the toxin Styrene, and should be handled with utmost care, and only used in the recommended proportions as given by the supplier. Usually mixing proportions are in the order of one part hardener to fifty parts of resin/gel coat.

Except for the lamp housing (Section B, Fig. 4), which should be made from clear Perspex or plastic tube, the rest of the beacon was moulded with the following items:

Section 'C-"Chambourcy" cheesecake container.
Section D-Top cover of an "Airball" air freshener.
Section E-A "Blue Flush" W.C. treatment container.
Should any of the above be unavailable, the great variety of plastic lids and caps found on supermarket shelves should provide a near equivalent.

## MOULDING

Each container should be washed, dried, and polished with wax polish. Next, a thin layer of gel coat, with hardener added, was applied using a small glue brush to the inside of the container. When the gel coat had started to harden, a second thin layer was applied on top.

Fine glass matting was then applied in small strips, each strip being firmly pressed onto the gel coat by means of a stippling action with the glue brush dipped into resin. Three layers of glass matting and resin should be sufficient to give the required strength and rigidity.

When the three sections C, D and E have been moulded, a circular hole should be cut through the centre of the base of section $D$, and the rim of section $E$ bonded in place using glass matting and resin. Seal any cracks on the outside about the join with gel coat, and cover with Sellotape until cured.

A lip must be formed around the internal circumference of the top of section $D$, and this may be achieved by trimming an inverted "mousse" container mould, and inserting it into the glass-fibre shell to form a trough, 5 mm in depth, between the mousse mould and shell. Into the formed trough, a mixture of resin and glass-fibre should be poured until it is flush with the rim of the casing. When set, the mousse mould should be removed, and the newly formed lip ground smooth using an abrasive sheet placed on a flat surface.

## FLANGE

A flange has to be formed onto section C in order that it may be bolted to section D when the beacon is completed. Using two plastic or Formica sheets, apply gel coat to one surface, and when set, lay down glass matting and resin until a
depth of 4 mm has been achieved. Apply gel coat to one surface of the second plastic/Formica sheet and place firmly, gel coat downwards, on top of the first sheet to form a gel-glass-fibre-gel sandwich.

When cured, the plastic sheets may be removed and the flat glass-fibre sheet cut with a small saw to form the flange to be bonded to section $C$.

Eight screw holes should be marked and drilled at equal intervals around the middle of the flange, and corresponding holes in the lip of section D.

Four holes should be drilled in the top of section C , and corresponding holes carefully drilled in the base of the clear plastic tube of the lamp housing. Four more holes are required at the other end of the lamp housing, and a small disc of Perspex or plastic cut and drilled to fit. The l.e.d. flasher (section A) is cut from a 15 mm block of clear plastic, drilled to accommodate the l.e.d. and securing bolts, and polished using a 400 grade abrasive sheet followed by Brasso-type metal polish.

The base of section $E$ should be drilled to take an eye-hook cut to a length of 40 mm and with a shaft diameter of 5 mm : This should be secured with a nut and washer on each side of the glass-fibre shell, the thread being first dipped in gel coat, and sealed on the inside of the beacon with a further amount of gel coat as illustrated in Fig. 4.

Assemble the complete beacon casing to ensure that a good fit has been achieved between the mating surfaces. Non-rust screws should be selected with regard to the thickness of materials through which they have to pass, and threads must be tapped with utmost care.

## FINAL ASSEMBLY

During the final assembly of the beacon, all mating surfaces and screw threads are coated in silicone rubber to ensure total sealing against water.

Cut a small slot in the centre of section $C$ just large enough for the xenon tube to pass through into the lamp housing, also a hole for the PCC1 as positioned in Fig. 4. Bond PCC1 into position using clear resin on the external surface to permit passage of light, and back with a small layer of resin and glass matting on the underside of section C to give mechanical rigidity.

Bolt section $B$ to $C$, using silicone rubber to seal. Drill a 3 mm diameter hole adjacent to the internal wall of the lamp tube through the top of section C. Insert a 54 mm length of small-bore metal tube through this hole, so that it passes up to the top of the lamp tube.

Install D6 into the clear plastic 1.e.d. housing and screw into position on the 32 mm diameter plastic disc that seals the top of the lamp tube. Solder a couple of thin wires onto the 1.e.d. leads, insulate the solder joints with a little plastic sleeving, and pass the two wires down the fine metal tube previously positioned within the lamp tube. Screw l.e.d. housing assembly onto the top of the lamp tube
and feed through any excess wire from the l.e.d. assembly down into section C.

Drill a small hole through p.c.b. 2 at any convenient position, away from parts of the circuit bearing high voltage, and pass the lead wires from the l.e.d. and PCC1 through this hole. Insert xenon through the hole in the centre of section C, solder D6 and PCC1 wires to their positions on p.c.b.1. Add small blobs of silicone rubber to the perimeter of p.c.b. 2 and place the circuit board against the protruding flange of section C .

Check that B2 is connected and that D6 is operational. Connect battery B1 and place it into the base of section $E$, along with a small bag of silica gel to ab sorb any moisture that may build up inside the beacon once sealed. Add a small quantity of foam packing, if necessary, to prevent movement within the battery compartment.

Connect charger wires to the batteries, cover PCC1 and check that xenon flashes when the lamp housing is vertical. Connect up external charger circuit, insert energiser coil into the recess in the beacon shell and check that the charging l.e.d. (D5) operates.

Glue a piece of foam rubber to the base of p.c.b. 1 so that it helps support the circuit assembly. Smear silicone rubber onto the two mating surfaces of sections C and D , working the sealer well down into the eight screw holes, and screw the two sections together.

Hang the beacon in an inverted position and allow the silicone rubber seals to harden, during which time the charger may be inserted and the beacon is ready for use within 24 hours.

## NOTE ON BATTERY SUPPLIES

Considerable difficulty was experienced in obtaining ni-cad cells of suitable size and capacity. Varta are able to supply the 170 DK , of which eight are required (seven for xenon, one for l.e.d. flasher). However, since Varta impose a $£ 30$ small order charge, it was found to be more economical to obtain a couple of PP3 ni-cad equivalents. One "PP3" was used to power the xenon, the other was broken up and one cell* removed to power the l.e.d. flasher.

[^1]

The charger p.c.b. (above left) is mounted into a small plastic case. The top view of the Distress Beacon (above right) shows the light dependent resistor and the recepticle for the charger coil. Below, the shape of the charger coil can be seen.



The pòcket Distress Beacon is designed for non-nautical applications and as such, does not need to be in a sealed case. Therefore, the inductively coupled charger and the rechargeable batteries are not required and replaceable alkaline battries (the Duracell type) can be used.

Referring to the main circuit diagram in Fig. 1; all the circuit to the left of the batteries B1 and B2 is omitted (that is, D1 to D5, C1 to C6, R1 and R2, L1 and L2, VR1 and IC1). S1, the mercury switch, is replaced with a normal slide switch and the light dependent resistor PCC1 is also left off. All other componets are used and, for convenience, the references are unchanged on the constructional diagrams.

## P.C.B. ASSEMBLY

Preparation of the printed circuit board shown in Fig. 9 is fairly straight-
forward, the only special precautions necessary are in the mounting of the potcore for T1 and the two electrolytic capacitors forming C11.

These components need to be mounted in cut-outs in the p.c.b. due to the height restrictions imposed by the Lascar instrument case (as used for small hand held instruments) employed in the design.

The pot-core of T 1 is held together with a nylon screw. A nut is not used to secure the two halves together, but instead the end of the screw is passed through a spring steel clip (as used in the motor trade for securing self-tapping screws), and holding the pot-core firmly, the excess length of the nylon screw is trimmed, and the remaining stub melted over the clip with a hot iron.

The transformer was thoroughly sprayed with "Hots" ignition sealer to maintain interwinding insulation under
damp conditions, and it is suggested that when complete, the whole circuit board is treated with several coats of sealer also.

Resistors, diodes and dias should be inserted into the p.c.b. first as the capacitors will have to have their wire ends preformed in order that they may be mounted horizontally above neighbouring components.

The board is available from the $E E$ PCB Service, Order code 8309-05.

## LAMP REFLECTOR

The curved reflector for the xenon tube was made from a 1 mm thick sheet of plastic $70 \times 40 \mathrm{~mm}$. This was gently heated in hot water until pliable, then set into shape by pressing over the side of a spent 55 mm aerosol can.

When cool, the plastic sheet will retain its shape, and a 34 mm wide strip of aluminium foil may be glued into position as shown in Fig. 10 to form the reflective surface.

Fig. 10 shows the dimensions of the xenon tube terminal supports which are passed through the reflector sheet, and glued into position. Ensure that the aluminium foil does not come into contact with the xenon tube supports otherwise the power supply to the tube may be shorted out.

## POWER SUPPLIES

A 9V alkaline PP3 will fit snugly into the case in the space provided by Lascar. (Note: It was found that some makes of battery were too wide, and it was impossible to close the case completely. Allowing


Fig. 9. The full-size artwork and component layout of the Pocket Distress Beacon p.c.b. Note that the two capacitors forming C11 and the transformer T1 require cutouts in the board due to height restrictions placed by the Lascar hand-held case. Other large capacitors and the triac CSR1 will have to be carefully folded to mount horizontally above neighbouring components. The battery, B2, is mounted to spring clips soldered directly to the p.c.b. This board is available from the $E E P C B$ Service, Order code
$8309-05$.
for production spreads, Duracell and Berec were found to fit well.)

The positive wire from the battery clip is soldered to a point indicated on the layout diagram, Fig. 9, and the negative to the miniature slide switch terminal is also shown.

The l.e.d. flasher is powered by a 1.5 V battery (size AAA) and spring terminals should be soldered directly to the p.c.b. in order that the battery may be replaced easily.

The supply to l.e.d. D6 is taken from points located on the p.c.b. near to IC1. A small hole drilled through the mid-point of the top section of the case as shown in Fig. 11 is used to locate D6. This continuously flashing l.e.d. serves to locate the unit quickly if dropped or misplaced.

A lanyard should also be fixed to the casing, and this may be achieved by passing a length of nylon or polythene cord through a small hole drilled just below the casing catch. The end of the cord is then heated with a flame and flattened into a disc so that it will lie neatly between the battery and casing wall, and cannot be pulled out.

## REFLECTIVE STRIP

A Perspex or clear plastic sheet should be cut to fit the case to protect the xenon tube. There is plenty of room to bond and seal, with silicone rubber, the window to the inside surfaces to the case. To this window, on either side of the xenon tube, reflective strips (as used on disco equipment and "custom" cars) are glued into position and act as a signal device under sunny conditions.

Amber reflective plastic sheet, as used for car rear number plates, is stuck to the upper main surface of the case. The rear surface of the pocket beacon is covered with Velcro, and the fixing patch of the fluffy nylon should be sewn to a suitable area of the wearer's anorak or rucksack.

It is advised that the mating surfaces of the casing halves are covered with sticky tape to prevent moisture entering, and a rubber gasket cut to fit over the slide
switch, and positioned between the switch and internal casing.

## OPERATION

In case of difficulties encountered while climbing or walking, the unit described will only be of use if used sensibly. Depending upon the quality of components used, and the care taken in winding T1, battery life will be found to be between three and five hours in the continuous flashing mode.

All walkers and climbers should always leave details of their itinerary with a friend or official so that action may be taken should they become overdue, and under such circumstances the pocket beacon used at regular intervals of time will remain usable far in excess of the three to five hours continuous running.

At temperatures below freezing, the beacon should be kept in a pocket until required to prevent the battery voltage


Fig. 10. Constructional details of the xenon tube reflector. The tube supports should be formed from well insulated, stiff wire.
from falling to a point where the unit would become inoperative. It is wise to carry a spare battery for your beacon at all times-as well, of course, for your conventional torch.


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AST month we discussed the mechanism by which conduction occurs in pure semiconductor materials. Conduction in such materials is known as "intrinsic conduction". In practical devices pure or intrinsic semiconductor materials are seldom used. Such devices employ semiconductor materials containing very small amounts of impurity elements which modify their properties considerably.

This morth we shall consider how conduction occurs in semiconductor materials containing small amounts of doping elements as impurities. There are two main types of such materials, the one being known as $n$-type in which the charge carriers are the negative electrons and the other is known as $p$-type in which the charge is carried by positive holes.

## $N$-TYPE MATERIAL

Let us first consider a silicon crystal lattice similar to the one we discussed last month but, as shown in Fig. 5.1, one of the atoms of silicon ( Si ) has been replaced by an atom of arsenic (As). When the arsenic has formed the four bonds to
neighbouring silicon atoms, it still has an electron left over which is not taking part in the bond formation.

This extra electron present in the arsenic atom is not so firmly attached to the arsenic atom as any of the other electrons in the lattice. In other words only a small amount of thermal energy needs to be acquired by the electron to enable it to leave the arsenic atom and to wander through the crystal lattice. Thus this electron can lead to conduction in the crystal.

The conductivity of silicon can be greatly increased by the addition of very small amounts of elements such as arsenic. Even one part of arsenic in ten million parts of silicon or germanium can greatly affect the electrical properties of the material. The number of electrons provided by the impurity atoms is usually far greater than that provided by movement of electrons from the valency band to the conduction band.

The impurity atom in Fig. 5.1 provides an electron without creating a hole, since the arsenic atom has its full complement of four electrons in chemical combination after the extra electron has departed.

Conduction in materials of this type is therefore almost entirely by negative electrons, the small number of holes providing a very small contribution.

Silicon doped with arsenic is known as $n$-type material, since negative charge carriers (electrons) carry the current. The arsenic atoms are known as "donor" atoms, since they donate electrons to the silicon lattice.

## N-TYPE ENERGY BANDS

The energy band diagram of an $n$-type material is shown in Fig. 5.2. It should be noted that this is rather similar to the energy band diagram for a pure semiconductor material, but there are two important differences. The first is the presence of the donor band just below the conduction band.

Only a small amount of energy is required to lift an electron from the donor band into the conduction band. This results in the second difference in the energy band diagram, namely that there are many more electrons in the conduction band than in the case of the pure semiconductor material.

## $P$-TYPE MATERIAL

Another type of semiconductor material is shown in Fig. 5.3, in which the central atom of silicon ( Si ) has been replaced by an impurity atom of boron (B). The boron atom has only three valency electrons as opposed to the four of silicon. There is therefore a hole or vacancy by the boron atom which can be filled by an electron from the valency band of another atom.

As shown in Fig. 5.3, an electron can move from a neighbouring silicon atom to fill the hole by the boron atom, in which case the boron atom will have four chemical bonds to neighbouring atoms instead of the three shown in Fig. 5.3. In addition, the silicon atom which donates the electron to the boron will have a hole left by it and an electron from another silicon atom can fill this hole. Thus this movement of electrons can be considered as a movement of a hole, as discussed last month.

Silicon or another semiconductor material containing atoms of an element like boron as an impurity, is known as $p$-type material, since conduction is considered to occur by the movement of positive holes (although it is actually the movement of many negative electrons which causes the conduction).

## P-TYPE ENERGY BANDS

The energy band diagram of a semiconductor material containing impurity atoms such as boron, is shown in Fig. 5.4. The energy band gap of the material is essentially unchanged, but a new acceptor band has appeared just above the valency band. This acceptor band can accept electrons from the valency band and leaves holes in the latter which greatly increases the conductivity.

It should be noted that boron is orly
one example of an acceptor type of im purity atom which produces $p$-type material; ahother example is indium. Similarly, arsenic is one example of a donor impurity atom which produces $n$-type material; others are antimony and phosphorus.

## CARRIER RECOMBINATION

When electrons and holes meet in a semiconductor material, the electron normally fills the hole. Thus electrons and holes are continually being removed as fast as they are created by thermal or other energy.

If a semiconductor material contains both $p$ - and $n$-type impurities, the two types produce opposite effects and more or less neutralise one another. If there are more $p$-type impurity atoms than $n$-type, the material will be $n$-type.

In a $p$-type material the number of free electrons will be far less than in the pure intrinsic material. This is because there are a large number of holes in $p$-type


Fig. 5.2. Energy band diagram of an $n$-type semiconductor material.
material and any electron moving through the material will probably combine with a hole very quickly and be removed.

Similarly, in $n$-type material the number of free electrons is large, so any hole moving through the material will quickly be removed as it meets one of the electrons. The product of the hole and the electron concentration is constant for a given semiconductor material at a given temperature, no matter what impurities are present provided that the impurity concentration is small.

## SEMICONDUCTOR JUNCTIONS

A semiconductor junction device may be made by employing $p$-type material on one side of the junction and $n$-type material on the other side. However, this type of junction must be fabricated as a single crystal-it is quite useless bringing a piece of $n$-type and a piece of $p$-type material together.


Fig. 5.4. Energy band diagram of a $p$-type semiconductor material.

As soon as a $p n$ junction is formed, holes from the $p$-type material and electrons from the $n$-type material diffuse across the junction. The electrons from the $n$-type material fill some of the holes in the $p$-type near the junction, whilst the holes from the $p$-type combine with some of the electrons in the $n$-type.

This flow of charge results in the type of picture shown in Fig. 5.5. The passage of positive charges to the $n$-type and negative charges to the $p$-type, results in the $n$-type material having a positive potential with respect to the $p$-type material.

## JUNCTION POTENTIAL

There is therefore a potential across the semiconductor junction. Any more positive holes from the $p$-type material which tend to approach the junction are repelled away from the junction region by the positive charge of the $n$-type. Similarly, any electrons in the $n$-type moving towards the junction are repelled back in the $n$-type region by the negative charge of the $p$-type.


Fig. 5.5. A pn semiconductor junction. The charges shown encircled are fixed in position by chemical bonds, whilst the others are free to move.

Fig. 5.1. A silicon lattice containing an $n$-type impurity atom.


Fig. 5.3. A silicon lattice containing a p-type impurity atom.



Fig. 5.6. Semiconductor junction diode characteristic showing the rapid increase of forward current with forward voltage. The reverse breakdown can be made to occur at quite high voltage.

There is therefore a region on each side of the junction which is depleted of charge carriers; this is known as the depletion region.

## RECTIFICATION

If a potential is applied across the $p n$ junction of Fig. 5.5 so that the $p$ type material is made more negative and the $n$ type more positive, the electrons in the $n$ type material and the holes in the $p$-type material will be repelled even further from the junction so that the depletion region is increased in depth.

As no charge carriers are present in the junction region when a voltage of this polarity is applied, no current can flow and the diode is biased in the reverse direction. Actually a very small current (known as the leakage current) flows, since the $n$-type material does contain a
very low concentration of holes whilst the $p$-type material contains a small concentration of electrons.

The increase in the depth of the depletion region with reverse bias, results in a decrease in the capacitance across the junction, since the depletion region acts as an insulating region between the $p$-type and $n$-type materials, whilst these materials act as the plates of a capacitor. Thus a reverse biased junction diode can be used as a semiconductor variable capacitor the value of which is controlled by the applied reverse bias.

## FORWARD BIASING

If, however, a voltage is applied to the junction diode in the other direction, that is with the $p$-type positive and the $n$-type negative, a different type of behaviour can be observed. The electrons in the $n$ -
type material are attracted towards the junction and the holes in the $p$-type material are attracted towards the junction also.

If the applied forward bias is not great enough to overcome the natural junction potential of the unbiased diode (about 0.55 V in silicon) the diode will not conduct. When the forward voltage exceeds this value, however, the charge carriers are attracted across the junction and a forward current flows. The size of this forward current increases very rapidly with increasing voltage as shown in Fig. 5.6.

It can be seen that the reverse current remains very low until the junction undergoes breakdown, afer which the reverse current rises very rapidly with voltage.

The small reverse current which flows before the breakdown voltage is reached is due to minority carriers (that is electrons in the $p$-type and holes in the $n$ type). The concentration of these charge carriers increases rapidly with temperature and therefore the leakage current also increases rapidly with temperature. The leakage current is much smaller in silicon than in germanium diodes owing to the larger energy band gap.

## OTHER DEVICES

Semiconductor junctions are present in almost all semiconductor devices. For example, two junctions are present in junction transistors (either npn or $p n p$ ), so many of the principles we have discussed can be applied to junction transistors.
to a pre-determined value, determined by R1, the relay RLA is turned on. The relay can be used to fire an alarm or activate an electrically-operated valve, which feeds water to the plant.

The probes are two thick wires or rods 120 mm long, the upper ends of which are fixed to a piece of non-conducting board of suitable size (say, $40 \times 20 \mathrm{~mm}$ ). The rods should be straight and parallel with a separation of 15 mm from each other.
J. Sreekumar, Cochin, India.

## AUTO WATER FEEDER

[Eeding water to plants can be automatically done using the circuit shown. When the soil moisture level falls

## practical <br>  <br> PROJECTS <br> Computer Memory Error Correction Board 555/741 Tester <br> Guitar Active Tone Controls <br> Ground Communication System <br> Logic Analyser Part 2

## Plus

UK Satellite TV System Explained
Microfile looks at the $\mathbf{Z 8 0 0 0}$
and all the regulars including
BAZAAR (free readers advertisements),
SPACEWATCH, INGENUITY UNLIMITED ON SALE NOW

## Video Facts

Sound is now becoming a major selling point in video. But much of what is written, and advertised, glosses over important facts. Here's what you should know if you are buying or renting a new video recorder and want good sound.

As l've previously explained, it's well nigh impossible to get genuine hi fi stereo sound from a domestic video cassette recorder. The stereo tape tracks are so narrow (around 0.25 mm wide) and the tape speed so low (around $2.5 \mathrm{~cm} / \mathrm{s}$, or half audio tape cassette speed) that it's a wonder that we can get anything at all

The VHS manufacturers all use the Dolby B system, to improve signal-to-noise ratio. Some manufacturers, for instance National Panasonic, started to use the Dolby C system to cut tape hiss even further. But, unlike Dolby B, Dolby C isn't really compatible

In other words although a Dolby B recording sounds reasonable when replayed on a machine without Dolby B circuitry, a Dolby C recording sounds distinctly odd. So Dolby C isn't finding favour.

Likewise the Sony Beta format noise reduction system, BNR or Beta Noise Reduction, isn't popular. It's more like DBX than Dolby, and thus cuts hiss quite dramatically. But like all powerful noise reduction systems, it can sometimes be heard breathing or pumping. Background hiss comes and goes with pulsating speech or sound.

It's a great pity that the Beta manufacturers, for political rather than technical reasons, wouldn't use Dolby. The inadequacy of BNR is why it's still difficult to get pre-recorded Beta video tapes with stereo sound.

Some of the software companies have been so unhappy with BNR that they've held back on Beta stereo releases. ThornEMI, for instance, took a decision to delay on Beta stereo releasing even though the company's catalogues had already been printed to advertise some films as available in Beta stereo.

Secretly the software companies are banking on the British launch of Beta hi fi. This system, now on sale in the US, puts genuine hi fi stereo sound into the video waveform as an f.m. signal. Expect a Beta hi fi launch in Europe later this year, or early next year, when it will give a shot in the arm to the Beta system.

## Double Take

Meanwhile the VHS manufacturers have launched two-speed machines. These can run the tape at half speed to double the recording and playing time available from a single cassétte.

This means that a VHS recorder can now offer up to eight hours continuous playing time from a single, standard VHS cassette. This move is, of course, meant to compete with the V2000 format, which offers up to eight hours from a single cassette, but with the need to flip it over halfway through like an audio cassette.

The half speed machine is cleverly designed. You only need to switch the speed when you are recording, and you can do so even halfway through a programme. On replay the machine senses the speed at which the tape was recorded, and automatically switches replay speed accordingly.

There's just a short break up of the picture on screen. The snag of course is that because tape speed is halved, to around one quarter the speed of audio cassette tape speed, sound quality is very poor.

## Simulcasts

Anyone buying a stereo machine will eventually want to tape simulcasts with it.

Because we have no stereo sound with television in Britain, and are unlikely to have it for several years, the TV stations sometimes team up with f.m. radio stations to transmit separate stereo sound and pictures.

You can record a/stereo video tape of a simulcast by feeding the sound in from a stereo hi fi system while taping the picture off-air through the video recorder's own tuner. But to do this easily you need a simulcast switch, which temporarily disconnects the mono sound tuner inside the video recorder.

The VHS stereo machines sold by JVC and Ferguson have this feature, but others don't. For instance, to tape simulcasts with the Sony C9 Beta stereo recorder or the Grundig V2000 stereo recorder you need to plug and unplug extra wires bèfore and after each recording. This is a clumsy approach, especially on the Grundig machine, where the plugs are on the rear of the machine.

Philips promise a V2000 stereo recorder late this year with a simulcast switch. It's coming late on the market because the Philips factory is having to make special models for Britain with this feature.

## Teleprompting

If you thought that some politicians at the last election hustings were unusually sincere and eloquent, there's something you should know. There's a gadget, made by an American company called Q-TV, which lets a public speaker look an audience straight in the eye, while reading from a script displayed on a television screen.

The system is invisible unless you know how to spot it. Watch out for the plain glass sheet that's in front of Prime Minister Margaret Thatcher next time you see her eloquently talking into the middle distance. Actually, it's a two-way mirror that lets the audience see her, while she reads text from a video screen.

Teleprompting is an old idea. Actors have found that it's often impossible to learn a script in the short time available for a TV performance. In the early days, the words were written on idiot boards, large notices propped up around the studio.

Some actors depended entirely on this kind of help. If you remember the old Tony Hancock programmes, you may also remember seeing his eyes wander round the set until they fell on the nearest prompt board.

Then came Autocue, a clever system that lets an actor look straight at the camera, while reading a script. Peter Dimmock of the BBC was experimenting with it 30 years ago, but a commercial system was slow to arrive.

Before transmission the script is typed or written on a long roll of anti-glare paper. In a corner of the studio, or hidden back room, the paper is unrolled in front of a video camera, and this camera output fed to a TV monitor underneath the studio camera. This is shooting through a two-way mirror angled so that it sees only the scene to be televised, but an ac tor looking at the camera sees only a reflected image of the prompt screen.

Now, all television studios rely heavily on Autocue systems. Although actors still learn their words for drama productions, the announcers and link men and women you see on news and current affairs programmes are
almost always reading their words from an Autocue screen on the camera.

Once in a while the Autocue goes wrong, for instance the roll of text jams, or the operator runs it through at the wrong speed, or there's a technical failure. That's when you'll see a look of panic in the link person's eye, while they switch to ad-libbing or try to read from a script on the desk in front of them.

## Audience Contact

The Q-TV system was developed to give business speakers and politicians the equivalent of Autocue, but at public meetings where there's no life-saving TV camera with Autocue to gaze at. The script, or often just a list of crucial statistics and facts ${ }_{9}$ is again typed on anti-glare paper, in an or̈dinary typewriter or word processor.

Behind the scenes the speaker's assistant moves from page to page to suit what's being said out front, while a closed circuit TV camera displays an image of the text pages on a TV monitor hidden underneath the speaker's table. This screen image is reflected up by a two-way mirror which is set at an angle in front of the speaker's eyes.

If the audience sees anything, it's just a plain glass plate a few feet in front of the politician's eyes. But the politician sees the prepared text running through on a television screen. So he or she can pretend to look straight at the audience, while quite literally pulling facts and figures out of the air.

The Q-TV system was developed in America, because over there they learned early about the need to simulate eye-to-eye contact with an audience. It's now used by firms like $3 \mathrm{M}, \mathrm{RCA}$, Con Edison, General Motors and Ford, to help their public speakers look more sincere.

President Reagan uses one and that's how Margaret Thatcher found out about the idea. Now an enterprising British firm is importing the system for any politician over here who sincerely wants to look sincere.

# MICROCOMPUTER INTERFACIIG TECHIIOUES INCLUDING MANY USEFUL CONSTRUCTIONAL PROJECTS PART THREE: ANALOGUE-TO-DIGITAL CONVERSION 

## BY J. ADAMS B. Sc. M. Sc. \& G. M. FEATHER b. Sc.

$P$Hysical quantities in the world outside the microcomputer are generally analogue in nature; that is, they vary in a continuous way. In the previous article electrical signals representing analogue quantities, such as light intensity, have been recognised by the microcomputer when the particular value achieves some predetermined threshold level.
If the actual magnitude of such a quantity is to be "logged" by a microcomputer then the analogue electrical signal developed by a transducer is often conditioned before being converted into a digital form which the microcomputer can accept. This is illustrated in Fig. 3.1.

## TRANSDUCER

The transducer senses the magnitude of the quantity to be measured and produces a corresponding analogue electrical representation.

As an example, a temperature sensing device could be employed which produces a 0 to 1 V output voltage which varies with temperature over a temperature range of say 0 to 100 degrees Celsius.

## SIGNAL CONDITIONING

The analogue signal obtained from the transducer will convey the signal's information content but it may require amplification before it can be presented to an analogue-to-digital converter.

Signal conditioning circuits, often employing operational amplifiers, are generally present in data acquisition systems.

## ANALOGUE-TO-DIGITAL CONVERTER

An analogue-to-digital converter (ADC) is a device which will convert a suitable analogue input into a corresponding digital output code.

## SUCCESSIVE

## APPROXIMATION

 CONVERTERSVarious techniques exist for the conversion of the analogue signal obtained from a suitable transducer/conditioning circuit into a digital form which can be recognised by a microcomputer.

The successive approximation ADC offers one of the best combinations of ease of application and economy allied with speed of conversion and resolution.

The technique resembles, in some respects, the way in which many physical quantities are measured-by trial and error.

This analogy can be employed to give some understanding of the working of a successive approximation ADC.

Consider the determination of an unknown mass by comparison with known
masses using a beam balance as shown in Fig. 3.2.

When the trial masses equal the unknown mass, the pointer will be at zero deflection. The trial masses are arranged to be related by powers of $2: \mathrm{M} / 2, \mathrm{M} / 4$, M/8, M16, and so on.

Measurement is started by placing the largest trial mass on the left-hand pan; if $\mathrm{M}_{\mathrm{x}}>\mathrm{M} / 2$, the pointer moves towards the right and the result of this test determines whether we increase the trial mass or make another trial with the next smallest mass. In the successive approximation ADC, the test is made with a trial voltage (developed from an accurate fixed reference voltage) and the result will set a bit in a register to logic 0 or 1 , depending on the result.

The trial process is continued until the scale is balanced, each decision setting a less signficant bit in the register to a 0 or 1 , so building up the digital representation of the analogue quantity.

Converters offering resolution to 6,8 , 10 and 12 bits are available but 8 -bit devices offer a satisfactory compromise between resolution and conversion time (the Ferranti ZN448 can perform more than $110 ; 000$ conversions per second with a resolution of 1 part in 256, or about 0.4 per cent).

All internal operations of the ADC are run sequentially by the system clock

Fig. 3.1. Analogue-to-Digital Converter (ADC) and data acquisition.


Fig. 3.2. Successive approximation balance analogy.

which hence determines the overall conversion time. A complete conversion occupies nine clock cycles so, for example, a ZN448 running at a clock frequency of 900 kHz will give a conversion time of $10 \mu \mathrm{~s}$ and therefore a conversion rate of $100,000 \mathrm{~s}^{-1}$.

## OPERATING PROTOCOL

To use any type of ADC, a precise sequence of operations must be adhered to. A simplified description of this is given below for the successive approximation type.

## STEP 1 START CONVERSION

An output pulse from the microcomputer instructs the ADC to initiate a conversion. (This arrangement is sometimes referred to as "convert on command".)

## STEP 2 END OF CONVERSION

An output signal from the ADC instructs the microcomputer that the conversion has been completed.

The digital representation of the analogue quantity is, at this moment, stored in the output latches of the ADC, but the actual output lines are, in many converters, tri-state configured. That is, unless instructed to do so, the ADC will not output the digital information on to its output lines-they will remain in a high impedance state, effectively disconnected from the microcomputer so as not to interfere with other signals which might be present on these lines.

This is important when the lines are required for other purposes such as channel multiplexing, software control of signal conditioning amplifier gain, and so on.

## STEP 3 ENABLE TRI-STATE

An output signal from the microcomputer instructs the ADC to make available at its output lines, the data present in its output latches.

The microprocessor is now in a position to read the binary code presented by the ADC.

## STEP 4 DISABLE TRI-STATE

The enable output signal from the microcomputer reverts to its original state, sending the bus lines to a high impedance state. The cycle is now complete and the ADC awaits the next start conversion pulse before repeating the sequence.

## HIGH SPEED <br> SINGLE CHANNEL BIPOLAR CONVERTER

The ADC employed in this project is a successive approximation Ferranti ZN449 device in a standard 18 -pin d.i.l. package as shown in Fig. 3.3. The 8 -bit binary code presented by the ADC to the microcomputer will hence allow the resolution of an input analogue voltage in 256 discrete steps. This " 8 -bit" resolution is simply the number of bit outputs that the ZN449 possesses and implies nothing about the accuracy of the device.

The device offers a fast conversion rate (up to approximately 110,000 conversions per second), an on-chip clock and reference voltage, tri-state outputs and +1 l.s.b. resolution.
Conversion time lasts for nine clock cycles giving the same maximum conversion rate as that for the ZN448.

## CIRCUIT DESCRIPTION FOR BIPOLAR OPERATION

The complete circuit diagram of the High Speed Analogue-to-Digitai Converter is shown in Fig. 3.4.

The A-D device, IC2, requires, in addition to the usual +5 V supply, a negative rail which in the circuit given, stands at about -3.5 V . This could, of course, be derived from a second battery but in the circuit, Fig. 3.4, uses IC1 as a simple multivibrator, the output of which is rectified and smoothed by the D1, D2, C2, C3 network; this provides the necessary negative supply.

ADCs may be wired to give either "unipolar" or "bipolar" operation. In the unipolar case, only positive input voltages (that is, those which make $\mathrm{V}_{\text {IN }}$ positive with respect to ground) will produce a digital output. The design used in the project employs the bipolar technique in which the input voltage is offset by $+\mathrm{V}_{\mathrm{REF} / 2}$ so that the voltage at the input to the device itself will always be positive, even when $\mathrm{V}_{\mathrm{IN}}$ goes down to $-\mathrm{V}_{\mathrm{REF} / 2}$. The offset voltage is derived from $+V_{R E F}$ by the resistor network R1, R2, and VR2.

Bipolar operation is useful in that it allows the converter to sample values of an alternating voltage. As both positive and negative voltage ranges are encountered it is necessary to provide a method of digital coding to represent this. The coding most widely employed is referred to as "offset" binary. The line connected to the m.s.b.-the line with the greatest "weighting"-is used to indicate the sign of the voltage. When the m.s.b. (bit 7) is set and the line goes high then the input

Fig. 3.4. Circuit diagram of a single-channel high speed Analogue-to-Digital Converter (ADC).

voltage to the ADC is positive，whereas if the m．s．b．is clear and the line goes low then the input voltage is negative．

Table 3.1 below indicates the logic coding for bipolar operation．

VR1 and VR2 provide for adjustment of the output for inputs of plus and minus

Table 3.1

| Analogue Input | Digital Output |
| :---: | :---: |
| ＋（FS－ 1 l．s．b．） | 11111111 |
| ＋（FS－ 2 l．s．b．） | 11111110 |
| ＋1／2 FS | 11 10めめめ口 |
| ＋ 1 l．s．b． | $1 \varnothing \emptyset \emptyset \emptyset \emptyset \emptyset 1$ |
| $\emptyset$ | $1 Ф \square \emptyset \square \emptyset \square \varnothing$ |
| －1 I．s．b． | Ф1111111 |
| －1／2 FS | Ф1øøøø币ந |
| －（FS－ 1 l．s．b． | ФФФФФФФ1 |
| －FS | ФФФФФФФФ |

FS＝Full Scaie．
$( \pm)$ full scale．A detailed description of the setting－up procedure is given．The resistor values given produce an analogue input voltage range of -5 V to +5 V ．

In the design，the eight data lines （which are connected to $\mathrm{P} \emptyset-\mathrm{P} 7$ ）are used solely for the purpose of applying the out－ put of the ADC to the user port and， although the ZN449 does have tri－state outputs，the tri－state enable pin is kept at logic 0 （ground）so that the output latches are always enabled．

## READING CONVERTED DATA

During a conversion，invalid data will thus be present on the output lines and the microcomputer must not read the data available until the conversion is complete．When operating in Basic，this does not present a problem since the con－
version is completed well before the machine is ready，but for machine code operation it is necessary to employ the end of conversion（EOC）signal generated by the ADC to instruct the microcom－ puter to read the then valid data．

The EOC signal can be applied to the edge－sensitive handshake line as was dis－ cussed in the first article in this series． Readers should note that the Commodore 64 microcomputer requires a negative going edge－sensitive handshake and con－ structors intending to use this machine should include the inverter，IC3，in the EOC line．For the other machines，the LINK should be included and IC3 should not be fitted．

IC4 is a 78 L 055 V regulator and need only be fitted if a 5 V supply is not available at the user port．In this case，an external 9 V supply for the board may be used；a PP9 battery will allow many hours of operation．

# HIGH SPEED ANALOGUE－TO－DIGITAL 

 CONVERTER BOARD CONSTRUCTION
## CONSTRUCTION OF ADC

All the components are accommodated on a single－sided printed circuit board， size $150 \times 95 \mathrm{~mm}$ ．The full－size master p．c．b．pattern to be etched is shown in Fig．3．5．This p．c．b．is available through the EE PCB Service，Order code 8309－ 01 ；see page 565 for further details．

The layout of the components on the topside of the board is shown in Fig．3．6． The 12 leads from the board to the user port connector are soldered directly to undrilled oval areas on the track pattern after feeding through 1.3 mm diameter holes near to the board edge．This acts as a strain relief mechanism for the soldered connection．

Begin assembly by soldering in the i．c． sockets，followed by the link wire， Veropins，resistors，presets，capacitors and finally the semiconductors．Pay at－ tention to the orientation of IC4，the two diodes and the electrolytic capacitors．

The p．c．b．has been designed to be used with the BBC Micro，PET，VIC－20 and Commodore 64 computers．The latter re－ quires a negative going edge－sensitive
handshake．This is provided by the inver－ ter in IC3．This i．c．should not be inserted if the board is to be used with the other three machines，but a link wire must be fitted between IC 3 socket positions 8 and 9．This is the＂dotted＂link in Fig．3．4．

There is no +5 V supply rail available on the PET computer user port，but IC4 with C 6 and C 7 provide a suitable +5 V supply for the on－board components when a 9 V battery such as a PP9 is con－ nected．These components may be omit－ ted if building this project for any of the other computers mentioned above．

Finally，connect the user port cable to the p．c．b．according to Figs． 3.6 and 3．7．

## SETTING UP PROCEDURE

The ADC board should be connected to the microcomputer via ribbon cable and appropriate user port plug as shown in Fig．3．7．If necessary，an exernal battery may be added．

The simple Basic program TEST， appropriate to the particular microcom－ puter，should now be loaded into the
microcomputer．When run，the screen will display a number in the range 0 to 255. （This is the decimal value of the 8 －bit binary number at the outputs of the ADC．）

It is first necessary to apply -4.98 V （ -5 V will do）to the analogue input and adjust the offset control VR1 until the display just flickers between 0 and 1 ．

The input connections should now be reversed and the gain control VR2 ad－ justed until the display flips between 254 and 255．The first step should now be repeated．

Finally，the analogue input terminals should be short－circuited，when the dis－ play should read 128 ．If this is not the case，then repeat the whole procedure．

## STORAGE OSCILLOSCOPE

With the very fast conversion rate offered by the ZN449 periodic waveforms of frequency up to a few kilohertz may be sampled several times per cycle and stored in immediate access memory（rAM）for subsequent display at a convenient time．


Fig. 3.5. Master pattern (actual size) to be etched for the ADC board. This board is available through the EE PCB Service,
Order code 8309-01.


O = VEROPINS
Fig. 3.6. Layout of the components on the topside of the p.c.b. with interconnection details to the user port connector.

BACK OF USER PORT


Fig. 3.7. Connection details for the user port connector cable with, in brackets, connection to ADC board. Top gives details for BBC Micro, lower for PET/VIC20/Commodore 64. *-CB2 on VIC-20 and PET, PA2 on Commodore 64; **-CB1 on VIC-20, CA 1 on PET, FLAG 2 on Commodore 64.


## ADC TEST ROUTINES

5 FEH EEC TEST FFOHFFHOT

```
10 % 5512%=5
```



```
O-51%W=6
40 FFJHT FE512Q
5a buTO 3a
```

5 EEM FET TEST FROUFTM
16 FWKE 59459. 9
2 CH POE 59468. PEEKC5946g20R192


50 FOKE 594FS, PEEKCSO4ESORS2
6. FFIHT FEEKG5946?

76 GOTO SO

5 FEM WTM-2日 TEST FFODFFM
16 FOtE ST1OE.

39 POKE ST148,FEEKC3714EODF192



TG FEIHT PEEK STISE
30 GOTO 40

10 FOKE 555\% E
20 FOEE 5657P, FEEKCEESTSOOF4


50 FOKE 56576, FEEKCSESTGOR4
E0 FFIHT FEEK 56573
YG GOTO 30

This opens up the attractive possibility of using the system as a relatively inexpensive storage oscilloscope. For this very fast application of the $A D C$, machine code software is esseritial, and normally involves incrementing an index register and storing perhaps 256 digitised points in contiguous memory locations using indexed addressing.

Even non-periodic waveforms, such as one-shot impulses, can be sampled at great speed if the storage of results occurs only when the analogue signal exceeds a "trigger" level.

One very interesting arrangement is to connect a high output crystal/ceramic microphone to the ADC via the signal conditioning amplifier described in this article. Instantaneous values of a speech waveform can then be sampled and stored before being displayed either on a vDU or printer.

Further use of this board, including another constructional project for fast data logging, will be dealt with later in this series. Software will be available through the EE Software Service for those wishing to implement machine code software with their ADC board.

| Resistors |  |
| :---: | :---: |
| R1,2 | $12 \mathrm{k} \Omega$ |
| R3 | $330 \Omega$ |
| R4 | $7.5 \mathrm{k} \Omega$ |
| R5 | $47 \mathrm{k} \Omega$ |
| R6 | $390 \Omega$ |
| Rll 1 W |  |
|  |  |

Capacitors
C1 $\quad 10 \mathrm{nF}$
C2,3 100nF polyester type C280 (2 off)
C4 $\quad 100 \mathrm{pF}$ ceramic plate
C5,6 $\quad 1 \mu \mathrm{~F} 6 \mathrm{~V}$ elect.
C7 100nF polyester type C280

Semiconductors
IC1 7414 TLL hex Schmitt inverter
IC2 ZN449, 448 or 447 8 -bit analogue-todigital converter
IC3 7414 TL hex Schmitt inverter
IC4 78L05 100 mA voltage regulator
D1,2 1N4148 small signal silicon

## Misciellaneous

VR1,2 $5 \mathrm{k} \Omega$ cermet multiturn preset ( $\frac{3}{8}$ inch square)

Printed circuit board: singlesided size, $150 \times 95 \mathrm{~mm}, E E P C B$ Service, Order code 8309-01; 14pin d.i.). sockets (2 off); 16-pin d.i.l. socket; single-sided Veropins ( 4 off); self-adhesive rubber feet ( 4 off); ribbon cable and connector to suit computer used.

Approx. cost Cuidance only

## $\mathbf{E 1 3}{ }^{\text {orclusidig }}$

cable/connector

## SIMPLE EXPERIMENTS WITH THE ADC

## Joystick Potentiometer

The simple potentiometer circuit of Fig. 3.8 should be set up and the output connected to the analogue input terminals of the ADC. Rotation of the potentiometer spindle should cause the digital value displayed on the screen to vary over a range of approximately 25 to 230 corresponding to analogue inputs of -4.5 V to +4.5 V . It is left to the reader to devise a simple program in which this digital value of the analogue voltage can be used to control the $y$ (or $x$ ) coordinate of a plotted point.

## Optical Intensity Measurement

The circuit of Fig. 3.9 employs an ORP 12 light dependent resistor (l.d.r.) in a simple potential divider arrangement. When fully illuminated, the resistance of the l.d.r. falls to a value of about 100 ohms; the output voltage from the potential divider will then be approximately
equal to the supply voltage, 4.5 V . When dark the l.d.r. resistance increases to approximately $100 \mathrm{k} \Omega$, thus decreasing the voltage across R1 to almost zero. As this transducer provides a positive analogue input to the ADC the digital output representation is limited to the range 128 to 255 , inclusively.

The output voltage and optical intensity are not related linearly, but if a photometer is available, the arrangement may be calibrated. The authors have employed this technique in colorimetric applications.

## Simple Temperature Measurement

In the temperature measurement application, a thermistor forms part of a potential divider arrangement, the positive output voltage of which depends upon the temperature of the thermistor. The circuit is shown in Fig. 3.10.

As with the previous case, the output voltage is not proportional to the temperature of the thermistor. A calibration curve, Fig. 3.11, is given for the particular type used, and can be held in immediate access memory (RAM) as a calibration table. The software solution to nonlinearity involves searching the table until the largest input value corresponding to a particular temperature reading is found.

## SOFTWARE

The eight lines available at the user port must be configured for input if the microcomputer is to accept the eight bits of digital information from the ADC. This is achieved by clearing each bit in the data direction register for the port.

| BBC Model B | VIC-20 | PET | Commodore 64 |
| :---: | :---: | :---: | :---: |
| $? 65122=\emptyset$ | POKE37138, $\emptyset$ | POKE59459, $\varnothing$ | POKE56579, $\varnothing$ |



Fig. 3.8. Simple joystick for the ADC.


Fig. 3.10. Temperature transducer for the ADC.


Fig. 3.9. Optical transducer for the ADC.


Fig. 3.12. Form of CB2 Start Conversion (SC) pulse.
operation of the CB2 line. In pulse output mode the bits are configured as shown in Fig. 3.13. Once this has been achieved by

## ? $65132=$ ? 65132 AND160

then any write to the I/O register, for example, $? 6512 \emptyset=\emptyset$ will send a one-shot negative pulse from pin 4 to the start conversion pin of the ADC and initiate conversion.

## PET

Pin M (CB2) at the user port can act in much the same way as the BBC Micro CB2 line. The three bits PCR7, PCR6, PCR5 in the Peripheral Control Register (PCR) are again used to characterise the mode of operation of the line. The register is located at memory address 59468. In this case two further modes of operation will be used to send the start conversion (SC) pulse to the ADC. Manual output mode is first selected by setting bits PCR7 and PCR6 in the PCR. This can be achieved as follows:

## POKE59468,PEEK(59468)OR192

Note that the logical or operator is used to ensure that only bits PCR7 and PCR6 are set. This must be done independently of the other bits in the register which are used for other purposes.

Once this mode has been selected, the CB2 line will reflect the state of bit PCR5, that is, if PCR5 is set then the CB2 line is held high, but if PCR 5 is clear then the CB2 line is held low.
The following coding illustrates how a
start conversion pulse can be fashioned by manipulating the contents of the PCR with logical operations.

10 POKE59468,PEEK(59468)OR192:REM CB2 MANUAL OUTPUT MODE $2 \emptyset$ POKE59468,PEEK(59468)OR32:REM CB2 HIGH
$3 \emptyset$ POKE59468,PEEK(59468)AND223:REM CB2 LOW
40 POKE59468,PEEK(59468)OR32:REM CB2 HIGH

## VIC-20

The start conversion pulse can again be derived from the CB2 line at pin M. The Peripheral Control Register is located at memory address 37148. For example:

10 POKE37147,PEEK(37147)AND227: REM DISABLE SHIFT REGISTER $2 \emptyset$ POKE37148,PEEK(37148)OR 192: REM CB2 MANUAL OUTPUT MODE 30 POKE37148,PEEK(37148)OR32: REM CB2 HIGH
40 POKE37148,PEEK(37148)AND223: REM CB2 LOW
$5 \emptyset$ POKE37 148,PEEK(37148)OR32: REM CB2 HIGH

## COMMODORE 64

The Commodore 64 has no CB2 line available at the user port. Bit 3 of Port A (PA2) is, however, brought out at pin M. To configure this line for output it is necessary to set the corresponding bit in the Data Direction Register for the port located at memory address 56578.

The start conversion pulse can then be derived by setting, clearing and re-setting bit 3 in the I/O register for the port. This can be achieved as follows:

## SIGNAL CONDITIONING AMPLIFIER

The electrical signals from some transducers may be too small to feed into A-to-D Converter circuitry to obtain satisfactory results. A simple signal conditioning amplifier which will give boost to the transducer signal by factors of $\times 10$ and $\times 100$ is shown in the circuit diagram of Fig. 3.14.

The circuit is based around a differential operational amplifier i.c., IC1, the much used 741. This is arranged in the "non-inverting" mode. R1 sets the input impedance at 75 kilohm . Negative feedback consists of R3 or R4 with R2 which sets the gain of the amplifier.

The gain is equal to:
$\frac{R 2+R 3}{R 2}$ with $S 1$ in upper position
$\frac{\mathrm{R} 2+\mathrm{R} 4}{\mathrm{R} 2}$ with S 1 in lower position
Substituting in the given circuit values gives gains of 100 and 10 , respectively (actually the gains are 101 and $10 \cdot 1$, but the rounded off figures are good enough ones to work with, bearing in mind the resistor tolerances).


Fig. 3.14. The circuit diagram of a Signal Conditioning Amplifier for use with the ADC.


Fig. 3.15. Master pattern (actual size) to be etched for the Signal Conditioning Amplifier. This board is available through the $E E P C B$ Service, Order code 8309-02.


O $\equiv$ VEROPINS
Fig. 3.16. The layout of the components on the topside of the printed circuit board.


## Semiconductors

| D1.2 | BZY88C3V9 3.9V <br>  <br>  <br>  <br>  <br> 400mW Zener diode <br> IC1 off) |
| :--- | :--- |
|  | 741 op-amp (8-pin |
|  | d.i.l.) |

## Miscellaneous

| B1.2 | 9 V type PP9 (2 off) |
| :--- | :--- |
| VR1 | $10 \mathrm{k} \Omega$ cermet multiturn <br> preset $\left(\frac{3}{8}\right.$ inch square) |
| S1 | miniature s.p.d.t. p.c.b. | mounting type

Printed circuit board: singlesided size, $125 \times 95 \mathrm{~mm}, E E P C B$ Service, Order code 8309-02; single-sided Veropins (4 off); PP9 battery clips ( 2 pairs); $7 / 0.2 \mathrm{~mm}$ insulated wire for batteries; 8 -pin d.i.l. socket; self-adhesive rubber feet (4 off).

## Approx. cost Guidance only <br> 

The input signal is applied to the " + " input of IC1 and appears amplified at its output, pin 6, the amount of amplification determined by S1 position. Zener diodes DI and D2 limit negative and positive excursions of the amplified signal to a safe level for inputting to the ADC board. The maximum output will be $\pm 3.9 \mathrm{~V}$, the value of the Zener diodes.


The power supply for the circuit needs to be a split type ranging from $\pm 6 \mathrm{~V}$ to $\pm 15 \mathrm{~V} ; \pm 9 \mathrm{~V}$ was selected as this is readily available in battery form. Two PP9 batteries will give long service.

## CONSTRUCTION

The components are mounted on a single-sided printed circuit board, size $125 \times 95 \mathrm{~mm}$; the full-size master pattern to be etched is shown in Fig. 3.15. This board is available through the $E E P C B$ Service, Order code 8309-02.

The printed circuit board has been designed to accept a miniature p.c.b. mounting single-pole two-way toggle type having in-line pins. Two sets of fixing holes have been provided to allow switches with pins on a 0.1 and 0.2 inch pitch to be used.

Order of component assembly is unimportant. Check that the diodes are correctly orientated and that the i.c. is inserted the right way round in its socket. Component layout is shown in Fig. 3.16.

The power supply leads from the two batteries can be soldered directly to the board or via a p.c.b. screw terminal block as provision has been made for this connector to be fitted. This connection differs from that seen in the photograph which is fitted with three battery leads only.

Thoroughly check your assembly after plugging in the i.c. and when satisfied connect the two batteries and then short together the two input Veropins. Attach a voltmeter across Vout and adjust VR1 for zero deflection on the meter. Remove the shorting link and the unit is ready for use.

The sensor signal should connect to $V_{\text {IN }}$ on the amplifier board with its $V_{\text {OUT }}$ connected to the $\mathrm{V}_{\text {IN }}$ of the ADC board. Set Sl to the required gain so that the output signal is maximised without clipping (the Zener diodes are not activated).

## Next month:

Digital-to-Analogue
Techniques

# EJERYDAY ELECTRONICS SOFTWARE SERUICE 

The EE Software Service provides an easy and reliable means of program entry for our computer-based projects. All programs have been tested by us and consist of two good quality copies of the working program on cassette tape. Certain program listings are also available.

All prices include VAT, postage and packing. Remittances should be sent to Everyday Electronics Software Service, Editorial Offices, King's Reach Tower, Stamford Street, London SE1 9LS. Cheques should be crossed and made payable to IPC Magazines Ltd.

| PROJECT TITLE | $\begin{aligned} & \text { CASSETTE } \\ & \text { CODE } \end{aligned}$ | $\begin{gathered} \text { CASSETTE } \\ \text { COST } \end{gathered}$ | $\begin{gathered} \text { LISTING } \\ \text { CODE } \end{gathered}$ | $\begin{aligned} & \text { LISTING } \\ & \text { COST } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: |
| ZX81 SPEED COMPUTING SYSTEM (Feb 83) | T001 | ¢2.95 | L001 | £2.95 |
| REAL-TIME CLOCK (Apple) (May 83) | T002 | £2.95 | L002 | £2.95 |
| REAL-TIME CLOCK (BBC) (May 83) | T003 | £2.95 | L003 | £2.95 |
| EPROM PROGRAMMER (TRS-80 \& GENIE) (June 83)* | T004 | £3.95 | N/A | - |
| STORAGE SCOPE INTERFACE (BBC) (Aug 83) | T005 | £2.95 | - | - |

[^2]


## IMMERSION HEATER TELL-TALE

## HOME INTERCOM

An electronic aid for reducing energy costs. Feels the hot-water tank electronically at four levels and operates a corresponding display of l.e.d.s.

Use of i.c. audio amplifier in main station unit provides good quality reproduction. Volume control. Call facility at remote station.

## SHORT WAVE RADIO

A simple t.r.f. circuit using ready-made coils covering 1.5 MHz to 30 MHz in three tuning ranges. Provides an inexpensive introduction to short-wave listening.

# SEDAC '83 RESULTS 

## FINALSWINGS TO NOTTINGHAM NORFOLK ATTRACTS SECOND PRIZE <br> LANCS TEAMWORK SECURES THIRD PRIZE

Exciten ent and tension was almost up to Final's Day on the Centre Court at the Second Schools Electronic Design Award Competition (SEDAC), sponsored jointly by Mullard Ltd., and Everyday Electronics. The final judging and presentation of prizes took place in Mullard House, London, over two days- 28 and 29 June.

Schools from the length and breadth of Britain took part in the second national contest to help encourage the technologists of the future put their ideas into reality.

The three winners were selected from twelve finalists whose models were on display at Mullard House. At the presentation on 29 June, Ivor Cóhen, Managing Director of Mullard and Michael Paton, Managing Director of Youth \& Practical Group, IPC Magazines, shared the honour of presenting prizes to the winning teams

Opening his address at the prize-giving ceremony, Ivor Cohen made the point that "standards this year are so high that his technical judges were worried about their jobs!" This he observed was with some justification.

In his welcoming speech, Michael Paton also praised the high standard of papers and practical skills of $A L L$ finalists.

However, he did voice his disappointment at the lack of "competitive spirit" shown by Girls' Schools-who failed to participate. This was despite special invitations going out to one hundred Girls' Schools throughout the country.

## JUDGING PANEL

Scrutiny of Papers (Stage 1) and evaluation of working models (Stage 2) was performed by a panel of four judges representing the two sponsoring parties. The panel members were:

Andy Beer Technical Manager, Power Control Devices, Mullard Ltd.
Terry Giles
Fred Bennett
Brian Terrell
Mullard Lid
Editor, Everyday Electronics
Assistant Editor, Everyday Electronics
The judges wish to record their admiration for the high standards throughout, both in the written work and in the practical realisation of the designs. All students participating deserve the highest commendation and were a credit to their schools.

## HIGHLY SUCCESSFUL SECOND YEAR <br> FOR NATIONAL SCHOOLS CONTEST



The proud winner of the 1983 Schools Electronic Design Award Competition, Martin Cragg of Roland Green School, receiving the SEDAC Trophy from Michael Paton of IPC Magazines with Ivor Cohen of Mullard keeping a watchful eye on the proceedings.

## Winning Entries

The first prize of $£ 300$, the SEDAC Trophy and selection of components worth $£ 200$ were awarded to Martin Cragg (15), of Roland Green School, Welford, Nottingham, for his Electronic Pendulum.

Second prize of $£ 200$ plus components to the value of $£ 200$ was awarded to Robert Rowe (16), of Wymondham Coilege, Norfolk, for his Digital Gauss Meter.

Third prize was won by Michael Lawrie (15) and Lorna Wilkinson (13), of Hollins High School for their Environmental Data Recorder.

The Remaining nine runners-up received component packs valued at £100. The runners-up were: Mill Hill School, London; Ousedale School, Newport Pagnell; Parkview School, Barrow-in-Furness; Princess Margaret Royal Free School, Windsor; Ridings High School, Winterbourne; Skegness Grammar School, Skegness; Skinners' School, Tunbridge Wells; Thomas Alleyne's High School, Uttoxeter; Whitecross School, Lydney.

All twelve finalists received a signed certificate to mark their achievements in reaching the finals of the 1983 SEDAC competition.

Winding up the ceremony the sponsors announced that the competition would be run again in 1984. Although it may widen its scope into other fields where electronics can be of benefit. Is this an inducement to attract the girls? Or are they too shy?

Contestants and teachers expressed their appreciation in the way the competition was organised by the joint sponsors. They, were particularly grateful to Jackie Doidge, SEDAC Secretary, and Ron Powell, Mullard, for the efficient way they organised travel and accommodation for all finalists.

## ectronics



Martin with the SEDAC Trophy awarded to the outright winner of the schools competition.


Robert shows off the Digital Gauss Meter which won 2nd prize.


Michael and Lorna receiving their 3rd-place awards from Ivor Cohen of Mullard.

## A VISITOR'S VIEW

The SEDAC 83 final was a close run event, as the standard of all the projects was high indeed.

It was interesting to note that seven out of the 12 finalists had decided to construct computerbased projects, emphasising the changing trend in schools today. Many of the competitors agreed that they were now geared towards computing rather than learning about the physical side of electronics.

The winner of SEDAC 83 was the Electronic Pendulum, entered by Martin Cragg (15) from the Roland Green School, Nottingham. The object of this project was to determine a value for " $g$ ", the acceleration due to gravity, using a pendulum linked to a BBC Microcomputer. In the three months it took to design the project a few problems were encountered by Martin, when dealing with the computer software and also the suspension of the pendulum, but these were all ironed out in time for the final. Ironically, Martin, who had hoped to get an apprenticeship in electronic engineering was turned down by Plessey at Beeston.

The runner-up of SEDAC 83 was the Digital Gauss Meter, entered by Robert Rowe (16), from Wymondham College, Norfolk. This instrument was constructed for the accurate measurement of small amounts of magnetic flux. It provides a fast and easy-to-use system at a budget price. The unit was designed specifically for an Engineering Design 'O' Level, but it was then decided to enter it for SEDAC 83. This was one of the few entries that is being turned into a commercial proposition, having been spotted by Scientific Systems Ltd., of Swindon.

The third prize winner, the En vironmental Data Recorder, was entered by Michael Lawrie (15) and Lorna Wilkinson (13), from Hollins County High School, Lancashire. This was the only project amongst the finalists in which a girl participated in the technical design, and was again built especially for SEDAC 83.

The recorder was constructed
for field work and is a portable, water and weatherproof unit which reads analogue information from sensors, converts these to digital form and stores the information in a memory. The results can then be graphically displayed later, back in the laboratory via a computer and television screen.

It was a stimulating experience to walk around and examine the projects and to "chat-up" all the finalists.

Nearly all the competitors seemed keen to pursue a career in the electronic or computing fields, except Lorna from Hollins County High School, who thought she would rather pursue acting on the stage!

Reaching the final of SEDAC 83 was a great event for the competitors. The two-night's accommodation in a nearby hotel at the sponsors expense made this a pleasant and memorable trip to the capital. As one teacher clearly pointed out, his pupils had never been to London before and had been thrilled at the prospect of sight-seeing in the capital.

Talking to the competitors, it became apparent that a couple of the schools involved, although eager to take credit for their pupils success, were not very eager to give financial backing, in effect limiting the competitors to what they could personally afford in the way of components. In total contrast, it is pleasing to report that the competitors from Hollins County High School were provided with pocket money by the Parent Teachers Association. It was a general feeling amongst the contestants that the schools had not been a driving force behind them to enter the competition, and it was on their own initiative that they had managed to succeed.

Many people commented on how well the competition had been organised. It was thought by one or two participants that SEDAC 83 seemed a better run event than some Government backed schools competitions that they had previously entered.

## THE 1983 TWELYE WINNING DESIGNS



Biological and environmental science studies are difficult to carry out when the quantities to be measured change slowly. Experiments may take days or weeks to be completed and when field work is being done the problems are even greater.
This project is a portable, water- and weather-proof unit designed to read analogue information from sensors. The unit may be used to determine temperature changes, oxygen content, light levels and may also be used with other sensors.

The data is stored digitally in a memory having already been converted from analogue-to-digital by an 8 -bit converter. The results are


The purpose of the Logic Gate Demonstrator is to provide a visual demonstration of the operation of the five common logic gates, AND, OR, NAND, NOR and EX-OR such that the truth tables of these gates can be easily understood.

The project is intended for use in a class of about 20 pupils and therefore the display has to be of a size which will be easily visible and clear to the pupils. The display board of dimensions, 44 cm by 60 cm uses 6 V indicating lamps.

Environmental Data Recorder
Hollins High School, Accrington
Michael Lawrie (15) Lorna Wilkinson (13)

Mr. J. S. Hagan (teacher)


## Logic Gate Demonstrator

Mill Hill School, London
G. Chelliah (16)

Dr. W. D. Phillips (teacher)


The circuit design consists of a multivibrator producing pulses at approximately 1 Hz . These pulses are then fed to a 2 -bit binary counter which drives the inputs of the
five logic gates. The output to be displayed is selected by means of the gate selector switch and at the same time the corresponding indicator lamp is illuminated.


THE Compu-Colorimeter is designed to use a computer to record data for experiments determining rates of chemical reactions. The

THis project allows the ZX81 and ZX Spectrum to control external pieces of equipment and also to monitor external equipment. The unit provides eight input lines and eight output lines, and these may be used individually or in groups.

The port is input/output mapped as op posed to being memory mapped which means


DURING experiments in the physics laboratory which require the measurement of waveforms lasting no longer than a few milliseconds, it occurred that it would be easier and more accurate if the waveform were frozen on the oscilloscope screen.


## Compu-Colorimeter

Ousedale School, Newport Pagnell
Mark Cole (17)
Mr. W. G. Thomas (teacher)

electronics have been kept simple, and complex Basic and Z80 Assembly language programs developed to process the data.

Operation consists of a light source which passes through a filter to obtain optimum wavelength and then through a test-tube containing the reacting solutions and on to a light dependent resistor. As the reaction commences, the colour of the solution changes and varying amounts of light fall on the light detector.

## ZX Input/Output Port

M. Cowperthwaite (16)

Parkview School, Barrow-in-Furness Mr. S. J. Redding (teacher)

routine enables the port to be used from Basic. On the ZX Spectrum the port can be used directly from Basic.

Another feature of the port is that it can be made to respond to any one of the 256 possible input/output port addresses. To avoid any clashes between the port and other input/output mapped devices (ZX printer), a d.i.l. switch is built into the circuit. Hence, the port can be made compatible with virtually any other piece of hardware just by altering a few switches.
that the unit is compatible with any memory mapped device.

When using the ZX 81 , the port, being input/output mapped, cannot be used directly from Basic. However, a short machine code

The equipment contains two separate compartments, optically sealed so light can only pass through the test-tube containing the reaction.

The voltage at the output of the detector is proportional to the light intensity, and is converted to a digital number and set up in a latch to be read by the computer's 8 -bit input port. The software inputs data at set intervals, stores it, calculates results and displays them graphically.

## Storage Oscilloscope

Princess Margaret Royal Free School, Windsor
Ron Hejdeman (17)
Andrew Wilding (17)
Mr. G. Mort (teacher)
 OUTPUT SIGNAL


There is a type of oscilloscope which can do this, known as a storage oscilloscope, however, these are expensive, therefore an unviable proposition for schools. As a result, an alternative was designed to convert a normal oscilloscope into a storage instrument.

An oscilloscope displays a waveform by moving a "dot" across the screen at a fixed speed. To display a continuous waveform, the waveform has to be repeated many times. To do this, a memory is required, and this is the basis of this design.

To enter the waveform into the memory, it has to be converted into binary using an analogue-to-digital converter. And to convert it back to a voltage for the oscilloscope, a digital-to-analogue converter is employed. Logic is used to synchronise all furictions.


THE unit was designed as a general purpose telephone interface for any 8 -bit microprocessor. The memory of the computer is converted into an audio signal, which is then passed down a telephone line and converted back to a digital signal. This signal is then stored in the memory of the receiving computer.

The main design considerations were operation, speed and synchronisation. Transmitting clock pulses was discarded as it halved the


THE object of this project is to determine a value for " g ", the acceleration due to gravity, using a pendulum linked to a BBC Microcomputer. Experimental time is reduced and a fairly accurate value of " $g$ " may be determined. It can also be used to show how errors can be reduced by increasing the number of pendulum swings.

A pendulum is kept swinging at a constant amplitude by an electromagnet housed below the pendulum. A pulse from a light dependent

## Telephone Interface for Micros

The Ridings High School, Winterbourne
David Omar (16) Julian Danton (16)
Mr. P. S. Maund (teacher)

speed of data transmission, this meant an accurate clock was needed. The accurate clock pulse is provided by a crystal oscillator.

The units only requirements are a $\pm 5 \mathrm{~V}$ power supply, a suitable interface with two 8bit programmable ports and suitable software.

## Electronic Pendulum

Martin Cragg (15)
Roland Green School, Wilford Mr. L. Haywood (teacher)
 soon as the bistable has changed state a pulse is supplied to a monostable causing it to change state. The output from the monostable causes a large current to flow in two transistors wired as a Darlington pair.

After a short delay the monostable will
return to its stable state and the Darlington pair will energise the electromagnet giving a kick to the pendulum. The computer keeps count of the number of pendulum swings and then calculates " g ".


## ALL THE WINNERS!

The twelve ' 83 finalists outside Mullard House on Presentation Day

> WHO WILL BE IN THE PICTURE NEXT YEAR?


THIS unit is designed to replace the mercury thermometer. Its main application will be for classroom demonstrations due to the large digital display and the ability to remember temperatures taken every $2 \frac{1}{2}$ seconds.

The temperature sensor is a constant current generator whose output is directly proportional to the Kelvin temperature. This current is converted to a voltage and the next stage converts this voltage to time. The output is anded with the clock and the result is a number of pulses equal to the Kelvin temperature.


THE unit was designed as an aid to increasing the accuracy of experiments where a precipitate is timed as it forms. The design improves on the crude methods of timing used in schools at present and should be of value in work at all levels of chemistry. Throughout the design, emphasis has been put on the accuracy of about half a second, and the cost which schools would be able to afford.

Digital Thermometer with Memory
Skegness Grammar School, Skegness
Paul Thorlby (15) Paul Rock (16)


There are three modes: review, normal and store. In store, each reading is displayed and remembered. In review, the measuring apparatus is disabled and readings taken from the memory, and in normal, the memory content is safeguarded and the unit displays readings, but does not record them in the memory.

These are counted by a 4 -digit b.c.d. counter. The control logic, routes each reading to and from the memory, display and counter via a data bus.

David Lewis (16)
Duncan Reed (17)
Mr. L. T. Goldsmith (teacher)


The timer itself consists of a light shining through the reaction vessel onto a light dependent resistor (l.d.r.). As the precipitate thickens the resistance of the l.d.r. increases and this increase is used to control a switching circuit, which in turn stops a 4-decade counter timing the reaction.

## A WARDS FOR WINNERS

The SEDAC TROPHY to the outright winner
Valuable cash prizes to the first three winners
Boxes of electronic components and technical data booklets to all twelve finalists-plus a certificate and one year's subscription to Everyday Electronics
and All-Expenses-Paid Two-Day visit to London.


THIS project is intended for use in the chemistry laboratory and is a device which extends the capabilities of a pH meter by linking it to a Research Machine 380 Z using Control Basic.

Precise measurements can now be carried out to a great degree of accuracy instead of using a colour change with an indicator, or a measurement on an analogue meter, as numerical representation can be given. Graph plots may also be provided by the computer during chemical reactions.


THE circuit is a complete unit for interfacing the ZX81 in a school science laboratory. The unit has its own power supply, supplying 1A to the circuit, and also the external circuitry if connected.

The circuit has seven defined parfs; a selection system for selecting which part of the circuit is being used; the expansion port which enables timers and meters to be connected; analogue-to-digital and digital-to-analogue converters; a ROM port equipped with an eprom Programmer for +5 V rail EPROMs; a


THIS instrument is designed for accurate measurement of small amounts of magnetic flux in the range $\pm 2000$ gauss. It provides a cheap and easy to use system, within a school's budget.

The device uses a Hall effect transducer for detection of a magnetic field, this being moun-

## Computer Interface for Analogue pH Meter Stephen Bailey (16)

Thomas Alleyne's High School, Uttoxeter
Mr. A. H. Orme (teacher)


The design revolves around an analogue-todigital converter which converts the varying analogue voltage from a normal pH meter and digitises it, so that it can be used by the 380 Z . Before this stage there is a variable gain amplifier and buffer which obtains full graphical range from the 380 Z .

The final part of the proposed project is the audible feedback oscillator which alerts the
operator that a certain pH value is about to be reached by producing two tones. A visual indicator is also provided if it is more suitable in certain conditions.

All major controls such as sampling rate and warning system switching are software based therefore maintaining the flexibility of the whole system.

## ZX81 Expansion System

Whitecross School, Lydney

Peter Jarvis (14)
Katy Jarvis (18)
Mr. M. C. Jarvis (teacher)


Digital Gauss Meter
Wymondham College, Wymondham
Robert Rowe (16)
Mr. J. Edwards (teacher)

ted in a probe. The sensor gives a differential output, dependent on the strength and polarity of the magnetic field. This is fed into a digital voltmeter i.c. The sensor is biased to be linear up to $\pm 3000$ gauss.

The d.v.m. gives a $3 \frac{1}{2}$ digit liquid crystal display of the differential voltage applied to its input. A hold feature was essential to freeze
the display as small movements of the probe resulted in large changes in the magnetic field.

The 7106 d.v.m. i.c. employed in the design does not have an internal hold facility, so an external circuit was added. This uses an external clock and multiplexer, the latter necessary to prevent the display being destroyed by a constant d.c. drive voltage across it.

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40 watt amp - hifi $20 \mathrm{hz}-20 \mathrm{kHz}$
Microvolt multiplier
ordinary multitester
Pure Sine Wave Generator
Linear Power output meter
115 Watt Amplifier 5Hz 25k
Power supply for one or two 115 watt amps


(Top) The 2X81 package comes with computer, 9V power pack (excluding mains plug), programming manual and leads for TV and cassette. (Above) The p.c.b. showing the finger set to connect the printer and extra memory.

THE Sinclair ZX81 has, almost single handedly, been responsible for the boom in home computing and this familiar little "black wedge" can now be purchased for less than $£ 40$, a reduction of some 40 per cent on the original price. Still a best-seller in spite of the introduction of its big brother, the Spectrum (see Special Report, June ' 83 issue), the diminutive 1 K ZX81 is as readily available from the high street chain store as it is from the computer specialist.

So what do you get for your money? The package comes complete with the computer console, a 9 V mains power pack, a $1 \cdot 2 \mathrm{~m}$ long aerial lead to connect to a TV set, a pair of leads to hook up to a cassette recorder and the 210 -page BASIC programming manual. Also included is a software catalogue listing all currently available Sinclair software tapes and introductory leaflet.

So all you need to supply is a 3 -pin mains plug and a UHF television set (that is, one that receives BBC2 in the UK) and you can start computing.

## SETTING UP

The first chapter of the manual covers the initial setting-up procedure and this is a simple matter. The television set is connected to the ZX 81 via the aerial socket and the mains power pack is plugged in. The TV has to be tuned-in (using a spare channel button on push-button sets or just a little way past BBC 2 on rotary controls) and this is achieved when a little " $K$ " appears in a black square in the bottom left-hand corner of the picture.

I initially found a slightly fuzzy picture when working on a large colour TV setas well as it being slightly inconvenient crouched on the floor in front of it. Hardly the ideal working position! However, this is a criticism of the television and not the computer for when used with a portable black and white set, the picture was pin-sharp. And a far more comfortable work station is created with the whole set-up on a table top.

The 40 -key keyboard measures only $150 \times 50 \mathrm{~mm}$ and is of membrane contruction. That is, it has no moving parts, you simply touch the required key. Now this technique has recently come in for quite a lot of bad press and a whole multitude of full-size, moving-key keyboards have sprung up as add-ons.

I personally found this criticism a little unjust for, with practice, it is possible to

become quite proficient with the original membrane version. It does of course mean that you have to look at the keyboard and not the screen when entering data, but how many of us can touchtype anyway?

## MEMORY

One of the most important parts of a computer is the memory in which it stores data and instructions in order to carry out the programs. The unexpanded ZX81 has 1 K bytes of user memory and this is known as RAM (Random Access Memory). This means that it has 1024 (1024 and not 1000 because 1 K is equal to $2^{10}$ bytes in binary) memory locations for system management.
This is not a great deal by modern standards but certainly enough to introduce the user to programming techniques. It is, of course, expandable by 16 K with the add-on ram pack.

The ZX81 uses BASIC as its programming language and the central processor or "brains" of the computer (in this case, the popular Z80A processor) does not directly understand BASIC instructions. So a further memory is required to interpret the BASIC and provide instructions the processor can understand.

This additional memory is known as the rom (Read Only Memory) and the ZX81 has 8 K bytes of rom. The contents of the rom cannot be changed by the user whereas the ram can be altered at will but ram is lost when the power supply is disconnected whereas rom is not.

## PROGRAMMING

The ZX81 BASIC programming manual assumes no prior knowledge of computer programming and starts at absolute basics (excuse the pun!). Within minutes, the user is operating the ZX81 as a calculator and becoming familiar with the way to enter a problem and execute the answer.

But a simple calculator the ZX81 is not, and it very soon becomes apparent that it is capable of handling far more complex problems. The next few chapters introduce the BASIC functions, variables and strings used in programming.

By chapter eight (and it is advisable to follow chapters in numeric order) the beginner is actually running a simple program concerning the varying price of vegetables! The following chapter has you running a few more serious
programs, for example extracting square roots, and it starts to fall into place.

As previously stated, the ZX81 uses Sinclair BASIC and this language uses "keywords" for all instructions. Now BASIC follows normal English as closely as possible and, for example, the language uses the PRINT command to literally "print" a result on the screen.

The ZX81 has all these keywords as single key entries and to use the same example, if the program requires a result to be displayed, the first pressing of the " $P$ " key will give the PRINT command, without the need to spell the keyword in full. All subsequent pressings of this key will give the single character " $P$ " until a new line of program is started.

All new lines of program are given a number and the computer will automatically place these lines in order. The normal standard is to number the lines $10,20,30$ and so on in multiples of ten so that if an additional unforeseen line is required (and this is usually the case, even with experienced programmers), it can be added between two previous lines and the ZX81 will insert it at the correct location.

The ZX81 is not particularly fast and it can take a number of seconds, even minutes, to execute a program. However, it does have two modes of operation, FAST and SLOW. Under normal circumstances, it operates in SLOW mode and displays all operations as they happen. If the FAST mode is requested, it blanks out the screen during a program run (and as the display takes up memory space, it also takes up time) and consequently, the program runs much faster.

## GRAPHICS

The display of the ZX 81 has 22 lines of 32 characters making a total of 704 picture elements. Each of these picture elements is sub-divided into four portions, each known as a pixel. Each individual pixel can be addressed by its coordinates (using the PLOT command) resulting in a graphics screen area of 42 by 64 pixels. Note that the bottom two lines of the screen cannot be used for either PRINTing or PLOTting.

As such, the display is fairly low resolution and only in black and white. The standard graphics characters do, however, include a set of symbols that allow the pixels to show a kind of chequered pattern that gives the effect of a half-tone, so quite effective pictures can
be generated for games.
The display can also be animated by instructing the computer PLOT and then UNPLOT pixels, giving the effect of movement.

Now, assume you have mastered the fundamentals of programming and wish to save a program. Switching off the machine will erase all that is in the ram so the program will have to be stored on cassette tape. The ZX81 has a cassette interface to do just this and the procedure is fairly simple.

All that is required is a normal mono cassette recorder (a stereo recorder will work but better results are obtained on cheaper, mono machines) and a blank tape. The computer is connected to the recorder via the two leads supplied. These leads come with 3.5 mm jack plugs at either end so if your recorder uses different connectors, a special lead will be required.

The program can now be stored with the SAVE command, or entered from tape into the memory with the LOAD command. It does not always work reliably and often requires a fair amount of experimentation with record and volume levels but once mastered, is essential for the serious user of the ZX81.

## IN CONCLUSION

The final chapters of the manual go into the more serious aspects of programming, including using machine code (this is the language that the Z 80 A processor understands), PEEKing and POKEing and the organisation of the memory. But it still leaves you wanting for more information so as to fully exploit the capabilities of the ZX81.

I also feel that you will very soon be needing the services of the 16 K RAM Pack and quite possibly, the ZX Printer so this is worth bearing in mind when considering buying. Overall, it is a very good starting point (and a very cheap one, too) and over a million users can't be a bad recommendation.

In so far as add-on extras go, it is probably one of the best computers served. With software houses offering programs on games, educational and business subjects and independent manufacturers supplying keyboards, extra memory, high resolution graphics and interfaces, the humble ZX 81 could soon become the heart of a sophisticated system.

THIS six-part series describes experiments interfaced to the 3802 microcomputer using the Analogue-toDigital Converter described on page 559, but could easily be adapted for use with other such devices. The software will be given for each experiment.

All these experiments have, at some time or other, been demonstrated in the classroom in a teaching situation and have proved very successful. These are not simulations but actual experiments performed by the teacher or pupil.

The results clearly demonstrated the advantage of supporting difficult physical ideas with almost instant experimental results, rapidly analysed and plotted. Measurable physical quantities were no longer the dreary abstract graphs which have to be learned from book or blackboard, but they actually came alive on a v.d.u. as the experiment was being performed, thus enabling the teacher to concentrate more on physical principles and interpretation of results and then in a separate experimental session to concentrate on the practical aspects of any given experiment.

# COMPUTER AIDED EXPERIMENTS 

USING THE RM380Z MICROCOMPUTER


The experiment being carried out by lower sixth form pupils at La Retraite High School for Giris, London.

## 1. INVESTIGATION OF DIFFRACTION PATTERNS

T
THS experiment employs the 380 Z microcomputer to automatically store and plot the intensity distribution of the light pattern produced as a result of diffraction/interference from single and multiple slit light sources.

An optical sensor is made to scan the interference pattern produced and the resulting analogue electrical signal developed by this transducer is fed to an analogue-to-digital converter. We are using the unit described on page 559 in this experiment, although others could of course be used. The ADC is connected to the user port of the 380 Z .

Under software control the digitised data is read in by the computer and the detected intensity pattern graphically displayed on the v.d.u. wired to the 380 Z .

A monochromatic light source is required for this experiment which in this case is provided by a low power héliumneon laser. As can be seen in the photograph the laser sits on a special stand which also accommodates the slit(s) and a lens. The light pattern is observed on a vertical white screen.

## OPTICAL SENSOR

An integrated optical transducer is used as the sensor. It contains a high efficiency photodiode combined with a high gain low noise amplifier. The internal circuitry for this electronic sensor is shown
in Fig. 1.1. It requires a split power supply of between $\pm 2.5 \mathrm{~V}$ and $\pm 18 \mathrm{~V}$ which is simply provided here by two PP1 6 V batteries.

Under dark conditions there is zero output from the device. When light falls on the photodiode junction the output voltage rises, the level of the output being proportional to the intensity of the light received.

The connection details of this device are given in Fig. 1.2 where it is shown soldered to suitable lengths of plastic covered wire to be terminated in 4 mm plugs to suit the specified ADC unit. Sleeving must be used, as shown, to prevent possible shorting of the pins.

Set up the experiment and connect the sensor to the ADC, and batteries, as shown in Fig. 1.3. Set the apparatus to produce a well defined image on the screen.

## MOVING SENSOR

A means must be found for sweeping the sensor across the light beam. It can, and has successfully been carried out by hand, but a better method was found using the trolley normally found in the apparatus for velocity measurement. The sensor was fitted here, to the vertical metal mounting plate of a solderless breadboard system and held at the correct height with Blu-Tack. This
assembly was secured to the trolley which was then pushed at an even rate across the beam. This method was used to produce the double-slit interference pattern shown in Fig. 1.4.

The software for this experiment is shown opposite under LASER. The other routines STORE, POKER and GRAPH provide the facilities described in their respective REM statements.

SAVE "LASER" will copy the typed in program onto disc and give it the filename LASER. LOAD "LASER" will transfer the LASER routine from disc to user RAM. These routines may also be saved/loaded on/from tape using the CSAVE and CLOAD commands respectively.

The six experiments are:

1. Investigation of diffraction patterns
2. Napthalene cooling/heating curves
3. Voltage transfer characteristic of the bipolar transistor
4. Capacitor charge/discharge with variable time constant
5. Ohmic and non-ohmic resistances
6. I-V characteristic of a $p n$-junction

The theory behind this experiment is beyond the scope of Everyday Electronics and readers requiring this information are referred to school and college text books on Physical Optics.

## COMPUTER AIDED EXPERIMENTS

SOFTWARE: EXP. 1

## LASER

10 fem:this progremme reads the light
20 FEM: input from the A-bon via the
30 FEM: SBOZ user port relocates the
40 REM: Fexults and plots them.
50 GRAFH1:GFAFHO
SO CALL"RESOLUTION", O,2:FEM:HR-grephics
70 CALL"FLOT" $0,0,3$
80 FOR $1=1$ TO 300
PO FOKE 64511, O:REM: set channel.
$100 \gamma=F E E K(64511)$ : REM: read channel.
110 FLOT $70,50,5 T R$ 解 (Y) +" "
120 FEM LIMITS OF SCREEN ARE 318. 1.91
130 CALL"LJNE", I,Y*2, 3 REM:plot results
140 PDFE (s $6000+2$ ), FaFEM: FElocate results
150 FOR $Z=1$ TO 20G:NEXT Z
160 NEXT I
170 FRINT "PRESS E TO CLEAE GRAPHICS"
180 G=GET :
150 IF G 9 90 THEN $\mathrm{G}=3.3$
200 IFG=67 THEN CALL "RESOLUTION", O, 2 SAVE "LAJTRT"

## STORE

10 REM: this programme stores the
20 RENAFelocated data to dist file
30 CFEATE\#1O, "LAS. DAT"
40 FOF V $=8.0000$ TO ( $86000+300)$
50 EYTE=FEEK(U)
60 FRENT $10, \mathrm{Vm}$ " " "BYTE
70 NEXT V
80 CLOSE\#10
SAVE "SURE"

## POKER

10 FEln this programme returns
20 FEM: the datia to memory
ZO FEM: locations for replotting.
40 DFEN\# 10, "LAS. DAT"
50 FOR $y=5600$ TO $(86000+300)$
60 INFUT\#10, ADDF: EYTE
7O FOKE ADDR BYTE
By NEXT Y
90 CLOSE\# 10
SAVE "rotaz"

## GRAPH

10 FEM: this programme replots the
20 FEMm data returned by the preyious
BO REM: programme.
40 GRAFH 1
50 CALL"FESOLUTTON", 9,2
60 FOF $I=2$ TO 300
$70 \quad Y=\mathrm{FEEFK}(860 \mathrm{O}+1)$
EO CALL "LINE", I,Y*4, $\rightarrow$
\%O NEXT 1
SAVE "GRAYT"


Fig. 1.1. Circuit diagram of the sensor and its battery power supply.


Fig. 1.2 (above). Identification of the sensor lead-outs and connection details of extension wiring. See Shop Talk for supplier of sensor.

Fig. 1.3. Connecting the sensor and batteries to the ADC


## ADC


unit.
Fig. 1.4. Photograph of a double-slit interference pattern obtained from an experiment



# TRAFFIC LIGHT SIMULATOR 

BY N.P. NAUGHTON
|n this project three l.e.d.s are used to simulate a set of road traffic lights. The three l.e.d.s, a red, a yellow and a green are continuously sequenced on and off, and the sequencing speed is adjustable over a wide range so that the action can be frozen by pressing the button $S 1$. This enables a test-your-reflexes type of game where competitors freeze rapidly sequencing lights on a nominated colour.

The project may be put to a more serious use, that of teaching road safety to young children.

## CIRCUIT DESCRIPTION

The complete circuit diagram for the Traffic Light Simulator is shown in Fig. 1. The circuit uses a low power Schottky clamped TTL device and the unit will draw less than 40 mA . The transistors TR1 and TR2 together with their associated components comprise a pulse generating oscillator which drives IC1. IC1 (74LS90) is a decade counter which, as shown connected, repeatedly counts incoming pulses up to ten so long as the pulses are applied.

TABLE 1

| Decade Count | Binary Inputs | Outputs | Display |
| :---: | :---: | :---: | :---: |
|  | D C B A | '0' $1^{\prime \prime} 2^{\prime \prime} 3^{\prime \prime} 4^{\prime \prime} 5^{\prime \prime} 6^{\prime} \cdot 7^{\prime} 8^{\prime \prime} 9^{\prime}$ |  |
| 0 | 0000 | $\begin{array}{lllllllllllll}0 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1\end{array}$ | RED |
| 1 | 0001 | $\begin{array}{lllllllllll}1 & 0 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1\end{array}$ | RED |
| 2 | 0010 | $\begin{array}{llllllllll}1 & 1 & 0 & 1 & 1 & 1 & 1 & 1 & 1 & 1\end{array}$ | RED |
| 3 | 0011 | $\begin{array}{lllllllllll}1 & 1 & 1 & 0 & 1 & 1 & 1 & 1 & 1 & 1\end{array}$ | RED/AMBER |
| 4 | 0100 | $\begin{array}{llllllllllll}1 & 1 & 1 & 1 & 0 & 1 & 1 & 1 & 1 & 1\end{array}$ | RED/AMBER |
| 5 | 0101 | $\begin{array}{lllllllllll}1 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 1 & 1\end{array}$ | GREEN |
| 6 | 0110 | 1.11clllllllll | GREEN |
| 7 | $\begin{array}{lllll}0 & 1 & 1\end{array}$ | $\begin{array}{llllllllllll}1 & 1 & 1 & 1 & 1 & 1 & 1 & 0 & 1 & 1\end{array}$ | GREEN |
| 8 | 1000 | $\begin{array}{lllllllllll}1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 0 & 1\end{array}$ | AMBER |
| 9 | 1001 | $\begin{array}{lllllllllll}1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 0\end{array}$ | AMBER |

The counted numbers (from 0 to 9 ) appear in sequence at the $\mathrm{Q} 0, \mathrm{Q} 1, \mathrm{Q} 2$ and Q3 outputs of IC1 as binary coded logic levels. These are then decoded into a decimal count by IC2. Table 1 shows how the ten outputs of IC2 relate to the sequence of binary inputs.

It will be seen that only one output is ever low with the remaining nine always
high. In effect, the total counting time of one decade is divided into ten units, and Table 1 shows how these units of time are allocated to the l.e.d.s, D13, D14 and D15. The shorter "on" time of the amber lamp in real traffic lights is simulated but deliberately made longer for required purposes.

When IC1 is withdrawn from its holder, no l.e.d. will illuminate since IC2 recognises an open-circuit input as a logic 1 , resulting in none of the outputs going low.

The IC2 outputs " 0 " to " 9 " are grouped via buffer diodes D1 to D10. The buffers ensure that current may only flow into, and never out of the outputs. The output which is "low" will draw current through its buffer diode, a 220 ohm limiting resistor (R5 to R 7 ) and the relevant l.e.d. The rest of the buffers may be regarded as being reverse biased and so isolate the other outputs from the circuitry.

It might be thought that the red l.e.d. (D13) would momentarily dim when its current path is transferred from output " 0 " to " 1 ", or " 2 " to " 3 ". But the overall switching of the oscillator and i.c.s is much too fast for any flicker to be visible.

## OSCILLATOR ACTION

In order to promote rapid switching, an oscillator which allows d.c. coupling of its output is used. This avoids cap acitively loading the input of IC1.

The action begins with C 1 charging via limiting resistor R1 and the base/emitter junction of TR 1. The charging current alone causes TR1 to conduct so that when C 1 is charged, the transistor TR1 will turn off. TR1's collector voltage


Fig. 1. The complete circuit diagram for the Traffic Light Simulator. A yellow light emitting diode (D14) is used for the "Amber" light sequencing. The circuit uses a low power TTL Schottky device (IC1) and the unit draws less than 40 mA .


Fig. 2. Layout of components on the topside of the stripboard and interwiring to the front panel of the case. The underside of the board detailing breaks in the copper strips is shown far right.


Completed unit showing circuit board mounted on spacers in the base of the case.
therefore rises and TR2 will now conduct. The capacitor then discharges through TR2, VR1 and R2, developing a diminishing voltage across VR1 and R2 which will hold TR1 off.

A voltage of opposite polarity, developed across R3 restarts the cycle by turning TR1 on again. The charging current of C 1 continues the new cycle after TR2 (and therefore the initiating voltage) turns off. VR1 controls the frequency and R2 is simply a value high enough to sustain oscillation when VR 1 is at a minimum.

## CONSTRUCTION

The prototype unit was constructed on a piece of 0.1 inch matrix stripboard, 24 strips by 37 holes, as shown in Fig. 2. It is
helpful to mount the i.c. holders first (after all track breaks have been made), these will then act as reference points for positioning the other components.

Attach sufficient lengths of flying leads to reach the l.e.d.s to be fitted to the lid of the case. Drill the lid of the case to accept the three l.e.d.s and fit these in position and hold secure with fixing clips. The two switches and VR1 may be fitted next.

## CASE

The prototype stripboard was mounted in a standard aluminium box having dimensions of $102 \times 76 \times 38 \mathrm{~mm}$. Although any case of similar dimensions should be suitable. Remember to take into account the size of a 6 V battery pack when selecting the case.

## COMPONENTS

| Resistors |  |
| :---: | :---: |
| R1,4 $2 \cdot 7 \mathrm{k} \Omega(2 \mathrm{ff}$ ) |  |
| R2 1 | 100 |
| R3 $1.8 \mathrm{k} \Omega$ |  |
| R5,6,7 $220 \Omega$ (3:off) |  |
| All $\frac{1}{4} \mathrm{~W}$ carbon $\pm 5 \%$ |  |
| Capacitor |  |
| C1 4 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ tantalum |
| Semiconductors |  |
| D1-12 OA47 germanium (12 off) |  |
| D13 | TIL209 0.12in red l.e.d. |
| D14 | TIL2 120.12 in yellow |
|  | l.e.d. |
| D15 ${ }^{\text {D16 }}$ | TIL211 0-12in green I.e.d. 1N4001 silicon |
| TR1,2 ${ }^{\text {B }}$ | BC108 npn silicon (2 off) |
| IC1 | 74LS90 TTL low power |
|  | Schottky decade counter |
| IC2 | 7442 TTL b.c.d. to decimal |
| Miscellaneous |  |
|  |  |
|  | $220 \mathrm{k} \Omega$ lin. control potentiometer |
| B1 | 6 V battery pack (HP7 $\times 4$ ) |
| S1 | push-to-break |
|  | single pole toggle |
| Stripboard: 0.1 inch matrix size |  |
| 24 strips by 37 holes; aluminium |  |
| case, $102 \times 76 \times 38 \mathrm{~mm}(\mathrm{AB8})$; |  |
|  |  |

## Approx: cost

Guidance only

# BOON <br>  

## OPERATIONAL AMPLIFIER EXPERIMENTAL MANUAL

Author G. B. Clayton<br>Price $£ 13.00$ hard cover £6.95 limp<br>Size $\quad 220 \times 145 \mathrm{~mm} .130$ pages<br>Publisher Butterworth \& Co. Ltd.<br>ISBN 408012404 (hard)<br>408012390 (limp)

Primarily intended as a companion to the authors text "Operätional Amplifiers, 2nd Edition" (ISBN 0408000370 7), this manual can still stand alone as a guide to operational amplifier experiments for the serious student. It is designed to give the student a broad understanding of the practical applications of operational amplifiers.

A total of 21 different experiments are listed, ranging from basic resistive feedback configurations to complex phasesensitive detector systems, each one using the same test board, the constructional details of which are given in the introduction.

An appendix suggests a method of using a personal computer to investigate op-amps.

As operational amplifiers are the most frequently used and versatile active devices in analogue electronics, an appreciation of their capabilities and limitations is essential for the designer.
G.P.H.

## PRACTICAL ELECTRONIC BUILDING BLOCKS BOOK 1 \& BOOK 2 <br> Author Price Size Publisher ISBN <br> R. A. Penfold <br> $£ 1.95$ Limp edition <br> $180 \times 110 \mathrm{~mm} .94 / 110$ pages <br> Bernard Babani <br> 0859340929 \& 0859340937

ALL electronic circuits are built in various stages and contain a distinctive base circuit. Book 1 contains three chapters dealing with Oscillators, Monostable Multivibrators and Miscellaneous Circuits.

The basic building blocks for various electronic circuits are explained with the aid of good, clear diagrams.

A book that will provide the beginner to electronics with a good insight into circuit designs.

Book 2, a continuation of Practical Electronic Building Blocks Book 1, is designed to provide a number of useful electronic circuits with which a reader may build projects.

This book deals with circuits that process signals rather than generate them. The book contains three chapters, Amplifiers, Filters and Miscellaneous Circuits.
R.A.H.

# RADIO WORLD <br>  

## BY PAT HAWKER G3VA

## Political No-speak

Most radio amateurs are anxious to keep international politics firmly out of their hobby and like to think that freely talking to peoples of all nations plays a small but useful role in creating friendships across frontiers. But occasionally there are problems.

Several years ago, when the powerful Russian "woodpecker" radar was seriously affecting operations in some h.f. bands, a few amateur operators in North America made it clear that they would not contact amateur stations in the USSR until the interference-creating, over-the-horizon radar closed down. Most amateürs, however, recognised that no matter how much they disliked the woodpecker there was no hope that Russian amateurs would be able to influence their Defence ministries.

Indeed, the Russian amateurs were among those who suffered most from the woodpecker installations. It is as though we were to blame the Russian amateurs for the deliberate jamming of western h.f. broadcasts.

Then there was the period in the 1950 s when, under the Stalin government, Russian amateurs were forbidden to talk to amateurs in western countries. A ban that disappeared early in the Khrushchev era.

Following an espionage scandal involving a serving member of the RAF, some restrictions were placed on the British Services amateur radio clubs. I am not sure to what extent these were ever enforced in seeking to limit contact with Eastern bloc stations.

My reasons for recalling these events is that it now seerris that some East European amateurs have recently been declining to talk to amateurs in South Africa. Whatever one may think of a government, the idea of refusing to talk via amateur radio to its citizens seems totally against the traditions of a hobby that has always prided itself as constituting "a world-wide fellowship which knows no boundaries of race, class or creed"

## MASH Millions

The three big American commercial TV networks-ABC, CBS, NBC-are continuously locked in battle over the TV ratings. Particularly with the added requirement of being able to show that despite the growth of cable they can still attract larger audiences than ever.

In the current "all-time" list of the top ten television audiences (NTI household audience estimates) half of the programmes were transmitted in the first three
months of this year. On February 28 the final episode of MASH attracted an audience of over 50-million people for CBS, almost nine-million more than the previous best set by the "Who shot J.R.?" episode of Dallas in November 1980.

Four of the ten top audiences (No. 3, 4, 9, 10) were Super Bowl football games in 1983, 1982, 1980 and 1979. Part 8 of "Roots" (No. 5) gave ABC an audience of almost 36.4 -million in 1977. Advertising time during Super Bowl matches can cost over $£ 3800$ per second.

## Museum of Television裬酸

A new National Museum of Photography, Film and Television has opened in Bradford, West Yorkshire, as the first phase of a $£ 10$ million project. At present the museum exhibits are devoted mainly to photography and film, but it is planned to set up a major television gallery in 1984-85

The museum is closely associated with the Science Museum and has been funded jointly by the London Museum and Bradford Metropolitan Council. It includes a 340 -seat theatre equipped to show $70 \mathrm{~mm}, 35 \mathrm{~mm}$ and 16 mm film and there are also a number of computer-controlled multi-slide projectors.

Bradford seems to have stolen a march on Alexandra Palace where for several years there has been much talk of setting up a television museum in the part of the building (which escaped the fire a few years ago) that formed the original BBC high-definition TV studios in 1936.

In March this year three of the four episodes of "The Thorn Birds" mini-series gave $A B C$ audiences of 35.4 -million (Part 2), 35.99-million (Part 3) and 35.9-million (Part 4). Thorn Birds, not yet screened in the UK, is an American-made series lasting ten hours based on Colleen McCullough's novel about family life in the outback of Australia.

To obtain on three successive nights three places ( 6,7 and 8 ) in the all-time "top ten" ratings is a remarkable achievement that suggests that in the face of cable competition American TV production is learning the lessons of the old Hollywood film epics. And those who dismiss so much American TV as rubbish should ponder on the fact that the intelligently-made MASH should have finished its long run on such a remarkably high peak, while " 60 Minutes", a CBS weekly hard-hitting current affairs programme, figures prominently in the weekly "top ten" lists.

## Japanese No-code

Among the small but vocal minority of British enthusiasts who have been arguing in favour of amateur-radio h.f. licences free from the mandatory Morse test, Japan is frequently held up as an example of how international regulations can be circumscribed. The ITU's Radio Regulations insist on Morse proficiency below 50 MHz but leave it to national authorities to decide if transmissions are unlikely to cause interference in other countries, and therefore escape the international rules.

For over a decade a codeless radiotelephone class licence permitting operation of h.f. bands (other than 14 MHz ) with a power output limit of 10 watts has been available in Japan. The result has been an enormous increase in the size of the domestic amateur radio market. Almost one million Japanese amateur operator licences have been issued: considerably more than the combined total of Europe and North America!

While this has undoubtedly enabled the Japanese communications industry to become the dominating force in the world market, the state of the hobby in Japan is less healthy than might appear from that one million figure.

## High Drop-Out Rate

In the first place in Japan, as in the USA, a distinction is made between an "operator" licence and a "station" licence. The Japanese "operator" licence is a lifetime issue. The number of Japanese "station" licences is just over half-a-million, of which about 95 per cent are the lowpower "radiophone" licences.

There is a marked tendency for newcomers to lose all interest after about a year, with far less commitment to the hobby than in other countries arising from the limited value of the h.f. facility in view of the low-power of legal transmissions and the fantastic mutual interference. In effect, Japan turned its amateur radio licence into a substitute for a Citizens Band operation, though jnsisting that applicants receive some technical instruction.

The core of the Japanese amateur movement has thus remained with the small minority who have the higher grades of licence requiring a Morse test. Membership of the Japanese JARL national society has a very high drop-out rate, particularly among younger members, with new members being drawn increasingly from the over- 30 age group.

Japan is now creating a new form of short-range "Personal Radio Seivice" on 903 MHz with 79 simplex f.m. channels and a transmitter power of 5 watts. Perhaps an admission that you cannot combine $C B$ and true amateur radio licences.


BY L.S. COOK

Some electronic circuits demand a power source that will supply a positive and negative rail. In such a case, the circuit described here could be a useful extension to the existing power supply unit (p.s.u.), provided the latter can produce at least 135 mA at 9 V . The output from the Voltage Dualiser is approximately plus and minus 8 V (or a total of 16 V ) at up to 60 mA , the voltage varying a little with the current being drawn.

Electronic overload protection and indication have been incorporated into the design as a p.s.u. is unlikely to match the requirements in this respect.

Operational amplifiers frequently employ a dual supply, and voltmeter preamplifiers based on high input-impedance op-amps, are becoming increasingly important in view of the smaller currents encountered these days.

The Voltage Dualiser is not suitable for audio or radio frequency applications since the square-wave generated produces a high-pitched whistle at audio (and considerably more than that at radio) frequences.

## CIRCUIT DESCRIPTION

The complete circuit diagram is shown in Fig. 1 and is essentially a form of
voltage doubler. When the switch (S1) is depressed momentarily, pins 2, 5 and 13 of IC1 are taken low causing IC1a and IC 1 b, with R2, R3 and C1 to produce a square-wave at pin 11 of IC 1 b .

This square-wave will then switch on TR2 and TR3, alternatively via buffers IC1c and ICId thus steering current through l.e.d.s D1 (green) and D2 (red), and charging reservoir capacitors C2 and C3. When TR3 is off, l.e.d. D2 draws current from C2, similarly C3 supplies D1 when TR2 is switched off. Diodes D3 and D4 prevent C2 and C3 from discharging through TR2 and TR3 as well as isolating the source from the high voltage output.

With VR1 suitably set and no overloading the emitter-base potential of TR4 is sufficient to hold this transistor on and then a current is supplied from its collector to the base of TR1, via D1 which will become illuminated. TR1 is now held on. Pins 4 and 10 of IC1 are equipotential, so D2 does not illuminate.

## OVERLOAD

In the event of an overload, the output voltage drops, as does the emitter-base potential of TR4. The transistor TR4 and TR1 are then cut off and D1 is extinguished.
Pins 2, 5 and 13 of IC1 are taken high round the circuit resulting in the os cillator, TR 2 and TR3 being switched off and D2 being illuminated. This means that pin 10 is now high and pin 4 is now low. The resistor R1 grounds the base of TR1 to stabilise this transistor.

## Golistructan

## CIRCUIT BOARD

All the components apart from SK1 are mounted on $0 \cdot 1$ in matrix stripboard,

Fig. 1. Complete circuit diagram for the Voltage Dualiser.


25 strips by 17 holes, as shown in Fig. 2. The first step is to make the 13 breaks in the copper tracks as shown.

The switch S1 is soldered directly to the stripboard making sure that it will project by a suitable amount through the lid of the case. The remaining components may now be soldered to the board. Thoroughly check the positioning of all the components, making sure that the polarities of C2 and C3 are correct.

Having soldered all the components to the board, the holes in the lid of the case for S1, D1 and D2 may be determined. No specific input connector is suggested as this will depend on individual needs.

## OUTPUT SOCKET

The output socket used in the prototype is a 3-pin DIN socket and is wired as shown in Fig. 2. It is not necessary to use this type of socket and may be left to personal choice.

The case chosen for this project was a Verobox measuring $72 \times 50 \times 24 \mathrm{~mm}$, although any box of similar size will be suitable.

## TESTING

Connect a 9 V supply to the circuit and the red l.e.d. (D2) should light but then should extinguish, the green l.e.d. (D1) should now light up. The setting of VR1, to operate the cut-out at the appropriate point is quite critical and can be carried out by placing a 120 ohm resistor across one-half of the output (test each half in turn). Adjust VR1 so that the cut-out circuitry is just, and only just, activated. With a 150 ohm resistor similarly placed the unit should remain on.

Measure the output voltages for each half, they should each be about 1 V below that of the supply, the drop occurring across the semiconductors. If further tests are needed the voltages at pins 4,10 and


11 of IC 1 can be measured. With the output on they should all register about 4 to 4.5 V and with the output off, pin 10 should be at just under 9 V and pins 4 and 11 at zero.


# LETTERS 

## EE Excels

Sir-Many thanks for publishing my letter concerning the formation of the "National Electronics Correspondence Club". (See July issue.)

I have received a number of enquiries to date, entirely due to Everyday Electronics and hope the response continues enabling me to contribute in a small way to the furtherance of the electronics hobby, as you are successfully doing in a larger way.

I wish you continued success with your publication, which, in my opinion excels to cover all of the various specialised subjects within electronics.
E. Foley;

Levenshulme, Manchester.

## Exam Time

Sir-The Walsall Education Dept. have been operating courses to sit the Amateur Radio Exam No. 765 at Barr Beacon School for several years.

The examination can be taken at the school in December, March and May, and the new enrolment starts 22 September at 7.00 pm .

Those with adequate education in electrical theory may also sit the December and Māy exam. Courses are also provided for the beginner.

The teaching technique has been modified to suit CB enthusiasts who require more facilities.
F. A. Fear G8CVR,

Aldridge, Walsall.

Sir-The Hendon College Science and Technology is offering a part-time evening course for persons wishing to take the City \& Guilds 765 Radio Amateur's course.

Attendance is required on one evening per week for one year.

No special qualifications are required for admission since the course will start from basics, although candidates intending to take the examination in May 1984 will have a better chance of success if they possess some background in electricity.

Enrolment commences on 13 September at the Williams Building, The Burroughs, Hendon, NW4 4BT.

Chris Holford,
Hendon,
NW4 4BT.
Sir-No educational requirements nor is there an upper age limit for entrance to the Radio Amateurs Examination (R.A.E.) course being run by Brixton College for Further Education. The only requirement is an enthusiasm for Amateur Radio is essential.

Students attend for one evening a week from 6.30 pm to 9.00 pm . Students sit for the R.A.E. in the following May and the course then continues with a Morse class to the standard of the British Telecom Morse test.

Enrolment commences on 5 September at Brixton College, Ferndale Road, London, SW4 7SB.
R. McEwan Reid,

Brixton,
London.

## Resist-ance

Sir-I have two questions. First, I would like to know that when a speaker is said to be 50 ohms, does it mean that if an ohmmeter is connected across the speaker leads, it will read 50 ohms?

Secondly, how do you transfer a printed circuit board layout from the page to a piece of copper clad board? Your help will be highly appreciated:
E. Ali,

Bahrain,
Iran.
To answer your first enquiry, all loudspeaker specifications quote a nominal
input impedance. As a speaker contains a voice coil (that is, an inductive component) the impedance is not purely resistive and so varies considerably with frequency. A peak in the impedance occurs at the main bass resonant frequency of the speaker and the nominal quoted value of a speaker indicates the impedance at the first dip following this peak!

All this may sound a little complicated, but it is quite important when matching loudspeakers to high-quality hi fi amplifiers where the typical value is in the region of 8 ohms.

However, in the case of a 50 -ohms speaker, the variation of the impedance due to the inductance of the coil will be far less and the d.c. resistance (that is, when measured with an ordinary ohmmeter) will be a good indication of the speaker impedance. So it is fairly safe to say that the ohmmeter put across the leads of a 50 ohms speaker will read about 50 ohms.

Your second enquiry requires a little more discussion. There are several methods of producing p.c.b.s from diagrams in $E E$. All current boards are available from the EE PCB Service but some constructors may wish to make their own.

In the UK, readers can purchase special kits, an example being the Electrolube CM100, to photographically reproduce the layout and transfer it to the copper. But thesen are expensive and,l am unsure of the availability overseas.

A simple method is to lay the page over the copper clad board and mark the positions of all pad centres with a sharp point. This also helps to locate the drill when drilling the holes at a later stage. To avoid damaging the magazine pages, a photocopy of the layout can be used as the template.

The layout can then be carefully copied with an etch-resistant pen or dry-print transfers and crepe tapes. Alternatively, the track layout can be traced through using carbon paper and then the etch-resist applied. Before etching the board in ferric chlokide, it is always wise to thoroughly check the layout as it is easier to take copper off than to put it back later!


ELECTRICAL INSTALLATION
Authors A. O. Akintante, J. M. Hyde
Price $\quad £ 3.25$ limp
Size $210 \times 260 \mathrm{~mm}$. 146 pages
Publisher Macmillan Education Ltd.
(Introduction to Technology series)
ISBN 0333346807

Zundamentally, Electrical Installation is a textbook providing a theoretical introduction, with practical applications, for students taking City and Guilds courses. It meets all the needs of the major examining boards and uses SI units, I.E.E. regulations and British Standards throughout.

Many students and apprentices in allied engineering disciplines or those just interested in the generation, supply and distribution of electrical energy should also find benefit in this
book. The text is presented in a clear and highly illustrated way and no previous knowledge is assumed. Examples and exercises from old exam papers are included in each section.

The nine sections cover subjects ranging from safety and workshop processes through transmission, distribution, wiring and earthing of electrical systems, to installation, maintenance and metering. The final chapter discusses contract planning and there is two appendices containing electric motor data. G.P.H.

## BASIC \& PASCALIN PARALLEL

Author<br>S. J. Wainwright<br>Price $\quad £ 1.50$ Limp edition<br>Size Publisher ISBN $180 \times 110 \mathrm{~mm} .60$ pages Bernard Babani 0859341011

An unusual type of computer book which deals with the two computer languages, Basic and Pascal. The book takes the two languages and develops the idea of programming in both languages simultaneously.

The book contains eight chapters, the most useful being The Development of a Computer Program. Each new topic is easily explained, with program listings in both Basic and Pascal, so that program construction may be compared.

A useful book that will provide relative newcomers to computing with a useful program construction guide.
R.A.H.


## CLEANING STICK

Aglass-Fibre "stick", ideal for removing the protective coating found on the copper tracks of some p.c.b.s, is announced by Eraser International.

Available in both fine and coarse, the BR1 Fyberglass Cleaning Brushes are manufactured from pure spun glass and are also suitable for use on gold edge connectors, commutator cleaning and general oxidisation removal.

Further information can be obtained from:

Eraser International Ltd., Dept EE, Unit M,
Portway Industrial Estate,
Andover, Hants SP10 3LU.

## MEMO PAD

ABRITISH designed electronic memo pad which can carry out the functions of a calendar, diary, address book, note pad and expense account $\log$ has been marketed in the UK by Domicrest Ltd. Called the Biztek Pad it will retail for $£ 69.95$.

Comprising a potential memory of 4000 characters, the Biztek Pad combines a 43-key keyboard with a large liquid crystal display with 16 alphanumeric characters, 20 numeric and clock digits and 45 special symbols.

When used as a conventional note pad, the unit will record messages consisting of words and/or figures in its internal memory and display them again on demand. For example, the fluctuations of a company's share
simply by keying in the first few characters of the person's name and pressing the recall button.

Appointments or personal engagements such as weddings, birthdays and anniversaries may be stored and by adding a date and a time, and utilising the alarm function, the pad can be programmed to sound the alarm at the appointed time. The necessary reminder can then be recalled to appear on the screen.

For the expense account $\log$, the user keys in the relevant wording, enters the amount and presses the appropriate key to display one of the following: Auto, Entertainment, Hotel, Meal, Mileage, Phone or Travel.

Additional functions of the "memo pad" are a calculator, stopwatch and a permanent calendar and time display. A memory save switch ensures that

## ELECTRONIC CONTROL SOLDERING IRON

Amains voltage electronically controlled temperature soldering iron has just been marketed by Light Soldering Development.

The EC50 incorporates an electronic temperature control circuit module mounted inside the handle. This operates in response to a thermistor fitted inside the bit-mount.

Power to the 50 W heating element is controlled by a triac operated by a zero-voltage switching i.c., to minimise spiking and r.f.i. The iron is fully earthed so that it may safely be used on sensitive equipment and components.
A feature of the design is that the low voltage supply for the control module is obtained by means "(for which a patent is pending) which does not involve fitting a dropper resistor in or near the handle. It is claimed that
this problem previously prevented a mains iron of this type being made to run with a sufficiently cool handle.
The control circuit provides a proportional control band, so that power to the heating element is only fully on or off outside a temperature band centred on a preset value. Within this proportional band, power supplied in regular pulses of equal interval but of a length which varies according to the difference between "actual" and "set" temperatures.
Access is provided to the temperature control potentiometer, and settings may be varied from approximately $280^{\circ} \mathrm{C}$ to $400^{\circ} \mathrm{C}$. Standard setting is $370^{\circ} \mathrm{C}$.

For details of prices and nearest stockists, write to:
Light Soldering Developments Ltd., Dept EE, Spencer Place, 97/99 Gloucester Road, Croydon, CRO 2DN.



E LECTRONIC components are represen-- ted by symbols in circuit diagrams, some only differing in minute detail, such as the arrow direction on the emitter lead of bipolar transistors. The circuit symbol is also accompanied by a component reference letter(s) followed by its unique numerical position number in the circuit design (not its type number which usually follows this). Shown here are the circuit symbols and component reference for most of the components used in EE.

## RESISTORS AND POTENTIOMETERS



Variable resistor with preset control


Potentiometer with preset adjustment

## CAPACITORS





## SEMICONDUCTORS



Transistor $n p n$
Transistor $\rho n p$


Junction field effect transistor, $n$ channel


Unijunction transistor with $n$ type base


## miscellaneous



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\begin{aligned}
& \text { Battery with more } \\
& \text { than one cell }
\end{aligned}
$$

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 lluminating lamp


| Neon lamp or |
| :---: |
| indicator |


| Single pole single |
| :---: |
| throw switch |



Rotary switch, single
pole three-way $\quad \begin{gathered}\text { Single pole push-to- } \\ \text { make switch }\end{gathered}$


Single terminal socket and plug


Integrated circuit differential operational amplifier

LOGIC SYMBOLS


EX-OR


BUFFER




Variable inductance coil with ferrite core

Transformer with laminated core, no tappings

Two conductors crossing with no connection

Junction of connection of three conductors

Earth" connection: (a) to earth
(b) to chassis

Continous screened

# circuit EXCHANGE 

This is the spot where readers pass on to fellow enthusiasts useful and interesting circuits they have themselves devised. Payment is made for all circuits published in this feature. Contributions should be accompanied by a letter stating that the circuit idea offered is wholly or in significant part the original work of the sender and that it has not been offered for publication elsewhere.

## BLEEPER CIRCUIT

THIS circuit is simply two transistors TR1, TR2 switching each other on and off; but with the bleeper WD1 in circuit you do not notice this action as the capacitors C1, C2 keep the bleeper on until the power is pulsed to the opposite transistor.


The transistors can be almost an $n p n$ silicon type like BFY51, 2N2926, BC 108 , BC 107 and BC 109 . The circuit can work on 3-6 volts and the bleeper is a Bleeptone 12 V type. It can be used as a burglar alarm or a siren. (The bleeper tone can be changed by fitting a capacitor of a few hundred microfarads across the output to the bleeper.)

Martin Gill,<br>Romford,<br>Essex.

## ALARM CIRCUIT

have designed a simple, but nevertheless, very effective alarm circuit based upon the operation of the thyristor or SCR. When the two probes " $X$ " and " $Y$ " are connected the gate of CSR1 is made positive with respect to the cathode, and this in turn switches it on

The thyristor now behaves like a common-place silicon diode. Even if the gate current is removed CSR1 will still remain on until the so-called "holding current" is reduced below a certain level. This can be attained by disconnecting the power supply (that is, a PP3 9V battery), using S1. The warning device used was a 6 V solid state buzzer.
S. D. Stares,

Totton,
Hampshire.


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150 watt Output, Slave 500 mv Input 30 anker $150+150 \mathrm{w}$ Stereo. 300 w Mono Slave 500 mv . Inputs 150 w P. . Vocal. 8 inputs High/Low Mixer Echo Socket 100 watt Valve Model. 4 inputs. 5 Ouiputs Heavy duty 60 watt Valve Model. 4 inputs. Low imp and 100 v line output BAKER LOUDSPEAKERS

| Make | Model | Size | Watts | Ohms | Price |
| :---: | :---: | :---: | :---: | :---: | :---: |
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| Baker Hj -Fi | Superb | 12 in | 30 | $8 / 16$ | £26.00 |
| Baker P.A. | Group 45 | 12in | 45 | +/8/16 | ¢16.00 |
| Baker Hi-Fi | Auditorium | 12in | 45 | 8116 | £24.00 |
| Baker $\mathrm{Hi}-\mathrm{Fi}$ | Auditorium | 15in | 60 | 8/16 | ¢37.00 |
| Baker P.A. | DG75 | 12in | 75 | 4/8/16 | \$20.00 |
| Baker P.A. | Group 100 | 12 in | 100 | $8 / 16$ | ¢26.00 |
| Baker PA. | Disco 100 | 12 in | 100 | $8 / 16$ | £26.00 |
| Baker P.A. | Group 100 | [Sin | 100 | 8/16 | £35.00 |
| Baker P.A. | Disco 100 | 15 in | 100 | 8/16 | E35.00 | DRILL SPEED CONTROLLER LIGHT DIMMER KIT Easy to build kit. Control up to 800 watts $A C$ mains with plastic case

$4 \times 3 \times \mathrm{E}=\mathrm{in}$. $£ 5$ less case $£ 4$.

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| Complete kit of parts. printed circuit. Cabinet. Mains transformer channels Up to 1.000 watts each. Will operate from 200MV to 100 wats signal. Suitable for home Hi-Fi or Disco. £19. Post $£ 1$ OR COMPLETE READY BUILT IN CABINET $£ 27$. |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 200 Watt Rear Reflecting White Light Bulbs. Ideal for Disco Lights Edison Screw 75p each or 6 for $£ 4$ or 12 for $£ 7.50$. Post $£ 1.50$ per sik. |  |  |  |  |  |
|  |  |  |  |  |  |
| M 4 INS TRANSFORMERS Primary 240 V A.C |  |  |  |  |  |
| $250-0.250 \mathrm{v} 80 \mathrm{mA}$. . 6.3 V 3 |  |  |  |  |  |
| $350-350 \mathrm{Y} 250 \mathrm{~A}$. $6.3 \mathrm{~V} 6 \mathrm{Amp} \mathrm{C.T}$ |  |  |  |  |  |
| 220 V 25 ma . 6 | 220 V | 45 m | 6V 2 A | \$4.00 |  |
| 250 V 60 ma 6.3 V 2 A . |  |  |  |  |  |
| General purpose tapped outputs voltages available |  |  |  |  |  |
| $2 \mathrm{amps} 3,4.5 .6 .8 .9 .10,12.15 .18,25$ and 30V |  |  |  |  |  |
| 1 amp 6.8 .10 .1 | 20.24.3 | 30.36 | -0,48.60 | E6. |  |
| 2 amp 6.8.10.12, 16. 18,20.24.30, 36, $+0,48.60$ |  |  |  |  |  |
| 3 amp 6.8.10.12.16.18.20.24.30.36.40.48.60 ...... 112.5 |  |  |  |  |  |
| $5 \operatorname{amp} 6.8 .10 .12 .16 .18 .20 .24 .30 .36 .40 .48 .60$.......... $£ 16$ |  |  |  |  |  |
| 5-8-10-161 如 | ¢3.00 | £1 | 12-0-12V 2 a | $\pm 4.5$ |  |
|  |  |  |  |  |  |
|  |  |  |  |  |  |
| 9 V 250 ma ........ 1.50 £1 20 V |  |  |  |  |  |
| 9 V 3 a ......... 84.00 \&1 20-(0)-2 |  |  |  |  |  |
| $9.0-9 \mathrm{~V} 50 \mathrm{ma}$. $\quad$ ¢1.50 f 1 |  |  |  |  |  |
|  |  |  |  |  |  |
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| 12V750ma …c.e.s2.50 il 30V 5a |  |  |  |  |  |
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[^1]:    * Equivalent to Varta 100 DK0.

[^2]:    * Includes Command List with examples

