## 3ay to buld projects for everyone andiancs



# electrowize ELECTRONIC IGNITION KITS 

Two years ago we launched the Total Energy Discharge System, we knew it could outperform any competing system and the sales have proved just how good it is. With thousands of systems sold in over 30 countries around the World, from the cold of Norway to the tropical heat of Singapore, the system is an outstanding success.
THIS IS WHAT MAKES TOTAL ENERGY DISCHARGE SO GOOD-


The discharge circuit in block $A$ is the heart of the system. It looks simple but outperforms any other by far. A $2 \mu \mathrm{~F}$ storage capacitor (twice the usual size) charged to +370 volts, is discharged into the ignition coil primary by SCR1, providing a high energy pulse of the correct polarity. Long after the storage capacitor is discharged, the current in the ignition coil is sustained by 'flywheel' diode D4, preventing energy flowing back to the capacitor and giving $31 / 2$ times the spark energy and duration. Instead of relying on the effects of coil 'ringing', inductor L1 commutates the SCR, giving complete freedom from the usual latching problems and allowing the storage capacitor to be recharged whilst the discharge current is still flowing in the coil.

Block $B$ is the trigger circuit and provides faultless spark timing. The emitter of TR 1 is biased from the supply to provide a variable trigger threshold, allowing triggering with the supply down to about 3.5 volts but rejecting noise and signals from contact shuffle and vibration. Capacitor C3 and its associated resistors provide a variable inhibit period, after the contacts close, which filters out extreme contact bounce on 4 cylinder engines yet still allows 8 cylinder operation to over $7500 \mathrm{rev} / \mathrm{min}$. In effect the longer the contacts stay open the longer they must remain closed before the next spark can be triggered. (Be warned:- untimed sparks can seriously damage your engines health).

Block C is the inverter, the power behind the spark. It's a 'ringing choke' type. Well designed, this type can not only be regulated and charge the capacitor from zero volts, effectively a short circuit, but is also more efficient than the traditional push-pull type. Even though it provides around 3 times the power, it still doesn't need the usual finned heat sink. Transistors TR4 and TR5 regulate the invertor output, by controlling the amount of feedback, and are in turn controlled by TR3 which compares the voltage on the storage capacitor with the reference zener D5. The output voltage is set by the zener voltage so the full output is available over the whole supply voltage range, a powerful spark is produced even with the battery down to 4 volts.

These are the more obvious features, there are many more details like the absence of 'spikes' and low di/dt and dv/dt applied to the SCR, which together with top quality components make Total Energy Discharge not onlv a top performer but far more reliable.

This advanced circuitry gives all the well known advantages of the best capacitive discharge systems:
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PLUS
Super Power Spark; Better Starting; Optimum Spark Duration; Correct Spark Polarity; L.E.D. Static Timing Light; Low Radio Interference; Designed in Reliability.

| TOTAL |  |
| :---: | :---: |
| ENERGY |  |
| DISCHARGE | ORDINARY <br> CAPACITIVE <br> DISCHARGE |
| 140 W | 90 W |
| 36 mJ | 10 mJ |
| 135 mJ | 65 mJ |
| $500 \mu \mathrm{~S}$ | $160 \mu \mathrm{~S}$ |
|  |  |
| 38 kV | 26 kV |
| 26 kV | 17 kV |
|  |  |
| $25 \mu \mathrm{~S}$ | $30 \mu \mathrm{~S}$ |

You can buy your Total Energy Discharge system as a ready assembled and tested unit ready to fit to your car or as a comprehensive kit of parts containing everything required, even a length of solder and a tube of heat sink compound. The kit comes complete with detailed, easy to follow instructions which enable even a beginner to assemble a kit in just a matter of hours.
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## YOUNG READERS PLEASE NOTE

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A school may be represented by either an individual or a team of puplls.
The usefulness and novelty of a project are prime matters for consideration during judging. The degree of circuit complexity is not of major importance, since good design implies efficient use of components. It follows therefore that a quite simple circuit could win.
Please turn to page 721 for details and then contact your science or physics teacher.
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$2-12$ way $20 / 2-6 W, 30 / 2-4 W, 40 / 2-5 W$. $2-12$ way $2 \mathrm{p} / 2-6 \mathrm{~W}, 3 \mathrm{p} / 2-4 \mathrm{~W}, 4 \mathrm{p} / 2-3 \mathrm{~W}$
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## BRAVE NEW WORLD

A
n official view of the Brave New World soon to evolve has been given by the Minister for Information Technology. Speaking at the British Association in September, Mr Kenneth Baker MP painted a cheerful picture and was optimistic about what laid in store.

The central point of his story was that society would be greatly changed in its habits; the established life pattern of school, work, leisure and retirement would be broken. Learning would be an on-going process throughout life, in part dictated by necessity to retrain for new jobs created by advances in technology, and in part as purely recreational and cultural pursuits to help fill in those longer periods between work stints. For work would be performed in 2, 4, 6 or 8 hour "packages", according to individual preferences-at home on many instances.
The decentralisation of work was seen as a great advantage: sociallyin helping to nurture family life; politically and economically-in encouraging smaller industrial units that will be more flexible and efficient than over-centralised bureaucratic hierarchies.

According to Mr Baker, one out of two of the present UK workforce come within the classification of "information operators". This means those who are employed in service trades or professions that involve the organisation, transmission, storage and retrieval, application or creation of information; plus engineers and factory workers who make electronic equipment for the information economy.

Can man live by information alone? But of course there remains the other half of the working population to grow the food, build the houses, make the motor cars and produce those other little necessities that keep body and soul together.

And what of this technology upon which all depends. Is it a new technology or is it merely an amalgamation of well known services that have been long available? Our new series The Electronics of Information Technology starting this month will endeavour to get to the truth of the matter.

It seems ironic that salvation from over-centralised industries and government agencies is to come via the most highly organised use of electronic based services, with their interdependence one on the other.
Are we to be released from one net to become ensnared in another?
The strands linking the information networks of the future may be gossamer-like optic fibres but their data carrying capability will be tremendous. They will constitute a powerful means to influence, as well as inform people inside their homes and elsewhere.
This is one aspect of the coming Information Economy that could give cause for concern.

## Readers' Enquiries

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Fig. 1. Circuit diagram of the Combination Lock unit.

Inputs PL1-6, 7, 8 and 9 are used as a reset input, along with TR1, R2 and R3. When a high input is taken to one of these inputs, it goes via R2 to TR1 which is switched on and therefore puts R3 in paralled with R4. This changes the biasing on pin l of IC2a and causes it to go low (logic 0 ). If pin 1 is low then pin 3 will go low and also pin 2. As ICla has an input connected to the output of IC2a then it will also produce a low output and so will each successive gate in the system until all outputs are low.
Therefore a high input at PL1-6, 7, 8. or 9 causes the whole logic system to be reset and await a high input at PLI-1 to restart the sequence again.

## OUTPUT STAGE

Pin 8 of IC2d is the output of the logic system and is connected to the base of TR2. When pin 8 goes high, that is, when the combination input is correct and complete, TR2 is switohed on. This causes a current to flow through R5 to the base of TR3 and therefore switch it on and in turn energise the relay coil.
An integral diode in the di.i. relay is used to protect TR3 from the back e.m.f. of the relay coil and C1 is used to decouple the supply voltage.

## COMBINATION SWITCH

The circuit diagram of the combination switch unit is shown in Fig. 2. The +ve supply is brought in via and the ENTER switch, S2, to the pole of rotary switch S3. The number of the first digit of the combination must be connected to SK1-1, the second digit to SK1-2, and so on to the fifth digit connected to SK1-5.

The four unused digits are wired to SK1-6 to 9. 74 mm , as shown in the photographs. screened cable, the screen forming the common connection.


In this way, when the switch S 3 is turned to the relevant number and the enter button is pressed, the logic high will be entered on the corresponding input on PL1 on the main unit.
Any combination can be set up, but the same number must not be used as consecutive digits and all numbers must be conneoted to SK1


## COMPONENT BOARD

The components were mounted on a printed circuit boand measuring $95 \times 36 \mathrm{~mm}$, as shown in Fig. 3. Start by inserting and soldering the Venopins and the 9 -way minicon plug assembly, PLl, then the six short wire links. Resistors R1 to R5 oan then be soldered in. Ensure correct orientation of C1 and also the relay RLA1 which is a special di.i.l relay with its own internal protection diode, but an additional diode is required if a different relay is used, in which oase it should be soldered across the coil terminals on the relay. Solder in TR1, 2 , and 3 with care and also observe correct orientation of their leads.

IC1 and 2 can be soldered in but i.c. sockets are preferred.

## CASE

The main unit was housed in an aluminium case measuring $100 \times 40 \times$
Resistors

| Resistors |  |
| :--- | :--- |
| R1,3 | $4 \cdot 7 \mathrm{k} \Omega$ (2 off) See |
| R2 | $100 \mathrm{k} \Omega$ |
| R4 | $5 \cdot 6 \mathrm{k} \Omega$ |
| R5 | $1.8 \mathrm{k} \Omega$ |
| All $\frac{1}{4} \mathrm{~W}$ | carbon $\pm 5 \%$ |

Capacitors
$100 \mu \mathrm{~F} 10 \mathrm{~V}$ elect
Semiconductors

| TR1,2 | BC108 silicon npn (2 off) |
| :--- | :--- |
| TR3 | BFY51 silicon npn |
| IC1,2 | 74LS08 Low power |
|  | Schottky TTL quad |
|  | 2-input |
|  | AND gate (2 off) |

Miscellaneous
S1- s.p.s.t. miniature toggle switch
S2 Push-to-make miniature keyboard switch
S3 Single pole, 9-way rotary switch
RLA Miniature di.i.l. relay, 4 $10 \mathrm{~V}, 500 \Omega$ coil
B1.4 $1 \cdot 5 \mathrm{~V}$ HP7 cell ( 4 off)
SK1 9-way minicon socket, 0.1 in. pitch

SK2 3.5 mm jack socket
PL1 9.way minicon plug, 0 1in. pitch
PL2 $\quad 3.5 \mathrm{~mm}$ jack plug
Single sided p.c.b. $94 \times 36 \mathrm{~mm}$; aluminium case, $102 \times 70 \times 38 \mathrm{~mm}$ (type AB9); die cast box, $50 \times 50 \times$ 30 mm ; 9 -way screened cable, 1 m long; battery holder for 4 AA size cells; battery clip; P-clip; 14 pin l.c. holder (2 off); grommet (2 off); $7 / 0 \cdot 2 \mathrm{~mm}$ wire; knob;6B Acountersunk screws, 10 mm long ( 4 off ); 6BA nuts ( 4 off); 6BA clearance spacers, 6.4 mm long ( 4 off ).

S1 and SK2, the relay output, were mounted on one end of the case along with a grommet for the 9 -core cable to pass through.

The p.c.b. was mounted on one side of the case so that the 6BA countersunk screw heads would be covered by the lid of the case. The p.c.b. must be spaced away from the side to prevent the lid fixing screws from being obstructed by it.

The battery, made up of four HP7 (size AA) cells in a special holder, was held in place by an aluminium bracket made to suit the battery and case. This was fixed to the opposite



Fig. 4. Wiring of the remote combination switch for the number given. Any five figure number can be set up by rearranging the connections to SK1. Note that if a different switch is used for S 2 , it may require more space.


Top view with cover removed clearly showing the battery pack retaining bracket.


Complete Combination Lock system with a test lead plugged into the relay output to connect it to a solenoid or similar device.
side of the case from the p.c.b. by a 6BA countersunk screw, nut and shakeproof washer.

This bracket was also used to hold the 9 -core cable in place with a P-Clip. The 9 individual cores of the cable were connected to the p.c.b. by a 9 way minicon plug (PL1) assembly, cut from a 12 -way. This could easily be unplugged from the p.c.b. and the combination ohanged as required. The screen of the 9 -core cable was used as the + ve supply connection and this was soldered to the Veropin shown on Fig. 3, from which also a wire goes to the battery + ve via S1.
The 9 -core cable goes to the remote switch unit, Fig. 4, which consists of a 1-pole 9 -way rotary switch S3 and a non-locking push switch $\$ 2$. The screen of the cable connects to the pole of S3 via S2. The other connections of S3 make up the numbers 1 to 9 for the oombination input.

S2 and S3 were mounted in a small die cast aluminium box measuring $50 \times 50 \times 30 \mathrm{~mm}$. S2 was mounted on the underside edge of the box and its connections rest against the body of S 3 , therefore holding itself in place.

## FINISH

The main unit and switch unit were lettered with rub-down lettering and covered with a spray-on varnish for protection.

The lid of the main unit was covered with cabinet covering cloth to enhance its appearance.

## COMBINATION SETTING

By use of nine core screened cable all the necessary connections in the remote switch unit could be taken to the main unit. In the main unit the nine individual cores were connected
to the p.c.b. by a minicon plug and socket assembly. This means that the socket can be unplugged from the p.c.b. The contacts in it, which are connected to the nine cores, could then be swapped round, thus changing the combination as required.

In the nine core cable used on the prototype, the nine individual cores had different coloured sleeving so if it is noted to which number each colour connects to in the remote switch unit, it would make it easier to decide what the combination is when setting it in the main unit.

The combination is a five figure number and it must be remembered that the first figure connects to SK1-1, the second to SK1-2, the third to SK1-3, the fourth to SK1-4, and the fifth to SK $1-5$, and the remaining false numbers to SK1-6 to 9 on the minicon socket.


THE unit described below has been designed to work in conjunction with the ZX81 Microcomputer and a standard cassette recorder to allow LOAD and SAVE operations to be carried out without the need to unplug leads whenever a program is saved or loaded. The unit may also, with slight modification be usefully employed by ZX Spectrum users.

## TAPE LOADING

The normal procedure in loading a program from tape, is firstly, to judge roughly where the beginning of the program starts by using the cassette tape counter (if fitted) and then to pull the plug out of the "ear" socket thereby enabling the cassette internal speaker to operate. Now because the volume setting is normally kept at about three-quarters fully on (which is often forgotten) you are invariably blasted by a $1-2 \mathrm{kHz}$ tone everytime the plug is pulled. With the volume reduced the exact position is now found before the plug has to be put back and the volume level readjusted before making a load.
With some cassette recorders the volume setting is critical and having to repeat the whole procedure several times is not uncommon before a good
load is made. With this laborious and unsatisfactory method it is only a matter of time before the ear socket or plug conneotions become intermittent causing bad loads. The same can be said for the "mic." socket where if you name your prognams using the cassette internal or external microphone, the mic. lead has to be removed and replaced everytime.
The ZX Tape Controller contains a microphone and loudspeaker to overcome these problems.

## OTHER FACILITIES

Another useful feature that has been included is a switch to erase the tape when required.
The other plug that occasionally has to be pulled out is the 9 volt supply to reset the computer. This is necessary when you cannat break out of a loop or the program has crashed for some reason and the keyboard has no effect. The only way out of this problem is to disconnect the supply to the computer. The Controller is fitted with a switch to accomplish a reset. An l.e.d. lamp gives a visual indication that power is being applied to the unit from the ZX Power Supply.

## CIRCUIT DIAGRAM

The complete circuit diagram for the ZX Tape Controller is shown in Fig. 1. The supply voltage for MIC1 and D1 are taken from the ZX Power Supply.
The microphone element MIC1 contains an amplifier fed by a sound pressure sensitive condenser. The device will operate from 2 to 10 V and has been set to operate at 4.5 volts by the potential divider composed of R2 and R3.
Resistor R1 limits the current through D1, the light emitting diode power indicator. Smoothing capacitor C2 prevents hum while Cl couples the output of the microphone element to the 4 -way selector switch S3. Switch S1, a momentary action push-to-break type disconnects the power to the computer when pressed and thereby effectively resets the system. The switch S2 when pressed shorts the recorder microphone input to earth allowing the tape to be erased with the minimum of noise.

## MODE SELECTOR SWITCH

Only two poles of the three-pole rotary selector switch S3 are used. The 4 -way action of the switch will select one mode at a time. In Fig. 1,

S3 is shown in the load position where S3a conneots the EAR socket SK1 to the ear plug, PL1.

The next position on the switch is cue. This allows you to listen to any instructions preceding the program and to set the tape in the correct position prior to loading (or saving). This connects the loudspeaker LS1 to the cassette recorder ear socket.
In spite of the small physical size of the speaker, and its low efficiency, the volume level produced by the speaker is adequate for such listening purposes with the cassette volume control set to about three-quarters fully on-normal setting. A larger speaker could be used with a 1 kilohm preset wired in series with the speaker to reduce the volume, but this would require a larger case than that specified.
The third position of S3 is save, where S3b connects the MIC socket to the mic plug. The last position, talk, connects the microphone element to the cassette recorder mic. input allowing programs to be voice labelled, and any instructions regarding the program to be included.

## SPECTRUM CONTROLLER

The lead from the Spectrum Power Supply is terminated in a different type of connector to that found on the ZX81 Power Supply. It is known as a cassette power connector and requires
a matching socket. Thus PL3 and SK3 specification will be different for the Spectrum-see Component List.

Also, the input sockets on the Spectrum are located in a different position to the ZX81 and spaced apart on the rear of the computer. The screened leads running to the plugs will need to be longer than specified below. The exact lengths will depend on the final position of the unit with respect to the computer.


## NO BOARD

As very few of the components are not case mounted, it was not thought worthwhile using a circuit board. Instead, a point-to-point wiring technique was decided upon.

All the components fitted inside a plastic case measuring only $75 \times 50 \times 28 \mathrm{~mm}$. Details for drilling this case are shown in Fig. 2.

Start assembly by soldering two short lengths of connecting wire


Fig. 1. Complete circuit diagram of the Tape Controller.

## COMPONENTS

Resistors

| R1 | $1 \mathrm{k} \Omega$ |
| :--- | :--- |
| R2 | $4 \cdot 7 \mathrm{k} \Omega$ |
| R3 | $4 \cdot 7 \mathrm{k} \Omega$ |
| All $\frac{1}{4} W$ | carbon $+5 \%$ |

Capacitors
$\begin{array}{lll}\text { C1, C2 } & 10 \mu \mathrm{~F} 16 \text { volt elect. radial } \\ & \text { lead types (2 off) }\end{array}$
Semiconductors
D1 TIL220 0.2 inch dia. red l.e.d.

MIC1 Electret condenser microphone element (Tandy 270-092A)

Miscellaneous
S1 s.p.s.t. miniature press-to-break release-to-make, 1A d.c.
S2 s.p.s.t. miniature press-to-make, release-to-break
S3 3-pole 4-way rotary, midget wafer
SK1,2,3 $3 \cdot 5 \mathrm{~mm}$ jack socket (3 off), chassis type
PL1,2,3 3.5 mm jack plug ( 3 off)
LS1 $8 \Omega$ loudspeaker 28 mm diameter (J. Bull)
Plastic case $75 \times 50 \times 28 \mathrm{~mm}$; pointer knob; screened cable; rubber feet ( 4 off); connecting wire; solder.

ZX Spectrum
Components as above except:-
SK3 2.5 mm cassette power supply socket (Maplin HH62S)
PL3 2.5 mm standard cassette power supply plug (Maplin HH86T)

Approx. cost
Guicance only $£ 6.00$
(about 50 mm long) to each of the sockets SK1, 2 and 3 then fix the three sockets to the case. Now wire the three plugs PL1, 2 and 3 each with about 25 cm of screened cable.

## GLUED COMPONENTS

Next glue the loudspeaker in position with an adhesive such as Evo-Stik Clear.

The microphone sound aperture is only 2.5 mm in diameter and is located in the centre at one end of the element and is masked by a disc of cloth. To fix it to the case spread a film of glue around the edge of the cloth only, then hold the element with its connections positioned as shown in Fig. 3 centrally over the hole until the glue sets.

Next glue Dl to the case with it's anode lead facing towards the loudspeaker.

At this stage the assembly is best left for a few hours to allow the glue to harden.

When the glue has set, locate and secure the three switches and finally


The Tape Controller in use with the $\mathbf{Z X 8 1}$ showing top panel labelling.


Close-up view of the internal construction of the prototype unit.

complete all the wiring as shown in Fig. 3. It is a good idea to trace each connection on the plan as it is made using a coloured pencil or fell-tip pen.

## FINISHING OFF

The labelling of the Controller was made with dry transfer lettering (No. 1) onto coloured self adhesive labels (HP010) both obtainable from W. H. Smiths. See photographs for labelling details.
A rectangle of ribbed rubber matting was glued to the underside (the case lid) to prevent the unit from sliding around in use. Alternatively four self-adhesive rubber feet could be used.
With the labels attached, rotate and secure S3 so that when the pointer knob is fitted, it correctly aligns with the label marked positions.

TESTING
Carefully inspect all the connections and cheok that the unit is wired up correctly.

Connect the Sinclair Power Supply to the Tape Controller only and see that D1 lights up and is extinguished when S1 is pressed. If so, then plug the 9 volt lead (PL3) into the computer to check that power reaches the computer, and that S1 removes it. The computer will need to be plugged into the T.V. set in the normal manner to determine this.

Finally, plug the EAR and mic leads into the computer and tape recorder and test all the functions.

## CONTROLLER TIPS

1. Using the inbuilt microphone (Talk facility) to name and list
any instructions about your programs prior to the actual program itself is a good idea because what may be very clear when the program is first written may not be so clear a month or so later.
2. By pressing the erase for a short period after the save report code comes up on the screen will leave a clean section of tape and thus ready for your next save.
3. Save all your programs at least twice before re-loading to verify the save.
4. Reset the computer (by pressing RESET) only when you are certain the program has either crashed or gone into an endless loop. Remember once pressed all the memory is cleared.

## Ever the Optimist

The optimist is the fellow who says, "When one door shuts, another door opens"; while the pessimist says, "When one door shuts, another door closes". I belong to the former school of thought, although I am bound to admit that I must agree with Dr. Johnson when he said, apropos a friend of his remarrying after losing his wife, "A triumph of hope over experience"
I was therefore pleased to see that although Home Radio (Components) Ltd. had ceased business, T. Powell has just opened an electronics components shop in Edgware Road, London (see Shop Talk last month). If I may enlarge on that, $T$. Powell is handling the complete range of Cyldon compression trimmers and the A. 70 Short Wave aerial (designed very much on the lines of the original Eddystone Aerial). Both of these items were formerly stocked by Home Radio.
Enquiries about any other Home Radio items should be made to: Mr Alan Mason, Maydale Electronic Services, 2, Wellesley Parade, Godstone Road, Whyteleafe, Surrey CR3 OBL. I should add that the stock includes the "Universal Chassis"

## London Scene

I know that a large number of our readers must live in or around the Capital and so for reasons that will be apparent later I decided to look at the London scene. I did this with a view to informing constructors what is available in the way of component suppliers.
I decided to start off with Lyle Street, just behind Leicester Square. Readers who know it will be aware that at one time, besides a large number of surplus shops, there were many other unusual trades.

I can now report that it is a respectable area, but alas the surplus electronic shops are reduced to three, namely "West End Radio", "Direct Electronics" and "Service Trading"
Next I turned my attention to Edgware Road, Wt. Now here is a Mecca well worth exploring.

From the point where Praed Street crosses it and for the next half mile northwards towards Edgware, there are component shops and surplus shops on both sides of the road, literally dozens. I even noticed Henry's Radio back in business after their misfortune a few years ago.

## Company Report

While I was in the Edgware Road I decided to call on Mr Leslie Cohen, son of the founder of Radio and TV Components Ltd. (RT-VC Ltd.), one of the real success stories in the electronics business.
Many a person has found fame and fortune because they were sacked, and this is the fate that overtook the founder of RT-VC, David Cohen. Unflt for war service he worked for E.M.I. for the whole of the war and was made redundant at the very end of it.
He said it turned out to be the biggest favour they ever did him. He worked for Jesse Bull and then early in 1950 opened a shop in Acton.
His lucky break came when the Burgoyne Soldering Iron Company's assets were auctioned. When this particular lot came up, all the other dealers had gone out for a smoke and there was only Mr Cohen left to bid and consequently he bought the whole quantity for a very low price. So low, in fact, that he was able to sell them to the public for $19 / 6$ ( $97 \frac{1}{2} p$ ). The Burgoyne Iron was the first of the quick heat irons, the forerunner of the Weller.
When ITV started, RT-VC were quickly on the scene with a convertor kit, offering the aerial convertor kit and ten yards of cable for £2:19:6 (£2.971 2 ).
Their next success came as the result of a tragedy; the De-Havilland Comet disasters. When all the Comets were grounded, David bought pieces of the airframe and converted them into TV aerials. They did have a slight setback with this, as initially the aerials would not work and it turned out that the metal was anodised and the anodising was a perfect insulator.

At this point I asked Leslie Cohen how long he has been interested in electronics himself, and he replied "All my life. When I was young, the war was on and there were no toys, so I played with bits and pieces of radio equipment,"
Returning to the company history, after this came the transistor and a whole series of kits followed. Simple t.r.f. kits, superhets, and then battery portables. Also, RT-VC were the first firm to offer a stereo kit to the home constructor for under $£ 25$.
They opened their shop in Edgware Road in 1963, and in 1964 sold 50,000 car radio kits. David Cohen died in March 1978 and Leslie became managing director, his other two directors being his mother and Mr Nemes who is a technical genius.
Apart from being an astute business man Leslie Cohen is a skilled designer himself, and several designs which have appeared in the electronic magazines came from his drawing board.
When I tell you that since 1955 RT-VC have produced two and a half million kits I think you will agree that this is one of the most successful electronic businesses in the trade today.
I like to hear of new businesses starting up, but it takes a brave man to do it these days, especially if you consider the following statistics. "One business in three fails in the first year, and four out of five fail in the first five years". A sobering thought for the would-be entrepreneur I

## Conundrum

Whenever I possibly can, I like to leave readers with a puzzle to solve. It is not entirely divorced from electronics, as electronics concerns communication and so does my conundrum.
Imagine being imprisoned as a spy and the occupant of the next cells knocks on the wall. How would you communicate with him? To make it more interesting, I' will tell you that he doesn't know the Morse Code.
await your replies with great interest. All will be revealed next month.


AST month we explored circuits which contained only resistance. This time we shall add capacitance. But first, a look at a special form of resistance which is one of the most useful devices in electronics.

You know it as a volume control. It is also referred to (not quite aocurately) as a potentiometer, or "pot" for short. It is an adjustable device (Fig. 2.1a) with a resistive track which runs from $A$ to $B$ over which slides a moving contact $C$.

If a voltmeter MEl is connected between slider ( $C$ ) and one end of the track ( $B$ ) as shown, then as $C$ is slid from $B$ to $A$ slowly, the voltage indicated rises smoothly from zero to the full battery voltage (here 6 V ). The slider C can be set to "tap off" any voltage required, up to the full battery voltage.

## POTENTIAL

The voltage at a point like $C$ is often called a "potential". Potential means possible as opposed to actual. The voltage measured by the meter ME1 may be rather less than you expect. This is because the meter itself draws current.

This current, in flowing through the upper part ( $A-C$ ) of the track, causes a fall in voltage which adds to the fall caused by the main flow from $A$ to $B$. So the voltage registered by ME1 is less than the potential at $C$.

## POTENTIOMETRY

In the second circuit (Fig. 2.1b) a cell of unspecified voltage (but less than 6 V ) is included. If $C$ is moved to a point on the track where the voltage is exactly the same as the cell voltage then no current flows either
into or out of the cell. This is because the 6 V battery is trying to push current into the cell via $C$, and the cell is trying to push current the opposite way, in at $C$ and out at $B$. The two efforts cancel at this one point, the "balance point".
Since no current flows via $C$, the voltage at $C$ must now be the same as the potential at $C$. This in turn must be the same as the potential of the cell itself. If the cell potential is known, the balance point shows where the corresponding potential occurs on the track. The cell can be used to indicate a "calibration point" on the track. If the whole track can be calibrated, then balancing the circuit against an unknown cell enables the cell potential to be measured.
It is from its use in true potential. measuring circuits that the 'volume control' type of variable resistance has acquired the name potentiometer, though in its ordinary applications it is used merely as a voltage-adjusting device not a measuring device.

## BRIDGES

In Fig. 2.1c, the circuit can be balanced so that there is no potential difference (p.d.) between $C$ and $D$. This arrangement is a "balanced bridge" and it has many uses, mostly in measurements.
In these examples the pot is used to adjust a steady voltage from a battery, that is a d.c. voltage. (d.c. really means direct current but engineers use it of voltages as well.) In its familiar role as a volume control the voltages are a.c. (alternating current) voltages, continually changing at audio frequencies.

## CAPACITANCE AND CAPACITORS

At this point we'll look at our second circuit element, capacitance. Capacitance is the power to hold an electric charge and the component that provides it is a capacitor. Capacitors used to be called condensers, from their ability to concentrate a lot of charge in a small space.

A capacitor is an electrical sandwich. The bread layers are sheets of metal. The filling is insulating


Fig. 2.1. Circuits for illustrating the operation and use of a potentiometer.
material, called in this case the dielectric. The sheets of metal are called the plates. Since in most capacitors their job is merely to conduct a charge to the dielectric they can be very thin.

In many capacitors the plates are just coatings of metal on the dielectric, which is often paper or plastic film.

The bigger the area of the plates the greater the chargeholding ability. More surprisingly, the capacity to store a charge also increases as the dielectric is made thinner. This is fortunate because it enables a lot of capacitance to be packed into a small space by using a very thin dielectric.

A favourite method nowadays is to take a long thin strip of plastic foil, coat the two sides with aluminium to form the plates, then roll it up and encapsulate it in wax or more plastic. Two lead out wires are provided, one in contact with each plate.

## WORKING VOLTAGE

A snag about thin dielectrics is that they won't stand very high voltages. So every capacitor has a rated working voltage. You must not apply more than the rated voltage or the capacitor may be destroyed.

The unit of capacitance is the farad (after Michael Faraday the great Victorian physicist). For most practical purposes a farad (F) is far too large. The values you come across will be millionths of farads (microfarads, written $\mu \mathrm{F}$ ), or thousandmillionths (nanofarads, written nF ), or million-millionths (picofarads, written pF ).

Very compact capacitors can be made by immersing plates of aluminium foil in a chemical solution and applying a voltage. This causes a very thin layer of oxide to form on the aluminium. Aluminium oxide is an insulator and acts as the dielectric. Low-voltage capacitors can be made
with very large capacitances. If you ever come across a $1 F$ capacitor it will be of this type, which is called an electrolytic capacitor or just an electrolytic.

Unfortunately, if the voltage is removed the aluminium oxide slowly dissolves again so an electrolytic deteriorates when kept on the shelf. (If tantalum is used instead of aluminium the effect is not nearly so bad.) The oxide film dissolves very quiokly if the voltage is reversed, positive to negative. The result can be quite explosive! So you must always observe polarity when using an electrolytic and connect it only in circuits where there is always a d.c. voltage across it, positive to positive plate, negative to negative plate.

For use in circuits where the d.c. is liable to reverse there are special reversible electrolytics which are really two normal ones connected back to back.

## CHARGING AND DISCHARGING

For our experiments we'll start with an electrolytic of $1000 \mu \mathrm{~F}$. Since our battery is 6 V any working voltage above will do. (The one used by the author was rated at 16 V .)

First, charge the capacitor through your l.e.d. indicator (Fig. 2.2a). A brief flash of the red lamp is all you should see, unless the $1000 \mu \mathrm{~F}$ has been on the shelf a long time and has "de-formed" and gone leaky. In that case the red l.e.d. will stay on


The capacitor being charged up In circuit of Fig. 2.2.


The capacitor is discharged by connecting the Indicator +ve lead to the battery -ve terminal. The green l.e.d. flashes.
until the oxide has "re-formed." (If it stays on more than a minute either the capacitor is beyond salvation or the polarity is wrong. In the latter


Fig. 2.2. Capacitor charging and discharging: (a) shows the circuit diagram for this experiment and (b) the way in which the components are interconnected on the screw terminal block. The L.E.D. Indicator was constructed and explained last month.

case reverse the battery connections and try again. The indicator will protect against excess current and there is no danger of an explosion.)

Having charged the $1000 \mu \mathrm{~F}$, discharge it as shown by the dotted line. The green l.e.d. flashes as the current rushes out. Try repeating the experiment a few times. Evidently, from the briefness of the flashes, charging and discharging occur very rapidly.
The process can be slowed down by putting resistance in the path of the current (Fig. 2.3). Connecting Rl ( $=1 \mathrm{k} \Omega$ ) as shown increases the time to about a second. The l.e.d.s don't


Charging the capacitor through a resistor as in Fig. 2.3.


Discharging the capacitor in Fig. 2.3. There will be no green flash.
glow so brightly now because the current is restricted to a few milliamps. Increasing the resistance slows down the process even more by allowing even smaller charging and discharging currents to flow. With a resistance (R1) of $100 \mathrm{k} \Omega$, the times are about 100 seconds, but the current is so small that the l.e.d.s can't be seen to glow.

## TRANSISTORS

How can you tell that the capacitor is really charging when Rl is $100 \mathrm{k} \Omega$ ? You could charge it for a few minutes then dischange it through the Indicator directly, getting a flash. But we'll call in the aid of another device, the transistor.

A small current can turn on a transistor and make it pass a larger current, which can easily be 100 times greater. We can turn on the transistor with the small charging current and the Indicator with the larger, amplified current.
Connect up the cirouit as shown (Fig. 2.4) but without the $1,000 \mu \mathrm{~F}$ ( Cl ). The small ourrent through the $100 \mathrm{k} \Omega$ resistor R1 now passes from base ( $b$ ) to emitter ( $e$ ) and turns on the transistor. The amplified current passes through the collector (c) to the emitter. Both currents pass through the Indicator on their way to battery negative. The red l.e.d. lights.

Make sure Cl is discharged, then connect it as shown. The l.e.d. goes out, because the small current through R1 is now mostly diverted into the capacitor and not enough


With the free wire not touching any other component, the capacitor will charge up and switch on the Indicator, see Fig. 2.4.


Shorting the capacitor with the wire as shown will discharge the capacitor and the Indicator will turn off (see Fig. 2.4.).
gets to the base to turn on the transistor. However, after about half a minute when Cl has charged to about 2 V the transistor begins to

(a)

Fig. 2.3. Charging and discharging a capacitor through a resistor: (a) the circuit diagram (b) the method of connecting the components on the screw terminal block.


come on again and the red l.e.d. glows. If you short Cl as shown dotted, (Fig. 2.4a) the circuit is "reset" and a further half-minute blacking out of the red l.e.d. occurs. (The green l.e.d. does not glow when Cl is discharged because the current all goes through the dotted connection.)

## RC TIMING CIRCUITS

Clearly, we have here the basis of a system for producing fixed time delays. RC timing circuits, that is, circuits in which a capacitance $C$ is charged or discharged via a resistance $R$, are widely used.
The duration of the delay depends partly on the particular circuit (in our circuit, you'11 have noticed, we get only about 30 seconds instead of the 100 seconds mentioned earlier). But
the periods are always related to what is called the RC time constant. This is $R$ in ohms times $C$ in farads and the answer is in seconds. So 1 ohm and 1 farad give 1 second.
Increasing the 1 ohm to 1 million ohms (1 megohm, 1M) enables $C$ to be reduced a millionfold for the same result. So $1 \mathrm{M} \Omega$ and $1 \mu \mathrm{~F}$ also give 1 second.
Most of the timed periods in electronics are much less than a second because electronics often deals with



Fig. 2.4. (a) Shows the circuit diagram of a simple transistor current amplifier. The input to the amplifier is from the positlve plate of a charging capacitor, C 1 ; (b) how to connect the components on the terminal strip.


## RELAXATION TIME

Let's do it. We'll build an amplifier and turn it into an oscillator whose frequency is set by an RC circuit.

This time we use two transistors (Fig. 2.5). The second one amplifies the output of the first. So if each amplifies by 100 the overall amplification is $100 \times 100$.

Amplification is often called "gain". There are three kinds: voltage gain,
The circuit of Fig. 2.5 wired up on the terminal strip. The earphone has not yet been connected as you will be observing long time delays.

current gain, and power gain. In any one circuit they are usually all different.

With $\mathrm{Cl}=1000 \mu \mathrm{~F}$ as shown the red l.e.d. lights for a few minutes then goes out for a while then comes on again and so on for ever, or until the battery runs down. By changing Cl to $100 \mu \mathrm{~F}$ the process is speeded up about ten times. Much smaller capacitances such as $100 \mathrm{nF}(0 \cdot 1 \mu \mathrm{~F})$ or 10 nF $(0.01 \mu \mathrm{~F})$ increase the frequency so muoh that the flicker of the l.e.d. is too fast to be seen. To prove that the circuit is still-oscillating, connect a crystal earphone across the indicator as shown and listen to the oscillation. (If the l.e.d. goes out or dims when you connect the phone then it's not a crystal phone but a magnetic type. Get a real crystal earphone: they can be obatined for around 50 pence.)

Crystal earphones are usually supplied with a small moulded-on jack plug. Cut it off, leaving an inch or two of wire so you can use it again. Bare the ends of the phone leads and
connect to a piece of terminal block with two terminals. You can then connect your phone with ordinary thick wires instead of the awkward thin ones of its own cable.

This sort of oscillator is sometimes called a relaxation oscillator. What the transistors do, in effect, is turn the battery on and off regularly at the oscillating frequency. The resulting pulsating current is a mixture of a.c. and d.c. You know that the dielectric of a capacitor blocks d.c. beoause it is an insulator. You can now prove that it lets a.c. through.

To do so, all you need do is unhitch one phone lead and then remake the connection via a capacitor as shown dotted. (Any value from about 10 nF upwards will do.) The ascillation still comes through at full strength.

Used in this way, a capacitor acts as a simple filter to separate a.c. from d.c. In this role it appears in many amplifier circuits.

To be continued


0NE of the most frequent and popular projects to appear in electronic magazines is the humble metronome, and readers may well buckle at the knees to see yet another . . . but be patient and think back. How many digital metronomes have you seen? Haven't they all required time consuming calibration of the main dial; haven't they been battery-powered so that sooner or later the frequency stability is suspect?

Being a music teacher by profession the author is well aware of these drawbacks, and this design aims to overcome these disadvantages. It takes about two minutes to calibrate, is always aocurate, and provides a direct readout of the pulses per minute, updated automatically to register changes in tempo, caused by altering the main dial.

It is mains powered, which means there are no batteries to run down, and stability can be relied upon. The display can be blanked at any time without affecting the counting or the accuracy, and a flashing l.e.d. is incorporated as an alternative to the bleeper. It is inexpensive to build, and thus rivals many commercial units in terms of value for money.

## HOW IT WORKS

The main problem when designing this project was how to read pulses per minute. To wait this length of time before the display registered
was obviously of no value, and the answer lay in speeding up the oscillator by a factor of 60 so that the counter read pulses per second. Altering the resistance of VR2 and 3 increases or decreases the frequency of the oscillator which is displayed by the digital counter. This frequency is then divided by 60 to give pulses per minute, which can either drive an 1.e.d. or a gated audio oscillator, energising the transducer, WD1.

## COUNTER

The full circuit diagram is shown in Fig. 1 over page.
The main counter i.c. is the 4553 , a very useful смоs device that provides binary coded decimal output, capable of controlling three 7 -segment displays. This i.c. needs to be reset to zero before a count is made, and to do this a positive pulse is required at pin 13 (IC3). The gating for these functions is provided by IC2. When pin 12 (IC2) goes high, pin 13 (IC3) also goes high, resetting the counter.

On the return of pin 12 to a low state, the monostable formed by gates IC2c and d is triggered, and pin 10 will go low for a time determined by Cl and VR1. This low is inverted by IC2b to provide a high at pin 2, IC2a. When this occurs, any pulses on pin 1 (which is connected to the main oscillator) will be fed through to pin 3 and thus to the input of IC3 pin 12.
At the end of the monostable period, pin 10 goes high, pins 2 and

4 go low, preventing any more pulses reaching the counter. If this monostable period lasts for one second, adjusted by VR1, the counter will thus register how many oscillations occur in one second. This is the required count period.

## COMPONENTS

Resistors

| Resistors |  |  |
| :--- | :--- | :--- |
| $R 1$ | $10 \mathrm{M} \Omega$ |  |
| $R 2,4,11$ | $100 \mathrm{k} \Omega$ (3 off) |  |
| $R$ | $4 \cdot 7 \mathrm{M} \Omega$ | See |
| $R 3$ | 4 |  |
| $R 5,6,7$ | $100 \Omega$ (3 off) |  |
| $R 8$ | $1 \mathrm{k} \Omega$ |  |
| R9 | $270 \Omega$ |  |
| R10 | $680 \mathrm{k} \Omega$ |  |
| All $\frac{1}{4} \mathrm{~W}$ | carbon $\pm 5 \%$ | page 722 |

Capacitors

| C1 | $0.47 \mu \mathrm{~F}$ polyester |
| :--- | :--- |
| C 2 | $2 \cdot 2 \mu \mathrm{~F} 16 \mathrm{~V}$ tantalum bead |
| $\mathrm{C} 3,6$ | $1,000 \mathrm{~F}$ ceramic (2 off) |
| C 4 | $0.01 \mu \mathrm{~F}$ polyester |
| C 5 | $0.1 \mu \mathrm{~F}$ polyester |
| C7 | $100 \mu \mathrm{~F} 25 \mathrm{~V}$ elect. |
| C8 | $10 \mu \mathrm{~F} 25 \mathrm{~V}$ elect, radial lead |
| C 9 | $0.22 \mu \mathrm{~F}$ polyester |
| C 10 | $470 \mu \mathrm{~F} 25 \mathrm{~V}$ elect, radial |
|  | lead |

Semiconductors
D1 1N4148 silicon
D2 Tll.220 red I.e.d.
D3,4 1N4001 silicon (2 off)
IC1,2 4011B CMOS quad 2 input NAND gate (2 off)
IC3 4553B CMOS 3 digit multiplexed b.c.d. counter
IC4 4511B CMOS b.c.d. to 7-segment decoder/ driver
IC5 555 timer
IC6,7 4017B CMOS deciade counter (2 off)
IC8 $781212 \mathrm{~V}, 1 \mathrm{~A}$ regulator
$\mathrm{XI}-3 \quad$ DL704 0.3 in . high 7 -segment. common cathode display (3 off)

Miscellaneous
S1 s.p.s.t. miniature toggle switch
S2 d.p.d.t. miniature toggle switch with centre off
WD1 PB2720 transducer
T1 Miniature mains transformer, $15-0-15 \mathrm{~V}, 100 \mathrm{~mA}$ secondary
VR1,2 $1 \mathrm{M} \Omega$ miniature horizontal preset (2 off)
VR3 $1 \mathrm{M} \Omega$ linear potentiometer
LP1 Neon mains indicator with integral resistor
FS1 $500 \mathrm{~mA}, 20 \mathrm{~mm}$ long fuse Single sided p.c.b. $135 \times 72 \mathrm{~mm}$ and $65 \times 30 \mathrm{~mm}$; Verocase type 202-21041C; 20 mm fuse holder; red acetate display filter $60 \times$ 30 mm ; l.e.d. mounting clip; $7 /$ 0.2 mm stranded connecting wire: mains lead; grommet; cable clamp; control knob.

The high pulses at pin 12 of IC2 are provided by a pulse oscillator anound IClC and d , with its output conneated to pin 12. This gives a very brief high pulse approximately every $2-3$ seconds to automatically restart the counting sequence described above.
This oscillator is in fact a square wave generator with the duty cycle affected by the inclusion of D1, which makes the output low tume decided by the 4.7 megohm resistor, and the high time by this resistor in parallel with the 100 kilohm; thus it will be high for a very short time, compared to the low state.
The binary coded decimal output from IC3 is fed into the decoder/ driver, IC4, the outputs of which are connected to all three displays at once. However the cathode pins of the displays are fed back to IC3 which only switches on one display at a time. This switching speed is con-


Fig. 2. Power supply for the Digital Metronome.
trolled by the capacitor between pins 3 and 4, and is so flast that the eye records a stable display. This technique is called multiplexing and saves interconneoting wires and power consumption.

## DIVIDER

The output from the main oscillator also goes to the CLOCk pin of IC6, a cmos decade counter whose ten outputs go high in turn on each incoming pulse. It divides the frequency by

Fig. 1. Circuit diagram of the Digital Metronome.

ten, and its output is connected to the input of another counter, this time connected as a divide-by-six counter. Thus pin 7 of IC7 goes high very briefly once for every 60 pulses coming in to IC6.

The output pulses go either to illuminate the l.e.d. D2, current-limited by R9, or to the gated audio ICla and $b$ driving WD1, the piezo transducer. With the values given, the frequency should be approx 1 KHz , but construotors could well experiment with the values of R10 to find the resonant frequency of the particular transducer, when it is at its loudest.

POWER SUPPLY
Power supply circuit diagram is shown in Fig. 2.
The power supply consists of a 15 -$0-15 \mathrm{~V}$ transformer ( 100 mA ) rectified by D3 and D4, smoathed by C10, and regulated by a 12 V regulator, IC8. This requires deooupling capacitors C8 and C9 for correct operation. C7

was also included because with so many pulses occuring, the counter may be affected, but it can be omitted if it proves to be unnecessary.


## CIRCUIT BOARDS

All the components, including the transformer Tl , are mounted on the main p.c.b, a design for which is given in Fig. 3. The three digit display was also mounted on a p.c.b, artwork shown in Fig. 4, as to mount the displays on stripboard requires a great number of link wires. An aperture was out in the front aluminium panel of the verocase, and the display mounted behind it using epoxy resin, to avoid bolts showing on the front panel. A small piece of red acetate was included to improve contrast.

A double - pole, double - throw, centre-off switch was used, not only to switoh the supply from the regulator to the boand, but also to change from flashing l.e.d. to the transducer. This was mounted on one side of the
display, with the l.e.d. on the other side. Check the terminals on the l.e.d. before connecting it.

The potentiometer VR3, neon and display enable switch were mounted on the top panel, as was the transducer using two screws. Be careful with the leads of the device as they are quite fragile and can easily be pulled out.

If, like the author, constructors have struggled getting a neat pattern of holes for loudspeaker grilles, the following suggestion may help. The author used a rub-down transfer sheet of pads, intended for p.c.b. design and available from most electronic shops. This is laid on the panel, and a compass point pushed through the centre of the pads to give the desired pattern. The pads themselves aren't used and so are not wasted. Many different designs can be made this way, with a guarantee of regularity. The compass points then provide the starting point for the drill bit

The mains lead is fed through a hole in the back panel, with a rubber grommet to protect the sheathing. Another hole must be drilled for the fuseholder.

## GENERAL ASSEMBLY

The main p.c.b. is bolted to the bottom of the case, and connections made using lengths of insulated wire and Venopins. After checks have been

Prototype unit showing the wiring to case mounted components.


$O=$ VEROPIN


Fig. 4. Full size p.c.b. artwork and component layout for the display section.


Fig. 3. Full size artwork of the main printed circuit board and component layout. Note that IC4 is inserted the opposite way round to all other i.c.s.


Fig. 5. Interwiring diagram. Allow enough wire to position components in the case.
made the unit is switched on and allowed to settle down (a couple of seconds for the capacitors to get fully charged; this should also be remembered when altering the dial).
Time a period of one minute and count the number of pulses, then adjust VR1 until the display shows this number. VR2 is then adjusted to give the maximum number of pulses, 120 on the prototype. This also affects the minimum count, being about 40 . It would not be wise to go much lower than this figure, because the audio puises beoome too long to be accurate (see end of article).

Construlatons may like to add some numbers around the dial, but this was not thought to be necessary on the prototype. Using a 555 as the
main oscillator gives an almost linear frequency response so it is relatively easy to know the approximate position for a desired number, and the constantly updating display enables a highly accurate setting to be made. Because there will be some variation as to how the monostable period and
the main oscillator pulses coincide, the number may fluotuate by a digit either way.

R5, 6,7 were selected to give a bright display in the prototype, but depending on the efficiency of the displays used these values could be increased or decreased, but do not omit them altogether.

Pin 5 of IC4 is conneoted to pin 2 of IC2 to give a reading of zeros between each reading, but if readers would prefer to see the counter actually counting, simply remove this connection, and connect pin 5 to earth via a 10 kilohm resistor.

## CONCLUSION

As the lower limit of approximately 40 is reached, the audio pulses start to become long, and more unlike the short bleeps expected from a metronome. For those constructors who would like a lower limit, the circuit shown in Fig. 6 can be added enabling a much lower frequency limit, using just one more i.c. a 4001 quad Nor gate.

Two gates are connected together as a monostable, requiring a high input signal at pin 1 to give a high pulse at pin 4 , whose duration is largely independent of the length of the input signal. The use of this i.c. also has an advantage in that two gates are still unused and can be placed after the gated audio oscillator to drive the piezo transducer in a bridge circuit to greatly increase the output power. It can be constructed on a small piece of stripboard and mounted above the p.c.b, powered from the same supply.


Fig. 6. Additional monostable circuit for use at lower frequencies.


Main p.c.b. assembly. Note the use of ribbon cable for the display wiring.


## THE EIECTRONICS Of <br> 1IWFRRMATON TEEHNOLOGY PART OIE

THE Government's "Information Technology Year", announced in the January issue of Everyday Electronics, will soon be coming to its end. Has it all been just a publicity campaign-though with very legitimate commercial purposes? Or is Information Technology (IT) a genuinely separate engineering activity which we shall continue to recognise as such long after 1982 is over?
The editor was right to comment in that same issue that IT could be seen as something "newly labelled" rather than newly created. According to a report on the subject published in 1980 by the Government's Advisory Council for Applied Research and Development (ACARD), information technology "combines the technologies of computing and telecommunications" and "will perhaps be the most important area of application of microelectronics."
The report then goes on to list several examples of the hardware encompassed by this definition of IT, such as electronic telephone exchanges, word processors, electronic mail, video discs and viewdata systems.

## THIS IS THE QUESTION

This is a pretty big field! Is there really something essential which holds all these disparate elements together, to maintain a unity like that of an atom or a biological organism?
Whether the answer is yes or no, one would be quite right to point out that each of the separate products and systems will continue to go on its own sweet way, impelled largely by the commercial demand for this hardware. Engineers. will continue to develop and manufacture electronic telephone exchanges and the public will continue to use them quite regardless of what is happening in video discs or word processors.

## SHARING OF TECHNIQUES

However, we are all aware that there is a great deal of sharing of techniques between the different products and systems. One has only to think of how the electronic telephone exchange uses digital computing techniques that were originally developed for scientific, military and commercial applications.
The purpose of this series of articles, then, is to explore this

Plessey PDF Digital Facsimile Machine. In the transmitting and receiving system of this facsimile equipment both light intensity and position in space are represented by electrical variables.

commonality of technique-to see if we can discover the message that runs through all of it, like the words running through a stick of rock.
Some electronics specialists would no doubt say: "well, its just electronics, nothing more." And they would be almost right. For if you look at the way electronic components, circuits and systems are actually applied to the world at large you find that their main function is to handle information - sensing, measuring, transmitting, processing, transforming, controlling, recording, displaying and so on.

## NEW VIEW OF ELECTRONICS

But, nowadays, how electronic is electronics? The experimenter or engineer who puts together a collection of integrated circuits to do a certain job is scarcely interested in their internal functioning: he thinks in terms of their external functions and of the signals, logic states and information flows by which they are inter-related.
The arrival on the scene of specialists such as computer scientists and software engineers, needing to know little about electronic circuit design and probably caring even less, is altering our world view of electronics profoundly.

However, the readers of this magazine are by definition interested in electronics-whatever that is!-so we shall attempt to look at IT not from the point of view of the computer scientist or software engineer but from that of the electronics experimenter, technician or engineer.

## ELECTRICAL QUANTITIES

The core of the approach is that what we call information is nothing to do with its semantic content (for example sos sent by Morse code) but the symbols which represent it. These symbols are formed from electrical quantities. They may be static, as in a semiconductor memory, or dynamic, as in a signal.

Upon these electrical quantities representing information various operations are performed-coding, amplification, attenuation, comparison, integration and so on. The concepts of most of these operations will be already understood by the reader,

at least intuitively if not in precise detail, so our approach will be to emphasise this operational aspect: what the electronic devices and sys tems do rather than what they are.

## TWO-STATE LOGIC OPERATIONS

If there is indeed a common core to the multiplicity of IT systems it is probably the use of electronic devices to represent information by two-state or binary symbols. In electronic logic systems-from the simplest industrial controllers to advanced storedprogram computers-the two states represent the "true" and "false" conditions of deductive logic.
In telecommunications the two states appear in binary codes-for example in pulse code modulation transmission systems-which may represent voice, music, vision, numerical data or any other type of information. Where two-state electronic logic operations result in the production of binary codes, the electronics of computing, control and communications menge into each other.
But any non-technical person who tries to get some idea of what information technology is about simply by examining pieces of its electronic hardware will end by being completely baffled and demoralised. The construction of the electronics, typically a printed-circuit board, gives hardly any clue to the function. It is better to forget the hardware, at least to begin with, and concentrate on the functions it performs.

## BLOCK DIAGRAMS

This approach does, of coūrse, correspond with the mental processes of electronics design engineers, who, before they get down to detail, think of the systems they are designing as groups of functions and draw block diagrams of them. Two well known examples are shown here, Fig. 1.1 and

Fig. 1.2-a monochrome television receiver, and a microcomputer. Each block is labelled with the function it performs, while the lines connecting the blocks represent paths for information and show sketchily how the functions are acting on each other by means of this information.

## REPRESENTING INFORMATION ELECTRICALLY

Information from the outside world goes into IT equipment as changes of physical quantities in space and time.
current), its position (for example at a particular conductor or storage element among several) and the time at which it occurs.

## ELECTRICAL STEP

Consider, for example, the electrical step shown in Fig. 1.3, which could be part of a sequence like a pulse train. Both the magnitude of the step (a) and the instant in time at which this step occurs (b) are variable. Either of these variations can represent a physical quantity


These changes might be in air pressure, light intensity, mechanical force, temperature, magnetic flux, velocity, flow rate, humidity or indeed any quantity that can be detected by an instrument.
All such changes are events occurring in time, but with some information the position of an event in space is the most significant thing about it-for example, the position of a selected key on a keyboard, the position of a part in an electronically controlled machine, the position of an element in an optically scanned pattern.
To represent these events in space and time by electrical symbols we can make use of three aspects of an electrical quantity: its magnitude (for example as charge, potential, or


Fig. 1.3. Two properties of an electrical step that can represent events in the outside world: (a) variation of its magnitude; (b) variation of the time at which it occurs.
coming from the outside world. The particular conductor, say, on which (a) or (b) is occurring can represent a particular position in space.
Let us take a concrete example to illustrate these general points. Fig. 1.4 shows a simple system for counting objects moving along a conveyor in a factory. A light source produces a narrow beam of light which passes across the conveyor at such a level that the moving objects interrupt the beam. The light falls on a photodetector which converts its energy into electrical energy. Electric current from the photodetector passes into an electronic counter, which counts the interruptions of the light beam.

Fig. 1.5 shows in more detail what is taking place. At (a) is a graph of light energy reaching the photodetector over an interval of time. It will be seen that this energy falls to a very low value, practically zero, when the beam is interrupted by an object. (The small residual energy is the result of ambient light "leaking" into the detector.)
At (b) is a corresponding graph of the magnitude of the electrical


Fig. 1.4. In this simple industrial counting system information in electrical form is obtained about the interruptions of the light beam by objects on the conveyor.


Fig. 1.5. Graphs showing events in the Fig. 1.4 counting system: (a) falls in light energy at the photodetector as objects interrupt the light beam; (b) corresponding falls in electrical energy from the photodetector; (c) inversion of (b) to show it more clearly as a sequence of pulses.
energy from the photodetector resulting from the incident light energy. It has the form of a sequence of falls in electrical magnitude, corresponding to interruptions of the light beam.

These events can be distinguished more clearly if we invert the graph as shown at (f). They then appear as a sequence of pulses.

## STEP CHANGES

Within the electronic system the incoming information from the outside world-the passage of objects is represented by the variation of the electrical magnitude shown in Fig. 1.5(c). These pulses, which are subsequently counted, illustrate the two general principles shown in Fig. 1.3. Variations of the electrical magnitude represent the strong-light/weak-light conditions at the photodetector while the oncurrences of the pulses in time represent the presence of objects at the detection point.

## CONTINUOUS VARIATION

Fig. 1.5(c) is, of course, an example of an electrical signal-a sequence of


Fig. 1.6. Output of a microphone responding to sound waves-a signal with a continuous variation of electrical energy as distinct from the pulse signal in Fig.1.5.


Fig. 1.7. Analogies for (a) static or stored information and (b) dynamic or conveyed information. in (a) the amount of material held represents some physical variable, while in (b) the flow rate of the material represents it.
events representing and conveying information. Another type of electrical signal is shown in Fig. 1.6, actually the output of a microphone responding to sound waves. The electrical magnitude here, of course, is proportional to the air pressure variations in the sound waves impinging on the microphone.

Although this graph or waveform shows a continuous variation of the electrical magnitude in contrast to the step changes in the pulses of Fig. $1.5(\mathrm{c})$, it can still be regarded as portraying a sequence of events because it consists of successive values of the electrical quantity-successive values that are infinitely close together.

In both examples the successive values are electrical symbols for the corresponding events in the outside world. As such they can be manipulated and processed in various ways. For example, they can be assigned numerical values by encoding techniques, as we shall see later.

## STORED OR CONVEYED

In saying above that a signal represents and conveys information we make the point that it does both of these things. In certain IT processes, however, we may wish only to represent information without simultaneously conveying it-in other words, hold the electrical symbols at some place. The two functions are illustrated by analogy in Fig. 1.7.

At (a) the amount of material held in the container (which can be controlled by the inflow and outflow) may be used to represent some physical variable from the outside world. This is static information and the process of holding it is storage. At (b) the material is moving along a pipeand this could be the inflow or outflow pipe in (a). Here we could use the flow rate of the material to represent the same physical variable.

So here we have two further ways of representing a given physical variable. In one case the information is static (stored), while in the other case it is dynamic (conveyed).

## ELECTRIC CHARGE AND CURRENT

In the electronics of IT systems we can of course use electrons as the "material" in Fig. 1.7. In the static information case (a) these appear as a stored electric charge, while in the dynamic information case (b) the charge is moving and therefore appears as an electric current.

These are just two of the electrical quantities that can be used to represent information, but of course there are others such as voltage and power. And, as everyone with a tape recorder now knows, magnetism provides another way of storing information.

To be continued


Mullard Ltd-the largest electronic components company in the UK-and Everyday Electronics join forces to present this rewarding challenge to Secondary Schools. .

## DESIGN A PIECE OF ELECTRONIC EQUIPMENT HAVING A DIRECT PRACTICAL APPLICATION IN A SCHOOL'S SCIENCE LABORATORY

This competition is open to any United Kingdom Secondary School, State or Independent. Pupils of either sex in the age group 11-18 are eligible to patticipate in a team representing their school.
The competition will be conducted in two stages.

## STAGE 1

Submission of Papers describing the proposed project with full circuit details.
Papers will be judged for novelty, ingenuity and viability. Particular attention will be given to originality and good circuit design technique.
Schools whose designs are adjudged to be the most promising will be asked to produce a working model.

## STAGE 2

Models will be examined and prize winners selected on the basis of mechnical design, neatness of wiring and general assembly, plus operational performance.

All models will be exhibited at Mullard House, London, where the official presentation of prizes will be made at the end of June 1983. Representatives of fnalists will be invited to stay overnight in London as guests of the SEDAC sponsors.

## FIRST PRIZE The SEDAC Trophy and $£ 300^{*}$

## SECOND PRIZE £200* THIRD PRIZE £100*

* Plus a selection of components valued at $£ 200$ NINE RUNWERS UP a selection of components valued at $£ 100$

In addition, all twelve finalists will receive a certificate and one year's subscription to Everyday Electronics

Science teachers of Secondary Schools are invited to apply for a Registration Form which contains full details of this competition.

Write to: Schools Competition, Room 2130, Kings Reach Tower, Stamford Street, London SE1 9LS.

Secondary School Pupils make sure your school accepts this challenge and enters this contest. So bring this announcement to the attention of your science teacher or the head of your school.

Closing date for Registration:
November 151982

Closing date for submission of Papers:

January 311983


## By Dave Barrington

## Component Buying

Last month Paul Young gave some excellent advice on component buying and ordering.-See Counter Intelligence page.

The only addition we would make to his advice would be to look out for the "special bargain packs," sometimes Free (on certain orders), that are regularly offered by such advertisers as Bi-Pak, Rapid, Magenta and Chordgate. It also pays to browse through the Classified section. Here you will find such items as multi-packs of wires and service sheets on offer.

## Bargain Time at The Fair

Talking of special bargains. We hear that exhibitors at the first Electronic Новвеs Fair will be offering special, once only, bargain prices on certain components throughout the run of the fair.
So, if you are still hesitant about visiting us on our stand why not come along and pick up some bargains from the fair?

The Electronic Hobbies Fair is being held from November 18 to 21 at the Alexandra Pavilion, Alexandra Palace, London. Opening times are 10.00 to 18.00 hours for the first three days and 10.00 to 17.00 hours on the final day.

## Light Alternative

Earlier this year Anglia Components were one of the first component distribu tors to introduce blue l.e.d.s, now they are offering an alternative to the ubiquitous ORP12 light dependent resistor (I.d.r.).
Many variations of Cadmium Sulphide cells are available either in encapsulated or chip form, to special order.
One of the main rivals to RS, Anglia specialise in TV components and professional bodies and component traders will be most impressed with their components catalogue. This catalogue contains over 100 -pages and covers such items as TV replacement parts to semiconductor and i.c. devices.
Further information can be obtained from Anglia Components, Dep EE, Burdett Road, Wisbech, Cambs, PE13 2PS.

## Catalogues Received

With few exceptions, asking a musician to design and build a loudspeaker enclosure is like asking the housewife to repair the washing machine. They are both highly skilled and know whether the results are right, but the technical process that goes into creating the final result is a mystery to them.

With this concept in mind, Celestion International have recently published a Cabinet Handbook of system designs for the professional musiclan. This handbook will certainly appeal to the constructor, particularly the hi fi enthusiasts.

The 28 -page hand book contains detailed design drawings, construction details and performance guide for more than a dozen cabinet styles. Included are several bass housings, guitar or bass guitar cabinet and tweeter and mid-range boxes.

Specialised units such as a tweeter wedge monitor that is used for 'foldback", enabling musicians behind the main PA stacks to hear all instruments and vocals clearly, are also included.

The Celestion Cabinet Handbook costs $£ 1$ plus postage and is avallable from Celestion International, Dept EE, Ditton Works, Foxhall Road, Ipswich, IP3 8JP.

All products in the new 84 -page Eagle Catalogue are guaranteed for two years. The catalogue is divided into six sections and includes colour photographs and brief technical specifications of each item.
Equipment listed includes intercoms and door phones, public address and paging systems, microphones and mixers, test equipment and power supplies, security units and audio equipment. The audio section lists portable units, hi fi and in-car entertainment accessories and sets.
Copies of the Eagle 1982/3 Catalogue and local stockists can be obtained from Semiconductor Supplies International Ltd., Dept EE, Dawson House, 128-130 Carshalton Road, Sutton Surrey, SM1 4RS.

The latest edition of the 1982/3 Babani Books technical books catalogue is now available. Books listed cover such subjects as; CMOS projects, using calculators, programming and long distance television reception (TV-DX).
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## CONSTRUCTIONAL PROJECTS

## Combination Lock

The dual-in-line relay used in the Combination Lock is a 500 ohm coil type with an internal protection diode. This device is available from any RS Components approved stockists and should be ordered as: s.p. form A, stock no. 349-383.
The device (purchased from another source) used in our prototype was supplied without the full complement of pins and the p.c.b. was designed around this particular relay. However, there is ample room on the circuit board for drilling the extra holes for the RS device. Alternatively, although not advisable if the relay is to be used for another project later, the unused pins can be bent back so that they "sit" on the topside of the circuit board.

When purchasing a relay make sure it is a device with an internal protection diode. If a diode is not included then one will have to be wired across the coil. The anode lead to TR3 collector and the cathode to the positive supply line.

The 9 -way printed circuit board interconnecting plug and socket assembly will have to cut from a 10 -way version. These connectors are available from Amblt, Maplin, Watford, Magenta and any RS component supplier.

## Digital Metronome

The 10 megohm resistor R1 called for in the Digital Metronome may prove troublesome to locate, but the $\frac{1}{2} \mathrm{~W}$ rating type seems to be fairly common and can be used here.

The physical size of the transformer T1 will obviously vary according to type used and may necessitate the use of a larger case. The piezoelectric transducer is now stocked by most advertisers.

## Oscilloscope Companion

Most of the close tolerance resistors for the Oscilloscope Companion are readily available items from the E24 series. However, the 500 ohm type may cause troubles. It is suggested that readers follow the guide lines outlined in the article regarding the preferred value resistors.
The BZX61 Zener diodes D9 and D10 are stocked by Rapid Electronics. It would appear that most component stockists will be able to supply a $6.2 \mathrm{~V} \quad 1.3 \mathrm{~W}$ equivalent for these diodes.
The 9-way rotary switches S3(103) and S4(104) will have to be made up from types with adjustable limit stops as these are non-standard items. This also applies to the 6 -way types. Alternatively, 12 -way and 2-pole 6-way switches could, but not recommended, be used and all unecessary connecting tags wired together and fixed to the last switched or stop position.
The only source for the 10 A rotary switch $\mathrm{S} 3(103)$ that we have been able to locate are the high current types from RS Components. These are rated at 250 V a.c. $6 \mathrm{~A}, 30 \mathrm{~V}$ d.c. 10 A and have adjustable limit stops. The order code for this switch is RS 327-579-This switch is expensive.

## Photo Finish

The detector phototransistors TIL78 used in the Photo Finish are now general "off-the-shelf" items and should not present purchasing problems.
It is not necessary to stick strictly to the case depicted in the article. Another suitable type would be one of the small plastic "diecast" moulded case.

## Beat the Relay

The relay used on the prototype Beat the Relay game was a surplus GPO with a 125 ohm coil operatIng at 12 V . However, any relay with 12 V operation and a coil resistance of around 125 ohms with at least one set of normally open contacts will work here.

## ZX Tape Controller

The miniature electret microphone needed for the $Z X$ Tape Controller is available from most Tandy stores. This should be ordered as Microphone Element, stock number $270-092$ and is listed as costing £1-29.
The 25 mm loudspeaker is available from J. Bull and costs $£ 1 \cdot 75$, including VAT and postage/packing.


THe ascilloscope is one of the most useful pieces of laboratory equipment available and a desirable asset for any school. However, the price of even the simplest of 'scopes is high and therefore prohibitive for many secondary schools. It was for this reason that the author developed the Oscilloscope Companion.

This cheap addition to very basic or old oscilloscopes will enable them to measure very low currents and voltages as well as converting a single beam 'scope to a dual beam. version.

The Oscilloscope Companion offers voltage ranges, current ranges, a beam splitter, a peak voltage detector, a timebase delay, and a precision rectifier/buffer which allows an ordinary voltmeter to be connected for more accurate measurements.

## CIRCUIT DESCRIPTION

The overall circuit is complex and so may be divided into seven subcircuits, these being the current ranging, voltage ranging, peak voltage detector, timebase delay, beam
splitter, output precision rectifier/ buffer, and the power supply circuits. A detailed description of each is given.

CURRENT RANGING
The principle behind the current ranging is to measure the voltage across a resistor when a current is passing through it. The circuit diagram is given in Fig. 1. The resistors R1 to R27 are for this purpose and are switched into circuit by either S3, S4 or S5.

The values for resistors R1 to R9 have been calculated to give IV across the required resistor when it is switched into circuit and the current stated per division is input. For example, R1 has a value of 100 ohms and is switched into circuit when the $10 \mathrm{~mA} /$ division range is selected. Thus from Ohm's law, the voltage measured across the resistor with 10 mA flowing is:

$$
V=0.01 \mathrm{~A} \times 100 \Omega=1 \mathrm{~V}
$$

Resistors R10 to R27 work on the same principle except the voltage produced is 10 mV . This is too low for our needs and so a voltage amplifier with gain 100 is present in the form of ICl and its associated components.

ICl is a MOSFET operational amplifier with a very high input impedance so as not to affect the input. It has been configured as a non inverting amplifier with a gain of 100 , the gain being determined by the ratio of R29 to R28 which in this arrangement are $390 \mathrm{k} \Omega$ and $3 \cdot 9$ $k \Omega$. VRl allows the voltage offset of the i.c. to be nulled out, which is necessary if the accuracy is to be refined. VR2 and R30 compensate for any impedance mismatching problems

Finally, S2 is used to select one of the three ranges and S1 is used to select whether Current or voltage is to be measured.

## VOLTAGE RANGING

The voltage ranging has two ranges and these are selected via S6, see Fig. 2. If the high range is

VOLTAGE RANGES
Low Range $=1 \mathrm{mV}, 2 \mathrm{mV}, 5 \mathrm{mV}, 10 \mathrm{mV}, 20 \mathrm{mV}, 50 \mathrm{mV}, 100 \mathrm{mV}, 200 \mathrm{mV}, 500 \mathrm{mV}$ High Range $=1 \mathrm{~V}, 2 \mathrm{~V}, 5 \mathrm{~V}, 10 \mathrm{~V}, 20 \mathrm{~V}, 50 \mathrm{~V}$

INPUT IMPEDANCE
$10,000 \mathrm{M} \Omega$ on all Low Ranges $10 \mathrm{k} \Omega /$ volt on high range

## CURRENT RANGES

Low Ranges $=10 n \mathrm{~A}, 20 \mathrm{n}$ A, 50 nA , $100 \mathrm{n} \mathrm{A}, 200 \mathrm{nA}, 500 \mathrm{nA}, 1 \mu \mathrm{~A}, 2 \mu \mathrm{~A}, 5 \mu \mathrm{~A}$ Middle Ranges $=10 \mu \mathrm{~A}, 20 \mu \mathrm{~A}, 50 \mu \mathrm{~A}, 100 \mu \mathrm{~A}, 200 \mu \mathrm{~A}, 500 \mu \mathrm{~A}, 1 \mathrm{~mA}, 2 \mathrm{~mA}, 5 \mathrm{~mA}$ High Ranges $=10 \mathrm{~mA}, 20 \mathrm{~mA}, 50 \mathrm{~mA}, 100 \mathrm{~mA}, 200 \mathrm{~mA}, 500 \mathrm{~m} \mathrm{~A}, 1 \mathrm{~A}, 2 \mathrm{~A}, 5 \mathrm{~A}$
INPUT IMPEDANCE
Varies from $0.25 \Omega$ to $1 \mathrm{M} \Omega$ depending on range. Has no significant effect on the accuracy.

## ACCURACY

$2 \%$ on all ranges

## BAND WIDTH

5 MHz on all ranges with no loss in accuracy. 20 MHz maximum

## OUTPUT

Never exceeds range -4 V to +4 V . Will drive $500 \mathrm{k} \Omega$ without loss in accuracy
SYNC. OUTPUT
Varies from -4 V to +4 V . Will trigger most oscilloscopes


Fig. 1. (above) Circuit diagram of the left channel current range. Right channel identical except for all components prefixed with 100.


Fig. 4. Peak voltage detector circuit diagram. Left channel shown.
chosen, then the input voltage passes through one of the resistors R45 to R49 and through R44 to earth. The resistors R45 to R49 form five potential dividers with R44 and by selecting a different resistor, a different range is selected.
The basis of operation is that the output is tapped off at the junction of the two resistors forming the potential divider and that this output is always 1 V when the input voltage is equal to the $\mathrm{V} /$ division range selected. For example, if the $20 \mathrm{~V} /$ division range is chosen then R48 is switched into circuit.
The current which must flow through R44 for 1V to be developed is, from Ohm's law, $0 \cdot 0001 \mathrm{~A}$. Now if 1 V is dropped across R44, then $20-1=19 \mathrm{~V}$ must be dropped across R48 ( 20 because of a 20 V input). From Ohm's law we see that the value of R48 is:
$R=\frac{19}{0 \cdot 0001}=190000$ or 190 kilohms
This form of circuit is very accurate but does tend to impose a relatively high load $(100 \mu \mathrm{~A})$ on the input.


Fig. 3. Output produced by the peak voltage detector.

If the low range is chosen, the input passes to IC3. This is a MOSFET operational amplifier with an input impedance of $1.5 \times 10^{12}$ ohms and so imposes virtually no load on the input. This is wired as a noninverting voltage amplifier with unity gain and is used as a buffer for the input. VR4 is used to set the offset voltage at zero which would otherwise set the trace off centre.

R31 acts as a load resistor for IC4, another MOSFET op-amp. This is wired as an inverting voltage amplifier with variable gain. The gain is set by one of the resistors R32 to R40 on S7 and is calculated as the ratio of the selected resistor to R31.

For example, if the 2 mV /division range (R33) is selected, the required output from IC4 is 100 mV (not 1 V yet) and so the gain must be $100 / 2$ $=50$. Thus R33 divided by R39 (one kilohm) must equal 50. R33 is therefore $50 / 1=50$ kilohms. VR5 is used to offiset the output voltage as in 1 C 3.

R41 acts as a load resistor for IC5, another mOSFET op-amp. This is


Fig. 5. Left timebase delay circuit diagram. wired as an inverting voltage amplifier with a gain of 10 , the gain being set by the R42/R41 ratio. R43 is present to reduce inaccuracies in the output due to bias currents internal to the i.c. and a similar arrangement was not included with IC4 as the value of the resistor is dependent on R42 and R41 and this ratio varies with ICA. VR6 sets the offset as in IC3 and IC4.

The purpose of having two opamps (ICA and IC5) instead of one is to increase the bandwidth by reducing the gain of any one op-amp and to invert the inverse waveform from IC4.

## PEAK VOLTAGE DETECTOR

The peak voltage detector detects the highest positive voltage peak displayed and remembers it for approximately 10 seconds. To understand the concept refer to Fig. 3.

The input waveform is irregular and so making its peak its amplitude hard to measure. However, by switching the peak voltage detector into circuit, the trace seen on the screen is that shown in Fig. 3.


Fig. 2. Left voltage range selection circuit diagram. Again, the right channel is the same but with all component reference numbers prefixed with 100.


Fig. 6. Simple circuit showing the basic operation of the beam splitter section.

## PEAK DETECTOR CIRCUIT

The peak voltage detector is built around IC6, see Fig. 4. If the noninverting input ( pin 3 ) goes negative then so does the output. However, under such circumstances diode D6 is reverse biased and no output is present. If the input goes positive, the output also goes positive and diode D6 is forward biased. The inverting input (pin 2) is now applied with a voltage identical to that of the input through resistor R76. At the same time Cl is charged to the same potential as the inverting input.

If the input (pin 3) now goes negative, diode D6 is again reverse biased and so the charge on the capacitor remains, being slowly discharged by the op-amp input bias current and resistor R78. The initial input voltage is therefore "remembered" and displayed as a slowly decaying trace (decaying because the capacitor is discharging).

The component values have been chosen to give a time constant or discharge time of roughly ten seconds but by depressing Sl3 the capacitor can be discharged immediately. S12 switches the peak voltage detector into circuit when required.

## TIMEBASE DELAY CIRCUIT

The timebase delay is built around IC2, a cmos 555 timer and the circuit is shown in Fig. 5. R50 limits the input current to the trigger input, pin 2. The 555 is a universal timer i.c. which can be configured as a monostable or astable multivibrator, the former is used here to produce a time delay. The RC network of R51, VR3 and either Cl or C 2 sets the time delay and is variable from about $1 \mu S$ to 1 mS .

C3 sets the control voltage of the i.c., that is, the trigger voltage required to cause the output to change state. The order of events after a trigger voltage has ben detected is as follows. The output immediately changes state and an internal short across Cl and C2 is removed, allowing it to charge through R51 and VR3. When the voltage at pins 6 and 7 reaches the trigger voltage, the output of IC2 changes state again and C1 or C2 is discharged via the internal short to earth.

The period the i.c. is in the "on" state is very short and so no problems should arise with regard to the trigger rate.

C4, D1, R52, R53 and R54 serve mainly to smooth the output and reduce transients and false triggering of the 'scope. S9 switches the delayed timebase in or out of circuit as required.

## TRACE SEPARATION

The circuit description for this part of the circuit cannot be made complete unless the basic principle of operation is illustrated. The operation of the trace doubler is quite simple. The "heart" of the unit is the simple diode gate shown in Fig. 6.

Input 1 is capacitively coupled to the junction of D1 and R1, and input 2, to the junction of D2 and R2. The gate is operated by the square wave gate-drive signal which varies between zero and ten volts positive, but when this signal is a.c. coupled to the gate via C 3 , the d.c. component is eliminated and the signal at the top of R3 swings about zero volts as shown. Thus, on high points of the gate signal the top of R3 is 5 V positive and on low points it is 5 V negative.

Now, when the top of R3 is 5 V positive, D1 is forward biased and forms an effective short circuit, so that R1 is virtually connected direct to R3 and input 1 to the output terminal. At the same time D2 is reverse biased and effectively open circuit, so that no part of the input 2 signal appears at the output.

By potential divider action, the output signal is imposed on a potential of 2.5 V positive. Thus, under this condition the output consists of input 1 plus a bias potential of $2 \cdot 5 \mathrm{~V}$ positive.

When, on the next square wave half-cycle, the top of R3 goes 5 V negative, Dl is reverse biased and isolates input 1 from the output, whilst D2 is forward biased and connects input 2 to the output. In this case, however, the output signal is imposed on a potential of 2.5 V negative, so that under this condition the output consists of imput 2 plus a bias potential of $2 \cdot 5 \mathrm{~V}$ negative.

Thus, the composite output signal of the gate consists of input 1 above input 2 , as shown in the diagram, the two signals being separated by a potential proportional to the magnitude of the gate drive signal.


Fig. 7. (above) Circuit diagram of the beam splitter. This section of the circuit is common to both channels (see Fig. 10).


Fig. 8. Circuit diagram of the precision rectifier and buffer to drive the voltmeter output.

## BEAM SPLITTER CIRCUIT

TR1 and TR2 form a free-running multivibrator which gives an approximate square wave signal at TR2 collector (see Fig. 7 for the circuit diagram). D2 and D3 prevent emitter-base reverse voltage breakdown on TR1 and TR2 and improve oscillator stability. S15 enables either Fast or Slow operating frequencies to be selected. The square wave from TR2 collector is direct coupled to TR3 which sharpens up the waveform and passes it on to the diode gate circuit (D4, D5) via VR9, which acts as a trace separation control. The output of the gate is fed to S14 and then to the oscilloscope output, SK3.

The inputs to the circuit are fed to bootstrap emitter followers TR5 and TR6. The 5 kilohm emitter loads of these circuits serve as infinitely variable attenuators (VR10, VR12) which are set to give unity gain, while VR11, VR13 can be adjusted to reduce the gain where necessary. The signals are thus passed at low impedance to the diode gates and, via S16, to the sync output socket. R70 and R71
are placed in the collector paths of TR5 and TR6 to introduce a small degree of Miller feedbacks to prevent instability in the bootstrap circuitry.

S14 switches the beam splitter into circuit.

## VOLTMETER OUTPUT

This circuit produces a d.c. voltage which is in unison with the output. See Fig. 8. IC7 is the basis of a precision rectifier which is an extension of a diode rectifier, but does not suffer from the 0.6 V voltage drop associated with ordinary diodes.

When the input at pin 3 goes positive so does the output and D8 becomes forward biased and the voltage is passed on. If, however, the input goes negative then $D 7$ becomes forward biased and the negative voltage thus applied the inverting input of the op-amp geverrides the negative input at the noninverting input and a positive output results. S20 acts as a smoothing capacitor and C19 improves the response. R80 and VR7 act as a variable path to provide a slight bias to


Fig. 9. Power supply circuit diagram. Note that PL7 and SK7 are only necessary if the batteries are housed externally in an additional case.
diode D7 and hence increase the accuracy.

1C8 forms a non-inverting voltage amplifier with unity gain which has a high enough input impedance so as not to affect the output of the precision rectifier. VR8 is used to set the offset voltage of the op-amp at zero. C20 gives further smoothing of the output so there is virtually no ripple present at meter terminals.

## POWER SUPPLY

A mains power supply would be unsuitable in a circuit such as this because of the problems associated with producing a completely ripple free supply from a.c. As a result, a battery supply is used as shown in Fig. 9. The 9 V produced by each battery is reduced to 6.2 V by the action of zener diodes D9 and D10 and smoothed by C22 and C23 which also act as reservoir capacitors.

R88 and R89 are used so as not to short out the batteries when the Zener diodes conduct.

The battery voltage can drop by over 2 V without affecting the supply voltage or the accuracy. S 17 is the on/off switch.

Precision Resistors
R1, 101, $100 \Omega$ (4 off)
13, 113
R2, 102,
14, 114
R3, 103,
15, 115
R4, 104,
16, 116
R5, 105,
17, 117
R6, 106,
18, 118
R7, 107
R8, 108
31, 131
38, 138
R11, 111, 39, 139
R12, 112 40, 140
R19, 119
R20, 120
R22, 122
32, 132
R23, 123
33, 133
R24, 124 , 34, 134
R25, 125,
35, 135,
44, 144,
45, 145
$0.5 \Omega, 4 \mathrm{~W}$ (2 off)
$0.2 \Omega, 10 \mathrm{~W}$ (2 off)
$0.2 \Omega, 10 \mathrm{~W}$ (2 off)
$500 \mathrm{k} \Omega$ (2 off)
$50 \Omega$ ( 4 off)
$20 \Omega$ ( 4 off)
$10 \Omega$ ( 4 off)
$5 \Omega$ (4 off)
$2 \Omega$ ( 4 off )
$1 \Omega, 2 \mathrm{~W}$ (2 off)
$1 \mathrm{k} \Omega$ ( 6 off)
$500 \Omega$ (4 off)
$200 \Omega$ ( 4 off)
$1 \mathrm{M} \Omega$ (2 off)
$200 \mathrm{k} \Omega$ (2 off)
$100 \mathrm{k} \Omega$ (4 off)
$50 \mathrm{k} \Omega$ (4 off)
$20 \mathrm{k} \Omega$ (4 off)
$10 \mathrm{k} \Omega$ (8 off)

R26, 126
36, 136
R27, 127
37, 137
R46, 146
R47, 147
R48, 148
R49, 149

All $\frac{1}{4}$ or $\frac{1}{2}$ W metal film $\pm 1 \%$ unless otherwise stated. Where values are not in the E24 preferred series, the correct value should be made up from preferred values. See text.

| All other Resistors |  |
| :---: | :---: |
| $\begin{gathered} \text { R } 28,128 \text {, } \\ 80 \end{gathered}$ | $3 \cdot 9 \mathrm{k} \Omega$ (3 off) |
| R29, 129, | $390 \mathrm{k} \Omega$ (4 off) |
| R30, 130 | $6 \cdot 8 \mathrm{k} \Omega$ (2 off) |
| R41, 141 | $39 \mathrm{k} \Omega$ (2 off) |
| R43, 143 | $36 \mathrm{k} \Omega$ (2 off) |
| $\begin{array}{r} \mathrm{R} 50,150, \\ 53,153 \end{array}$ | $1 \mathrm{k} \Omega$ ( 6 off) |
| 54, 154 |  |
| R51, 151 | $2 \cdot 2 \Omega$ (2 off) |
| $\begin{array}{r} R 52,152 \\ 88,89 \end{array}$ | $100 \Omega$ (4 off) |
| R55, 58, 59 | $4 \cdot 7 \mathrm{k} \Omega$ (3 off) |
| R56, 57, 64 | $82 \mathrm{k} \Omega$ (3 off) |
| R60 | $680 \Omega$ |
| R61 | $18 \mathrm{k} \Omega$ |
| R62, 63 | $8 \cdot 2 \mathrm{k} \Omega$ (4 off) |

Capacitors

Semiconductors
D1, 101 2, 3, 6, 106, 7, 8
D4, 5
D9, 10
TR1, 2, 3, \}
4,5,6 IC1, 101, 3, 103,
4, 104,
5, 105, 6, 106,
7, 8
IC2, 102

$0.01 \mu \mathrm{~F}$ polyester
$1 \mu \mathrm{~F}$ polyester (2 off)
$2,200 \mathrm{pF}$ polyester (2 off)
,00pF polystyrene low leakage $\pm 5 \%$ ( 2 of )
$\pm 5 \%$ (2 Of)
$10 \mu \mathrm{~F} 25 \mathrm{~V}$ elect. ( 6 off)
$6.8 \mu \mathrm{~F} 25 \mathrm{~V}$ elect.
$1 \mu \mathrm{~F} 25 \mathrm{~V}$ tantalum ( 2 off )
$0.47 \mu \mathrm{~F}$ polyester
$4 \cdot 7 \mu \mathrm{~F} 25 \mathrm{~V}$ elect.
$470 \mu \mathrm{~F} 40 \mathrm{~V}$ elect ( 2 off )

1 N4148 small signal silicon (8 off)
OA200 germanium (2 off)
BZX61 C6V2 6.2V 1.3W Zener diode (2 off)
2N3904 silicon npn (6 off)

CA3140 f.e.t. input op-amp (12 off)

7555 CMOS timer (2 off)

## Miscellaneous

B1, 2
9V PP6 batteries (2 off)
Single-sided p.c.b. $173 \times 140 \mathrm{~mm}$; console case with front panel $300 \times 220 \mathrm{~mm}$ minimum (see text); case to house battery pack; knobs, 21 mm diameter, 4 off red, 8 off blue, 2 off yellow, 2 off green, 2 off grey; 8 -pin d.i.l. holder ( 14 off); Veropins; $7 / 0.2 \mathrm{~mm}$ equipment wire.

## Switches

S1, 101 6, 106, 15, 12, 112, 16, 116, 9 ,
109, 17
S2, 102
14
S3, 103
S4, 104,
5, 105,
7, 107
S8, 108
S10, 11
S13, 113
Connectors
SK1, 101
SK2
SK3
SK4
SK5
SK6
SK7
SK8, 10
SK9
PL7

## COMSTRITATIOH starits hare

## CIRCUIT BOARD

The main circuit is constructed on a single printed circuit board, 173 x 140 mm , the full size artwork of which is given in Fig. 11. On the original prototype shown in the photographs, the p.c.b. was larger than that given in Fig. 11. However, due to restrictions imposed by the size of the page, the layout has been made more compact but with components still retaining their relative positions.
One benefit of this reduction in size (apart from a cheaper piece of board!) is that the two PP6 batteries can be fitted into the main console unit, thus eliminating the need for the three-way DIN plug and socket (PL7 and SK7) and the case for the battery pack.

The components are inserted into the board as shown in the p.c.b. assembly diagram, Fig. 12, and all the usual precautions concerning polarity of diodes, capacitors and semiconductors must be taken. It is recommended that Veropins are used for all flying lead connections.

## FRONT PANEL

The rotary switches, toggle switches, control potentiometers, and input/output sockets are all mounted to the front panel as shown in Fig. 13. This panel is made from 1 mm thick ( 20 s.w.g.) aluminium sheet, 300 x 220 mm .

The positions of the hole centres are given on this diagram but the diameters of the holes will depend upon the type of component used, so this should be measured carefully before drilling.

Fig. 14 shows the rear view of the front panel and this is to be used as a guide when finally wiring up. Note that the voltage and current range resistors are mounted directly onto the relevant switches.

## COMPONENTS

Note that in the components list, the voltage and current range resistors are listed separately as Precision Resistors and that some of the values quoted are not in the E24 preferred series, for example, 500 ohms. Therefore, if the constructor wishes to maintain accuracy, these values must be made up from two E24 values, in our example, a 470 ohm resistor and a 30 ohm re-


Fig. 10. System wiring diagram showing how the two channels are wired together. Letters in italics refer to the interconnections shown on the individual circuit diagrams (Figs. 1, 2 and 4 to 9 ). Power supply connections not shown.
sistor (both E24 preferred values) will equal 500 ohms.

However, in order to save on cost (at the sacrifice of accuracy), the nearest E24 value can be substituted. For example, the 5 kilohm resistor can be a 5.1 kilohm, the 2 ohm a $2 \cdot 2 \mathrm{ohm}$ and so on. If this course is chosen, then these resistors can all be $\pm 5$ per cent tolerance types, if the subsequent drop in accuracy is acceptable.

It should also be noted that four sections of the circuit, the current range, voltage range, timebase delay and the peak voltage detector are duplicated for each channel. The circuit diagrams given are for the left channel and those for the right channel are identical but all components have been given the prefix 100 to assist identification. For example, R1 becomes R101, S13 becomes S113, and so on.

## CASE

The prototype unit was built into a specially made console type case constructed from Perspex clear acrilic sheet, 6.4 mm ( $1 / 4$ inch) thick with an aluminium front panel. Plywood of the same thickness could also be used.

The dimensions of the case are: base $300 \times 212 \mathrm{~mm}$, height, 120 mm at the rear and 60 mm at the front.

The base was butt jointed together with battens added to the corners for additional strength. The assembly is then glued together with epoxy adhesive (such as Araldite).

## WIRING

The final wiring is carried out with the aid of the p.c.b. assembly drawing, the rear view of the front panel and the wiring list. All wire
used must be the multistranded type, a $7 / 0 \cdot 2 \mathrm{~mm}$ equipment wire is ideal.

When routing the wires, it is suggested that the p.c.b. assembly and front panel assembly are positioned so that when secured to the case assembly, the wiring acts as a kind of hinge to permit easy removal.

## OPERATING INSTRUCTIONS

Power is supplied via the 3 -pin DIN socket SK7 mounted on the rear panel (unless the batteries are mounted in the case).

The unit accepts two inputs via the b.n.c. sockets (standard $50 \Omega$ ) located on the front panel. The signal should be connected to these inputs via good quality screened leads only.

Two outputs are provided by the unit. To the right of centre on the front panel are three 4 mm sockets. These are outputs for the oscilloscope and are connected up as follows - The red socket is connected to the signal input, the black socket to the earth and the yellow socket to the external sync input. To the left of centre are two 4 mm sockets. These are provided for a meter and are conneated, red to the positive meter terminal and black to negative.
Oscilloscope and Meter Settings
The following oscilloscope controls should be set as shown.
Sync select - external
Sync level - fully positive
Input select - a.c.
X shift, $Y$ shift - as appropriate to centre trace
Volts/division - $1 \mathrm{~V} /$ div or $0.5 \mathrm{~V} /$ div (see later)
Timebase (Time/div) - as appropriate
All other controls - as stated in the oscilloscope manual.


Fig. 11. Full size p.c.b. artwork for the Oscilloscope companion main component board. Photograph below shows completed assembly.



Fig. 13. Drilling drawing and assembly diagram for the front panel.


TAG CONFIGURATION TYPICAL OF SWITCHES
52, 54, 55, 57, 58, 516, 5102.
5106, 5105, $5107 \& 5108$


Fig. 14. Rear view of the front panel showing all switch terminal references and switch mounted components.

## OSCILLOSCOPE COMPANION POINT-TO-POINT WIRING DATA

All wires to be multi-stranded, p.v.c. sleeved equipment wire. Use as many colours as possible to assist identification. Some terminals have more than one wire soldered to them so it is advisable to check the next wire link on the list before making the joint.

- Wire links marked thus are only required if a separate battery housing is used.

| WIRE NO. | FROM | TO | WIRE NO. | FROM |  | WIRE NO. | FROM | TO |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 001 | S1-1 | S2-A | 043 | S101-5 | S112-2 | 085 | S14-2 | S14-8 |
| 002 | S1-2 | SK1 | 044 | S101-6 | S106-5 | 086 | S14-3 | S14-6 |
| 003 | S1-3 | S6-2 | 045 | S102-1 | S102-7 | 087 | S14-A | SK3 |
| 004 | S1-4 | S2-C | 046 | S102-7 | S103-A | 088 | SK3 | PCB-18 |
| 005 | S1-5 | S12-2 | 047 | S102-2 | S102-3 | 089 | SK5 | PCB-19 |
| 006 | S1-6 | S6-5 | 048 | S102-3 | S102-B | 090 | S15-1 | PCB-20 |
| 007 | S2-1 | S2-7 | 049 | S102-B | PCB-101 | 091 | S15-2 | PCB-21 |
| 008 | S2-7 | S3-A | 050 | S102-5 | S104-A | 092 | S15-3 | PCB-22 |
| 009 | S2-2 | S2-3 | 051 | S102-6 | S105-A | 093 | S15-4 | PCB-23 |
| 010 | S2-3 | S2-B | 052 | S102-8 | S102-9 | 094 | S15-5 | PCB-24 |
| 011 | S2-B | PCB-1 | 053 | S102-9 | PCB-102 | 095 | S15-6 | PCB-25 |
| 012 | S2-5 | S4-A | 054 | S106-1 | PCB-103 | 096 | VR9-1 | PCB-26 |
| 013 | S2-6 | S5-A | 055 | S106-3 | S108-1 | 097 | VR9-2 | PCB-27 |
| 014 | S2-8 | S2-9 | 056 | S106-4 | PCB-104 | 098 | VR9-3 | PCB-28 |
| 015 | S2-9 | PCB-2 | 057 | S106-6 | S108-A | 099 | VR11-1 | PCB-29 |
| 016 | S6-1 | PCB-3 | 058 | S107-A | PCB-134 | 100 | VR11-2 | PCB-30 |
| 017 | S6-3 | S8-1 | 059 | S107-X | PCB-135 | 101 | VR11-3 | VR13-3 |
| 018 | S6-4 | PCB-4 | 060 | VR103-1 | PCB-105 | 102 | VR13-3 | PCB-31 |
| 019 | S6-6 | S8-A | 061 | VR103-2 | VR103-3 | 103 | VR13-1 | PCB-32 |
| 020 | S7-A | PCB-34 | 062 | VR103-3 | S110-2 | 104 | VR13-2 | PCB-33 |
| 021 | S7-X | PCB-35 | 063 | S110-2 | PCB-106 | 105 | SK1-E | SK6 |
| 022 | VR3-1 | PCB-5 | 064 | S110-1 | PCB-107 | 106 | SK6 | SK4 |
| 023 | VR3-2 | VR3-3 | 065 | S110-3 | PCB-108 | 107 | SK4 | SK101-E |
| 024 | VR3-3 | S10-2 | 066 | S109-2 | S109-4 | 108 | SK101-E | S103-X |
| 025 | S10-2 | PCB-6 | 067 | S109-4 | S14-C | 109 | S103-X | S104-X |
| 026 | S10-1 | PCB-7 | 068 | S14-C | S112-5 | 110 | S104-X | S105-X |
| 027 | S10-3 | PCB-8 | 069 | S109-3 | PCB-109 | 111 | S105-X | S5-X |
| 028 | S9-2 | S9-4 | 070 | S109-5 | S16-4 | 112 | S5-X | S4-X |
| 029 | S9-4 | S14-B | 071 | S109-6 | PCB-110 | 113 | S4-X | S3-X |
| 030 | S14-B | S12-5 | 072 | S112-1 | S112-4 | 114 | S3-X | PCB(0V) |
| 031 | S9-3 | PCB-9 | 073 | S112-3 | PCB-111 | 115 | S17-2 | PCB $(+9 \mathrm{~V})$ |
| 032 | S9-5 | S16-1 | 074 | S112-6 | PCB-112 | 116 | $\mathrm{PCB}(+9 \mathrm{~V})$ | SK8 |
| 033 | S9-6 | PCB-10 | 075 | S113-1 | PCB-113 | 117 | S17.5 | PCB (-9V) |
| 034 | S12-1 | S12-4 | 076 | S113-2 | PCB-114 | 118 | $\mathrm{PCB}(-9 \mathrm{~V})$ | SK10 |
| 035 | S12-3 | PCB-11 | 077 | S16-2 | S11-1 | 119** | S17.3 | SK7.1 |
| 036 | S12-6 | PCB-12 | 078 | S16-5 | S11.3 | $120 *$ | S17-6 | SK7-3 |
| 037 | S13-1 | PCB-13 | 079 | S11-2 | SK2 | 121** | SK7-2 | PCB(OV) |
| 038 | S13-2 | PCB-14 | 080 | S16-3 | S14.4 | 122 | PCB(0V) | SK9 |
| 039 | S101-1 | S102-A | 081 | S14-4 | PCB-15 | 123 | PL7-1 | B1( +ve ) |
| 040 | S101-2 | SK101 | 082 | S16-6 | S14-7 | 124** | PL7-2 | B1(-ve) |
| 041 | S101-3 | S106-2 | 083 | S14-7 | PCB-16 | 125** | B1(-ve) | B2( +ve ) |
| 042 | S101-4 | S102.C | 084 | S14-1 | PCB-17 | 126* | PL7-3 | B 2 (-ve) |

The oscilloscope is now set up and any adjustments will be detailed in the appropriate section of these instructions.

The meter, if connected, should be set to read d.c. voltage and should read 5 V f.s.d. minimum.
Voltage Measurement (A.C. and D.C.)
Set the following switches on the unit as indicated -
voltage/current select - voltage. voltage range - as required or hi if order of input not known.
Voltage range switches - as required or $50 \mathrm{~V} / \mathrm{div}$ if order of input not known.
peak voltage detector select out
timebase delay - out
Channel select - the channel being used ( L or r )

Any input will now be sent to the CRO, the trace being deflected as per the voltage range selected.
Peak Voltage detector
Set the oscilloscope to measure d.c. and $1 \mathrm{~V} / \mathrm{div}$. With no input,
switch the peak voltage detector select to the 'in' position. Adjust the oscilloscope $Y$ shift control to centre the trace. Any input will now have its peak value displayed as a straight line on the c.r.o. screen. The reset button may be used to zero the trace after an input has been removed.

## Timebase Delay

Switch the timebase delay switch to the in position. With an input, adjust the delay time controls in conjunction with the oscilloscope's "Sync Level" control to produce a steady trace in a good position on the screen.

## Beam Splitter

Set the controls of each channel as required. Set the channel select switch to the bS position. Set the c.r.o. to read a.c. $0 \cdot 5 \mathrm{~V} /$ div. Adjust the trace separation control and the $Y$ shift control to obtain two traces, one half way up the top half of the screen and one half way
down the bottom half. Both channels may now be displayed simultaneously.

If the traces appear too large to comfortably fit on the screen, use the input attenuators. In the min position, the traces are accurate and readings may be taken from them.

## Sync Outputs

The SYNC output is selected via the appropriate switches and can be taken from left or right channel, beam splitter or signal or timebase delay. After setting, the "sync select" control of the oscilloscope should be adjusted to get a steady trace.

## Reading the Meter

If a meter is connected then all that has to be done is read it as normal. The voltage will relate to the r.m.s. value of the waveform and can be changed into the correct value by multiplying the meter reading by the Units/div value.

## Everyday News

Lightilines

# BRITAIN GETS WORLD'S LONGEST OPTICAL FIBRE . . . 

The world's longest optical fibre telephone cable came into service recently (July 22) between London and Birming. ham, the busiest trunk route in Britain. This is the latest addition to British Telecom's "Lightlines" optical fibre network.

British Telecom started laying the 204 kilometres (128 miles) of optical fibre cable in October last year. After only nine months the cable has begun carrying calls between the country's two major business centres.
The new fibre cable replaces the old metal cable London to Birmingham link first installed in about 1887. Then the cable consisted of seventy-six wires.

## .. . And Martlesham Medal for Optical Fibre Pioneers

Two British Telecom scientists, who achieved the first vital breakthroughs in optical fibre technology, have received the Martlesham Medal.

Dr George Newns and Dr Keith Beales were key members of a small Telecom research team which in the late 1960s and early 1970 s helped to steer Britain into a leading world position in optical communications.


The Chairman of British Telecom, Sir George Jefferson, holds up a specimen of optical fibre cables at the opening of the world's longest optical communication route.

In particular, they took the double crucible method of making fibre from a crude laboratory concept to a highly successful production process which has been licensed to British, European and American companies and which has a waiting list of applicants.

An eariy picture, circa 1887, of an engineer working on the original London to Birmingham metal cable link. The cable consisted of seventy-six wires. In those days, once the cable had been joined, it was finally "wrapped and wiped" in molten lead.

Cable laying teams installing the new optical fibre cables keep in touch with distant teams by radio link. Optical fibres are hair-thin strands of glass cable capable of carrying thousands of simultaneous telephone calls and digital information down a single cable.


Figures from the German Patents Office in Munich indicate no shortage of invention and innovation. The office has archives of 22 million patents with new applications at a rate of 100,000 a year.

Demand for audio equipment is at a low level with even the Japanese complaining. The slump has forced the closure of Wharfedale, a top name in Britain for loudspeakers over the last 50 years. - See "For Your Entertainment"

## TOOL OR TOY?

In a world still reeling in trade recession the personal computer is an unquestioned star. It has fired the imagination of millions. The professional computers in the offices of successful manufacturers are overheating totting up the daily profits.

Beautifully produced brochures and other promotional aids bear all the hallmarks of high-powered marketing, presenting the personal computer as a fun thing, which it is, as an education, which it also is, and as a modern necessity for managing personal affairs which is very much a matter of opinion.

The publicity bandwagon should have had a further boost from a BBC Radio 4 Tuesday Call Phone-in. The two experts in the studio were enthusiastic on TV games, uncomfortable when pressed to reveal more serious and useful applications.

It all sounded so unconvincing that I was prompted to cast my mind back to the pre-computer era and recall how mother, head of a poor but happy family in a previous recession, managed her housekeeping budget within a cash income of $£ 3$ a week. She had never then heard of a computer (they hadn't been invented), was not mathematically minded, knew nothing of economics.

But she, too, had a computer of sorts consisting of a row of jam jars kept on a high shelf in the pantry. They were labelled Rent. Hire Purchase, Food, Coal, Gas, Electricity, Insurance, Clothes, Entertainment and so on.
On pay day the total incoming cash was divided and entered physically into the jam jar store in the various categories of outgoings and then withdrawn and disbursed as necessary during the week. The jam jars, being transparent, acted not only as a store (RAM) but also doubled as an instant visual display unit (VDU) showing at a glance the cash status of any liability at any time in the weekly economic cycle.
At the end of the week any surplus cash was "dumped" into a main bulk store called the Post Office where an obliging clerk would up-date her investment balance with a handwritten hard-copy print-out in her savings book.
The only snag was that mother's computer gave us kids no exciting electronic games. But my two sisters acquired needleworking and dressmaking skills out of surplus scraps for their dolls. We couldn't afford to buy a wireless set so I found out how to build one. It was my first step to a career in electronics.

Brian G. Peck

## NEWS FROM SPACE

## Twenty Years On

In the 20 years since Telstar, the world's first communications satellite to relay live TV pictures between the USA and Britain, the technology and usage have grown to today's 300 aerials at 200 earth stations with 13 geostationary Intelsat satellites handling two thirds of the world's intercontinental traffic.

## Comet Study

British Aerospace is prime contractor in a team of nine from Europe in a $£ 34$ million project to study Halley's Comet when it comes near the earth in 1986.

The Giotto space sotellite which will carry experiments by a number of European universities is to be lounched in 1985, taking eight months to intercept the comet which passes close to earth every 76 years.

## CB Probe

Suggestions that different CB technical standards in EEC countries are a barrier against trade, and therefore illegal under Common Mar ket trade rules, could lead to harmonisation proposals on permitted power, frequencies and type of modulation.
British "breakers" may have to face yet another change of equipment if Common Market officials press for unification of standards throughout the Community.

ICL is to spend $\mathbf{£ 3 5 0 , 0 0 0}$ in support of relevant research projects in British Universi ties. In total; ICL's $\mathbf{R} \& \mathbb{D}$ budget is of the order of $£ 65$ million per year.

As a result of a re. appraisal of its UK activities, Tandberg the Norwegian electronics company is to concentrate entirely on the specialist field of high technology hi fi and electronic learning aids.

## Akio Wins Albert

The Royal Society of Arts prestigious Albert Medal has been awarded to Akio Morita, chairman and chief executive of Sony, for distinguished service in technological and industrial innovation. He is the first ever Japanese to win the medal.

## KEEPING AHEAD

A first contribution of $£ 6.9$ million has been granted by the EEC to the European Strategic Research Programme in Information Technology (Esprit).
This programme is intended to keep Europe abreast, if not ahead, of similar programmes in the USA and Japan. Apart from central EEC funds, national governments and companies involved will also be investing large amounts. GEC, Plessey and ICL and nine other leading European companies are participating.

## Next it's WCY

Hard on the heels of Britain's Information Technology Year is World Communications Year (WCY) in 1983 sponsored by the United Nations.

The All-Electronics/ECIF Show is changing its name to "The European All-Electronics/ECIF Show". The venue is to be the Barbican Arts and Conferences Centre and will run from April 19. to 211983.

## SEDAC WINNERS VISIT MULLARD

The 1982 prize winners of the SEDAC Schools Competition enjoying their trip around the Mullard, Blackburn factory. Our picture shows the winning team from Peel Sixth Form College, Martin Lysejko and Anthony Hudson, watching Pat Wilson operating a scanning electron microscope in the works laboratory-For details of how to enter the 1983 SEDAC Schools Competition see page 721.



DESCRIBED here is the construction of a simple device for determining the first to pass the winning post on a slot car race track. It can be relied on to give an unbiased judgement. Although primarily designed for a model car race track, the constructor may well adapt the device for other purpases. The circuit uses a phototransistor buried on each track to detect the passing cars.

## CIRCUIT DESCRIPTION

In the circuit diagram, Fig. 1, TR1 and 2 are two phototransistors and these are buried in the track so that when a car passes overhead it interrupts the ambient light falling on the transistors, causing the oollector current to drop, hence the collector voltage to rise. These positive going pulses are fed into the clock inputs of KCl (pins 3 and 11).

ICl is a CMOs 4013 containing two flip-flops connected together such that if one flip-flop changes its state, it prevents the other from doing so. This is done by connecting the $\bar{Q}$ output of one flip-flop to the $D$ (DATA) input of the other. Whenever a positive pulse is applied to the clock input of a flip-flop, the $Q$ output goes to the same state as the data input, and the $\bar{Q}$ to the opposite state.

Hence, if for example, the car detected by TR1 is the first to pass the post, the Q output of ICla goes to 1 and the $\bar{Q}$ output goes to 0 . The data input of IC1b is thus held at 0 , so that when the second car passes the post as detected by TR2, the $\mathbf{Q}$ output of IClb goes to 0 ; that is there is no change since the $Q$ output of ICla and IClb started at O , and the $\bar{Q}$ at 1.

Any further inputs to either flip-flop will cause no change in the output states until the flip-flops are reset by S1, which gives a momentary 1 to the reset inputs, pins 4 and 10.

The Q outputs 1 and 13 are connected to TR3 and TR4 respectively. When a flip-flop changes state the corresponding transistor is turned on, and hence drives the l.e.d. connected to it. R3 and R5 determine the current, and hence the brightness of the l.e.d.s. The l.e.d.s could be driven directly from the flip-flop outputs, but the current available does not give a very bright display, so TR3 and TR4 are therefore included to give more current drive.

The current used by the cirouit is less than $1 \mu \mathrm{~A}$ with no l.e.d. alight, and about 20 mA when lit.

## COMPONENTS

TR1 and TR2 were TIL78 phototransistors. Other types could be used, though R1 and R2 may need to be changed according to the sensitivity of the detectors. TR3 and TR4 may be any small signal $n p n$ transistors. The l.e.d.s may be any size or any colour, the prototype used one red and one green.

## CONSTRUCTION

The prototype Photo Finish unit uses a plastic case, a PP3 battery and an on/orf switch. It may well be
that the constructor may be able to fit the circuit board into an existing piece of race track accessory and use the power supply of the race track. The circuit will function over a wide nange of voltage up to 15 V , however, it may be necessary to adjust R3 and R5 if the voltage is significantly lower than 9 V .

If an external power source is used it may be necessary to use a diode and capacitor as shown in Fig. 2 to prevent electrical noise from the race cars from triggering the flip-flops.

A piece of stripboard 10 strips by 31 holes was used to mount the components. This size of board was chosen to fit the mounting slots of the box, size $85 \times 34 \times 56 \mathrm{~mm}$, with an aluminium lid.

Fig. 3 gives the component layout on the board, the underside track breaks, and the wiring to the components mounted on the lid. Drill a straight piece of race track to enable the phototransistors to be a tight fit and choose a position such that the passing car will obscure the ambient light. Push them in from the underside and secure with glue. The connections to the unit may be by direct wiring or via terminal blocks (3-way required).

## IN USE

In most cases the ambient light should be sufficient for the phototransistors to detect the cars. If not, place the piece of track with the detectors near a window or underneath a light. If no response is obtained from a cell, it may be that its connections are reversed.
Finally, it may be found that the shadow of someone passing nearby will trigger the photocell, if so, try to position the finish line away from such obstructions.


Fig. 1. The circuit diagram of the Photo Finish.


Fig. 3. Stripboard layout and external component wiring diagram. Note that the wires to the remotely mounted phototransistors TR1 and 2 can either go direct or via a 3 -way terminal block.


## COMPONENTS



Semiconductors

| D1 | TLL209 red l.e.d. |
| :--- | :--- |
| D2 | TIL211 green l.e.d. |
| TR1,2 | TIL78 phototransistor non |
| TR3,4 | (2 off) |
| BC109 silicon non (2 off) |  |
| IC1 | 4013B CMOS dual D-type |
|  | flip-flop |

Miscellaneous
S1 Push-to-make miniature push button
S2 s.p.s.t. miniature toggle
B1 9V PP3 battery
Stripboard, 0.1 in. matrix, 10 strips by 31 holes; plastic case with aluminium lid, $85 \times 55 \times 35 \mathrm{~mm}$; battery clip; l.e.d. mounting clip (2 off); 3-way terminal block; $7 / 0.2 \mathrm{~mm}$ wire.

# BEAT the RELAY 



AdVances in modern electronic games have rendered the humble board game a little old-fashioned, so here we describe a simple circuit to add a few flashing lights and new excitement to the basic board and counter type game.

Its uncomplicated construction and low cost make this project ideal for the beginner.

## CIRCUIT DESCRIPTION

The basic concept of Beat The Relay is to have a panel on which there is a matrix of drawing pins, and some of these are wired so as to trigger two flashing lamps when touched by a probe.

The full circuit diagram is shown in Fig. 1. The prototype model has 49 drawing pins in a 7 by 7 matrix and the circuit diagram shows five of these wired so as to set the lamps off. However, any number may be used, so this is left to the constructor.

These "hot" drawing pins are connected together and then to the positive of the battery. When the probe makes contact with one of these pins, the relay coil, RLA/1 is energised, thus closing the normally open contacts, RLA/1. This in turn, keeps the relay coil energised and so acts as a latch circuit.

To release this latch, switch S1 has to be opened, in so doing it breaks the flow of current through the relay and resets the circuit. This switch is also the on/orf switch.

## LAMP FLASHERS

Once the relay has latched, the positive power supply is fed to the flasher circuit. This circuit configuration is known as an astable multivibrator which will oscillate when energised.

To discuss how this type of circuit works, we first assume that transistor TR1 is "on" and TR2 is "off". This being the case, LP1 is on and LP2 is off. As TR1 is conducting, its collector (c) is almost at 0 V potential so electrolytic capacitor Cl will charge up through resistor R2 and this increases the potential on the base (b) of TR2 until TR2 itself turns "on".

When this occurs, TR1 turns "off" as the collector of TR2 is now very close to 0 V , this also appearing on the base of TR1. LP2 is now on and LP1 has gone off.

The whole process now reverses, with C2 charging through R3 until TR1 turns back "on" and the sequence repeats itself.

In this way the lamps flash on and off alternately, at a rate governed by the values of $\mathrm{Cl} / \mathrm{R} 2$ and $\mathrm{C} 2 / \mathrm{R} 3$, when RLA is energised.

## CIRCUIT BOARD

The multivibrator circuit was assembled on a small piece of stripboard, 11 strips by 12 holes, and the component layout is shown in Fig. 2 along with the interwiring details. Attention should be paid to the positions of the two track breaks and to the orientation of the electrolytic capacitors and transistors.

## CASE ASSEMBLY

Before the final wiring of the electronic components is performed, the case must be made. The prototype model was assembled in a home-made wooden console, constructed from 6 mm thick plywood as shown in the photographs. It measures 170 mm wide by 150 mm deep by 70 mm high. The sloping front panel is 120 mm from front to back.

The cabinet is butt-jointed and glued and then painted black. The front panel is secured with two screws to allow access for assembly.

The two lamp holders are glued beneath two holes in the top piece, Sl and the drawing pins are fixed into the front panel as shown. It is important that the points of the pins protrude through the back of the front panel. Finally add a small hole for the probe wire and mount the Terry clip holder.

Fig. 1. Circuit diagram of the Beat the Relay game.



Fig. 2. Stripboard layout and interwiring dlagram. Note that the "hot" drawing pins are all commoned up and connected to the wire from S1.

## COMPONENTS

Resistors
R1 $10 \Omega$
R2, $3 \quad 18 \mathrm{k} \Omega$ (2 off)
All $\frac{1}{4}$ W carbon $\pm 10 \%$


## Capacitors

C1, $247 \mu \mathrm{~F} 10 \mathrm{~V}$ elect ( 2 off )
Semiconductors
TR1, 2 BC108 npn silicon (2 off)
Miscellaneous
S1 s.p.s.t. toggle switch
LP1, $2.6 \cdot 5 \mathrm{~V}, 0 \cdot 15 \mathrm{~A}$ filament lamp (2 off)
RLA $12 \mathrm{~V}, 125 \Omega$ coil relay with normally open contacts
B1 9V PP6 battery Stripboard, 0.1 in . matrix, 11 strips by 12 holes; lamp holders ( 2 off, for LP1, 2); battery clips; 7/0.2 equipment wire; empty ball point pen holder (for the probe); draw. ing pins ( 49 off, must not be the plastic coated type); Terry clip (for the probe); 6 mm ( $\frac{1}{4} \mathrm{in}$.) thick plywood approximately $500 \times$ 200 mm (for the case); paint; glue. case

## FINAL WIRING

The relay is glued to the base of the case, and the board is then secured on two stand-offs alongside it. Leave enough space for the large PP6 type battery.
Wire up as shown in Fig 2 taking care to wire the relay correctly with the normally open contacts being used. Note that R1 is soldered to one of the relay terminals directly, with the probe wire connected to its other end.
The probe is made from an empty ball point pen tube with the wire threaded through it and soldered to a small screw, which is then pulled back to fit snugly in the pen holder.
The wire from Sl to the drawing pins is soldered to the point of one pin and then to all the other pins selected to be "hot". Choose these "hot" pins at random with perhaps one from each row with the occa. sional two together or a row with no "hot" pins at all. This all adds to the excitement.

## TO PLAY

The basic idea of the game is to start at the bottom left hand corner of the matrix "board" and work along and up the board, in the same fashion as Spakes and Ladders, with-

(Above) Photograph of the prototype circuit board before the mounting holes were made.
(Right) View inside the home-made case showing the basic layout and Interwiring.
out landing on a "hot" pin. A die is used to decide how many moves the player makes each go.
If the player lands on a "hot" pin, he must pay some penalty like moving back a few pins or missing a go. The actual rules can be made up by the players themselves (before the game commences of course!).
Remember that when moving the probe according to the score on the die, do not actually touch all the pins along the way as this may set the lamps flashing unnecessarily. Only the last pin for each go need be touched.


An additional game for one or two players, is a memory game whereby each player takes it in turn to have a "stab" at the board to try and locate a "hot" pin and at the same time, trying to remember where all the previously discovered "hot" pins are. The winner is the player who can touch all the "hot" pins with the probe, without touching a single "cold" (one which doesn't set the lamps flashing) pin.

Using these two basic principles, many games can be devised to provide many hours of fun for the whole family.

# LETTERS 

## Soviet Diagnosis

I happened to read Paul Young's comments (Counter Intelligence-September ' 82 E.E.) on the use of electronics in Soviet medicine on the day 1 returned from a holiday in the Soviet Union during which experienced it at first hand.
After being told by my own doctors that the stomach discomfort I had been experiencing for some time was due to gastritis or a small ulcer, tests taken at a holiday sanitorium in Yalta quickly diagnosed a hiatus hernia. The Medical Centres at Moscow and Yalta were full of electronic apparatus used for diagnosis and treatment... there was even an ECG on the beach where I stayed at Yalta for immediate tests on swimmers with suspect hearts! A mention that I suffered from migraine quickly resulted in me being wired up to monitors in a room that looked like the control centre of a spacecraft। I'm afraid that when I was given daily inhalation treatment I was more interested in the apparatus than the treatment! Of course, such apparatus is also used in other countries, but the difference seems to be In the quantity and general availability.
Where Paul is wrong is in attributing the widespread use of electronics in the Soviet

Union to there being a shortage of doctors. With over a million doctors, it compared favourably with other advanced industrial countries, including Britain and the U.S.A.

However, there is a much greater emphasis on preventive medicine which involves people having a regular medical check-up. In this, electronics is proving a valuable tool, not as a substitute for doctors but to be used by them in order to ensure the most effective use of their knowledge and skill. Incidentally, their medical service is free.

Fred Westa cott, Chesterfield, Derbys.

## Library of Electronics

I am trying to accumulate a "library" of electronics reference books and I am having great difflculty in finding secondhand books.
Can your readers please suggest any bookshops that might sell secondhand electronics books.
P. A. Antcliff, Hitchin, Herts.

## Model Train Chuffer

With reference to the Model Train Chuffer circuit described in the January 1982 issue, an effective modification is to replace the bridge rectifier and transistor TR1 of the v.c.o. with a light dependent resistor (LDR), illuminated with a 12 or 15 V bulb (LP1) connected across the track, Fig. 1.


Fig. 1.

This system is also less affected by other chuffers and controllers on other tracks.
J. S. Rushton

Papakura
New Zealand


Without fast and efficient communications, all the Army's modern technology and hardware would be useless.

Rather likerush-hourtrafficindensefog.
But with potentially much more disastrous consequences.

For this reason we're asking a lot from our Apprentice Technicians in the Royal Signals these days.

Right now we have a limited number of Technician Apprenticeships in Military Telecommunications.

You've got to have four 'O'levels, at grade C or better, including Maths, English and a Science subject before well be interested in you. Well want to be sure that you're the right type, both physically and mentally, for such a crucial and exacting job.

And we'll want to be convinced that you've a real interest in Electronics.

Naturally, since we're asking this much of you, youll expect quite a lot from us.

And you'll get it. If accepted, you'll go for two and a half years training at the Army Apprentices College in Harrogate, starting on January 6th next year.

There youll receive a technical education in electronicsequal to the best in the country, leading to a minimum of TEC Certificate.

And at the same time an academic and military training that will leave you in peak mental and physical form, ready for early promotion.

If you think you can handle this kind of challenge, and you'll be aged between 16 and $17 \frac{1}{2}$ in January, just fill in the coupon or call in at your nearest Army Careers Information Office, ideally before the end of the month.

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## ZH81 HALF PRICE 4K RAM

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## FUE-VOLT REFULRTED SVPPM

Handy mains operated unit delivers up to 500 mA d.c. Duplicate output terminals. Output voltage virtually unaltered for all load conditions.

## UELOCITY MEESURER

Uses ultrasonics for determining velocity of the object based on the Doppler effect. Outputs can be directly displayed on the internal 7 -segment readout, an oscilloscope or stored in a memory bank for ultimate feeding to a chart recorder to produce velocity-time graphs.


## Cable Options

The British Government is now pondering over what to do about cabling Britain. Its advisory panel, ITAP, said we should cable the country with wide bandwidth optical fibre trunk lines, but use only narrow bandwidth coaxial cables into Individual homes with local switching from the trunk routes.

Some people (myself definitely included) believe this to be a disastrously shortsighted suggestion. It underestimates the future needs of homes which will have several TV sets, computers and two-way interactive data links, or wanting to use the same cable connection for different purposes at the same time.

An optical fibre link into each home would leave the options open for the future. Coaxial cable would close them. No-one wants to dig up Britain again when, as sure as night follows day, the limited bandwidth of a coaxial system proves inadequate.

British Telecom also argues in favour of optical fibre cabling, and not surprisingly also argues that BT, not private enterprise, should be given the contract. "British Telecom is uniquely qualified... to play a major role in the re-cabling of Britain" says Sir George Jefferson, Chairman of BT. "It has the experience and skills needed to install the new cable networks'

## Grass Roots

Without doubt British Telecom has more experience than anyone else in Britain of laying cables of all types, including optical fibre links. But at grass roots level, British Telecom has a very poor track record, for instance when it comes to installing new phones or repairing faults. You have only to read the correspondence columns of the popular press to know the problems suffered by some subscribers.

Recently I encountered these problems first hand. The real revelation was the yawning communication gap between the upper echelons of British Telecom, who make the decisions and the public statements, and the lower echelons, who deal with the public.
"There are 150 faulty phones in the queue for repair ahead of you in your area" I was told by the North London engineers when my phone developed a fault. I'd queued on the line for the best part of quarter of an hour to get through to the engineers.
I then queued as long again to get through to the single extension in the Telephone Manager's office that dealt with subscribers' problems. The best I could get from anyone was a vague promise of action in a day or so.

A succession of engineers finally arrived over the next week, diagnosed different faults inside the house and in the street cables, and then disappeared again leaving the phone still faulty. Engineers responsible for fixing faults inside the house weren't authorised to fix external faults and vice-versa.

All this time, people trying to call me got either unobtainable, engaged or ringing tones.

## The Wharfedale Story

0VER the last ten years I've watched the progress of the loudspeaker firm Wharfedale with special interest. It was founded by audio engineer Gilbert Briggs in 1932.

The Wharfedale Wireless Works built its first two speakers in an Ilkley cellar. The war nearly put an end to the firm because it was impossible to get the cloth needed to make the cone surrounds. But Briggs and his colleagues made do with rejected army dusters.

In 1955 Briggs booked the Royal Festival Hall to give a concert of live and recorded music. To everyone's surprise the concert was a sell-out. Peter Walker and John Collinson of Quad, operated the disc and tape equipment while Briggs, on stage, controlled four Wharfedale loudspeakers, each powered by its own 15 watt Quad valve amplifier.

The BBC, EMI and Decca all cooperated with recorded material, in both mono and stereo. The London Mozart Players under conductor Harry Blech, Denis Matthews piano, Campoli violin, Leon Goossens oboe, and Ralph Downes organ, all contributed live music, while the LPO conducted by Sir Adrian Boult appeared off disc through the Wharfedale loudspeakers.

There was no question of involving the audience scientifically or technically. The concert was just a concert, but with the difference that some of the music was live
and some of it recorded. The object of the exercise was to prove that recorded sound could be directly comparable with live sound. Even in 1955 Briggs proved his point.

After the Festival Hall Briggs arranged similar concerts at Carnegie Hall in New York. Both Briggs and Wharfedale became famous names in audio.

In 1959 Briggs sold Wharfedale to the Rank Organisation. Major expansion began in 1967, at a factory site in Bradford. "The old man" as he was affectionately known worked on in Ilkley, writing books, until he died in 1978.

As the hi fi boom got underway and demand for loudspeakers grew, the Bradford factory added more workers and more production machinery. But this isn't the most efficient way to run a factory. Production techniques that work on a small scale won't work efficiently on a large scale.

At some stage you have to stop, and thoroughly re-design the production process to improve efficiency, and turn out more products at lower cost. Unfortunately Rank Wharfedale started to do this just as the hi fi bubble burst, and the recession started to bite.
For several years the Wharfedale factory in Bradford ran at a loss, and on Friday, July 23 rd 1982 Rank finally shut it down. The staff of 350 were made redundant, on the day they went on annual holiday. To
make matters worse, the Rank closure followed just one month after an Open Day of the Bradford factory to celebrate 50 years of Wharfedale and the launch of a new range of loudspeakers.

## Sound Future

But all is not gloom. The Wharfedale name, factory and assets have now been bought by a British firm Tradewest. This is owned by a British businessman, Peter Newman, who makes and sells loudspeakers in Spain for other Continental manufacturers.

The new Wharfedale firm will re-employ around 60 of the old Wharfedale workers, but with single-union representation.

Ironically the main problem for Newman and Wharfedale is that the company's laboratories have become rightly famous over recent years for their acoustic research and original work in loudspeaker design. As soon as Rank announced the closure of Wharfedale, the ten skilled engineers in the Wharfedale research unit started to receive offers of employment from competitor firms.

Only time will tell whether Rank's decision to sever contact with Wharfedale has been the best thing that ever happened to the company, because it streamlined production, or the worst, because it marks the end of an era in British acoustic research and development.

# Probably the fastest microcomputer in the universe 



## Key Features

- Revolutionary microcomputer language FORTH.
- Full-size moving-key keyboard.
- User-defined high-resolution graphics.
- Programmable sound generator.
- Floating point arithmetic.
- Fast cassette interface.
- Upper and lower case ascii character set.
- $24 \times 32$ character flicker-free display.


## The Jupiter Ace uses FORTH

The Ace is set apart from all other personal computers on the market by its use of a revolutionary language called 'FORTH'. Some computer languages are easy for humans to understand. others are easy for computers; FORTH is most unusual in being both. Its underlying principles are so simple that it takes even a newcomer to computers only a few minutes to learn how to do calculations on the Ace, yet the very same principles are powerful enough to allow you to invent your own extensions to the language itself.

At the same time, the memory-saving coded form used to store your programs inside the Ace allows it to obey them very fast typically in less than a tenth of the time it would take to do the same thing using a different language. Amongst other things, this makes the Ace ideal for games.
FORTH's unique combination of speed, versatility and ease of programming has already made it a prime choice for professional applications as diverse as pub games and radio telescopes, and gained it an enthusiastic national user group. Now the Jupiter Ace can bring this addictive language into your own home.

## Designed by Jupiter Cantab

Leading computer Designers Richard Altwasser and Steven Vickers have a reputation for pushing technology forwards. After playing the major role in creating the ZX Spectrum they formed Jupiter Cantab to develop their latest brainchild the Jupiter Ace. Jupiter Cantab, 22, Fuxhollow, Bar Hill, Cambridge, CB3 8EP

## Technical Specification

## Hardware

## Processor/Memory

Z80A running at 3.25 MHz .
8 K bytes ROM 3 K bytes RAM.

## Input

40 moving-key keyboard with auto-repeat on every key.

## Output

Memory-mapped $32 \times 24$ character display with high resolution user graphics. Output to drive normal UHF TV set on channel 36.

## Sound

Provided by internal loudspeaker.

## Cassette

Load Save \& Verify at 1500 baud, separate data storage.

## Software, FORTH

## Data Structures

Integer, Floating point and String data may be held as constants, variables or arrays with multiple dimensions and mixed data types.

## Control Stuctures

IF-THEN-ELSE, DO-LOOP, BEGIN-WHILE-REPEAT, BEGINUNTIL, all may be mixed and nested to any depth.

## Operators

Mathematical $+,-, \mathrm{X}, \div$.
Logical AND, OR, NOT, XOR.
Comparison <, $>_{\text {, }}=$.

## Program Editing

FORTH words may be listed, edited and redefined. Comments are preserved when words are compiled.


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New book by Tom Duncan in the popular 'Adventures' series This book of entertaining and instructive projects is designed for hobbyists and students. It provides a stepping stone to the microprocessor.
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## Legal maze

ONE thing is certain. Our legislation is lagging light-years behind modern technology. Office copying machines, audio and video tape recorders have stymied the intentions of the copyright laws and turned millions into lawbreakers.
CB radio with a.m. equipment, pirate broadcasting statlons, public interception of distribution satellite television channels, cordless telephones have all combined to bring the existing Wireless Telegraphy Acts into disarray although new legislation is threatened. Governments show their contempt for the international Radio Regulations by "jamming" broadcasts they dislike or by operating powerful over-the-horizon h.f. radars that disrupt other services.

Some sections of the Official Secrets Act have been virtually abandoned. "Bugging" is sometimes illegal, sometimes legal.

Briefly-booming video cassette libraries including those of Woolworth and W. H. Smith, have been closed because they cannot compete with the many "pirated" cassettes, while for many years similar problems have hit the reputable recording industry.

Television programmes across international frontiers have already led to tangled lawsuits, for example in Belgium. The different restrictions on television advertising in different countries may also prove a minefield when Direct Broadcast Satellites (DBS) begin transmissions.

I was reminded of these problems recently when Oliver $P$. Ferrell sent me a copy of the 5th edition of his "Confidential Frequency List" (Gilfer Associates Inc, PO Box 239, 52 Park Avenue, Park Ridge, NJ 07656, USA, \$9.95). This book provides over 220 pages crammed with information including callsigns and frequencies of thousands of what are described as "unusual" radio stations that can be heard in the shortwave spectrum, including many coast stations that pass traffic to and from ships, aeronautical stations, fixed "point-to-point" stations, diplomatic stations and the like.

To very few, if any, of these thousands of stations has a UK citizen any legal right to listen-let alone to divulge what he may "unintentionally" hear them transmit. He can, however, do this to a duly authorised officer of Her Majesty's Government, a person acting under the authority of the Secretary of State or a competent legal tribunal.
He may not reproduce in writing, copy, or make any use of such messages or allow them to be reproduced in writing, copied or made use of. So says the letter of the Law.
H.M. Government, it goes without saying, is not bound by such restrictions-if it were then unemployment in the Cheltenham area might rise dramatically. H.M. Government is even prepared to provide
facllities to foreign Governments to intercept our personal telephone conversations and telex circuits!

## Bad Laws

It used to be assumed that laws that are unenforceable are bad laws. It is legal for me to have a copy of the Confidential Frequency List (at least I hope this is the case), it is legal for me to have a general coverage h.f. communications receiver, but apparently illegal for me to tune this outside of the bands allotted to broadcast stations, amateur stations and presumably CB stations (or do I need to have a CB licence to listen to CB transmissions?).

Altogether more sensible is the US Communications Act. This prohibits the divulging of the contents of any non-broadcast transmissions but does not specifically outlaw listening to such transmissions.

One day perhaps legislation will catch up with technology.

## Wadley Loop

The other day a news release arrived on my desk from Racal Electronics casually announcing that this major electronics and communications group, made a net profit (before taxation) of over $£ 73$ million on a turnover that has reached a staggering $£ 536$ million.

A success story that began when, while still a relatively new and little known flrm, they backed the radlcal ideas of

Dr Trevor Wadley, a genuinely-brilliant South African electronics engineer. He had devised a novel tuning system, based on three stages of mlxing, that automatically cancelled out any oscillator drift and could readily be set to within 1 kHz , anywhere up to 30 MHz . An elegant (though extremely difficult to implement) system that preceded the full frequency. synthesis systems widely used for professional communications today. The Wadley loop system is still widely used and appreciated by short wave listeners.

In the 1950s, Dr Wadley brought his loop system to the UK and offered it to a number of well known British flrms: all regarded it with great scepticism. Only the small firm of Racal were prepared to take the risk and so was born the RA17.

It Is said that even Racal almost despaired at one stage in development until somebody found that by making a saw cut in the chassis the performance of the complex filters could be vastly improved. Such was the wolldwide success of the RA17 that it rapidly took Racal to the top and made them a real force in all branches of h.f. communications.

Dr Wadley also invented the "Tellurometer" system for measuring distances accurately by means of radio signals, a system adopted by surveyors in many countries. In 1979, the 25th anniversary of this invention was marked by a special South African stamp featuring Dr Wadley with his prototype instrument.

Dr Wadley, an engineer of brilliance and achievement, died recently in South Africa at the age of 61 years.

## Getting a Licence

Criticisms of the long delays involved in obtaining an amateur licence are at last bearing fruit. The City \& Guilds of London Institute has confirmed that in future it will hold at least three Radio Amateurs' Examinations each year. There will be examinations in December 1982, March 1983 and May 1983. This is a start, although still more needs to be done.

This year has also seen lengthy delays by the Home Office in progressing applications and then issuing the licence. At times this has taken a matter of months, but by the Autumn the period of waiting had been reduced to a few weeks. One problem has been that a peak of applications follow each R.A.E., so that more examinations should help even out the flow.

One sees that once again there are suggestions that the difference between Class A and Class B licences should be based on different-level 'teohnical examinations and that it should be possible to obtain a Class A licence without taking a Morse test.

While one can understand the feelings of those who believe (in my opinion wrongly) that Morse is an obsolete mode, some curious daims are being made. For instance that Japan has abandoned Morse tests ( $n o t$ true although it is possible to get a limited, low-power licence including some h.f. bands without Morse the more advanced licences still involve Morse tests) or that r.t.t.y. machine telegrapiny is as effective as Morse and occupies an equally narrow bandwidth (again not true).

# Examination Projects C. J. BOWES 

helpful advice to candidates from a teacher and external examiner

There are now several examinations such as O-level Electronics, Control Technology and A-level Electronics and even Design examinations where the work required for the course includes a project to be designed and constructed by the candidate. And there is of course the Schools Electronic Design Award Competition (SEDAC) sponsored jointly by Mullard Ltd and this magazine.

There are a number of factors involved in designing and building projects which are applicable to constructors embarking on their first projects, either as part of such an examination course or competition, or for their own personal pleasure.

## PERSONAL CHALLENGE

Irrespective of your reasons for taking the plunge, the design and construction of your own project presents an interesting challenge.
When you are designing and constructing a project of your own you will be the one who has to sort out the "bugs". If you are a fairly experienced constructor there is a good chance that you are unlikely to make any serious construction mistakes, but it is essential to check out your circuit design both in theory and on a breadboard very early on in the course of your project so that you can find out any problems and solve them before it gets too close to any deadlines.
Once you have accepted the possibility of errors occurring, the next thing to do is to realise that if a project fails to work first time this IS NOT FATAL, providing you have planned your strategy correctly, but is in fact normal luck

## PLAN YOUR STRATEGY

The key to success is in your ap. proach and attitude towards your project. The first necessity which must be accepted is that if you are working towards a deadline then you cannot afford to leave every. thing to the last minute.
When you come to choose the project that you intend to build it is an advantage to choose one that you actually would like to do, either because you want to use the thing you make or because it includes a technique that you want to try out.

If you know that you are going to have to produce a project as part of an examination course it is useful to carry round with you a small notebook and jot into it project ideas as you think of them so that you will have a list of possible ideas to choose from when the time comes to select your project.

## THE IDEAL PROJECT

A second factor to bear in mind is the difficulty of your project: A simple, easy to do project requiring no thought, is likely to be a bore to finish and, if it is too simple may not gain you many marks. The ideal project is one that is just a little harder to do than that which you could easily accomplish when you start. Preferably it should require a little research into a new component (to you) or circuit idea, BUT NOT TOO MUCH! This will provide you with a surmountable challenge and give you a pleasant feeling of satisfaction when it works.

## ORIGINALITY

If your project is for an examination, the novelty of the circuit ap proach, if it is outside the course work but not too far removed from the course, will usually get you a slightly better mark; but you must be careful to ensure that not only is the design original and not copied out of a magazine or book of circuits, but that this will be clearly seen to be so by the examiner when he or she reads the report.
This does not mean that you will be expected to produce completely new circuit ideas (there is a limit to the "newness" of any project idea) but you will be expected to produce something which has something produced from your mind to alter or extend any published design that you may use as a starting point.

Examples are for instance different combinations of standard circuits or substantial modifications to a published circuit to tailor it to your specific requirements.

## WRITE YOUR SPECIFICATION

Once you have chosen the title of your project, it is necessary to decide and write down, for future
reference, exactly what you want your project to do

In your detailed specification you need to put down all the important features which you feel your project ought to contain. This will include details about any other equipment that you want your design to connect with, any particular components that you want to include, whether the device is to be battery or mains operated and the cost if this is important.

## OUTLINE DESIGN

Once you have clarified your ideas by writing out your specification the next stage is to start on an outline design. Unless your project is extremely simple it is likely that you will need to divide it into a series of separate stages, each one of which can be achieved by a particular circuit.

A block diagram is sometimes useful here to sort out the stages and show how they interact with each other. At this stage you will only be concerned with the outline of the system and need not be concerned with the full detail of the actual circuitry contained in each block.

## ALTERNATIVE SOLUTIONS

Before detailed circuit design begins you should consider how you could actually solve each of the problems posed by the individual sections of the design.

If you are producing a project for examination purposes, you will be expected to show that you have considered alternative methods of solving the problems posed by the sections of your project in addition to the one that you actually develop.
Even if your project is for your own pleasure it is useful to consider more than one method of tackling the problem so that if your first solution does not work you will have other ideas to fall back on. Again the possible solutions and your reasons for the final choice you make should be written down in your notes for future reference.

Practical aspects of circuit design and project building will be discussed in the second and concluding part of this article.

To be continued

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