ESQUEMA GERAL - Microcomputador NE Z-8000 c/ SLOW

JAN MARTIN LUND - SANTOS - Janeiro 1983

74LS00 = 9, 10, 19, 23, 24, 26
04 = 7
05 = 4, 5
10 = 6
30 = 22
32 = 8, 25
74 = 11, 13
86 = 12
93 = 14, 20, 21
157 = 15, 17, 18
165 = 3
365 = 2
373 = 16

= IN914
= IN4148

RAM 1 2114
RAM 2 2114
ROM 2 2732
ROM 1 2732
Z 80 CPU
POWER
7805
VIDEO DIRETO / REVERSO
SOFTWARE SYNCHRONISM
CHARACTER LINE CLOCK
CHARACTER LINE RESET
MIC
EAR
VIDEO
VIDEO (MODUL.)
ROM 2 (ROM 1)
3.25 MHz
2.5 MHz
BC 548 C
BC 237
(8) 1 kΩ
(5) 47 kΩ
(2) 1 kΩ
(5) 47 nF
(8) 100 nF
47 µF