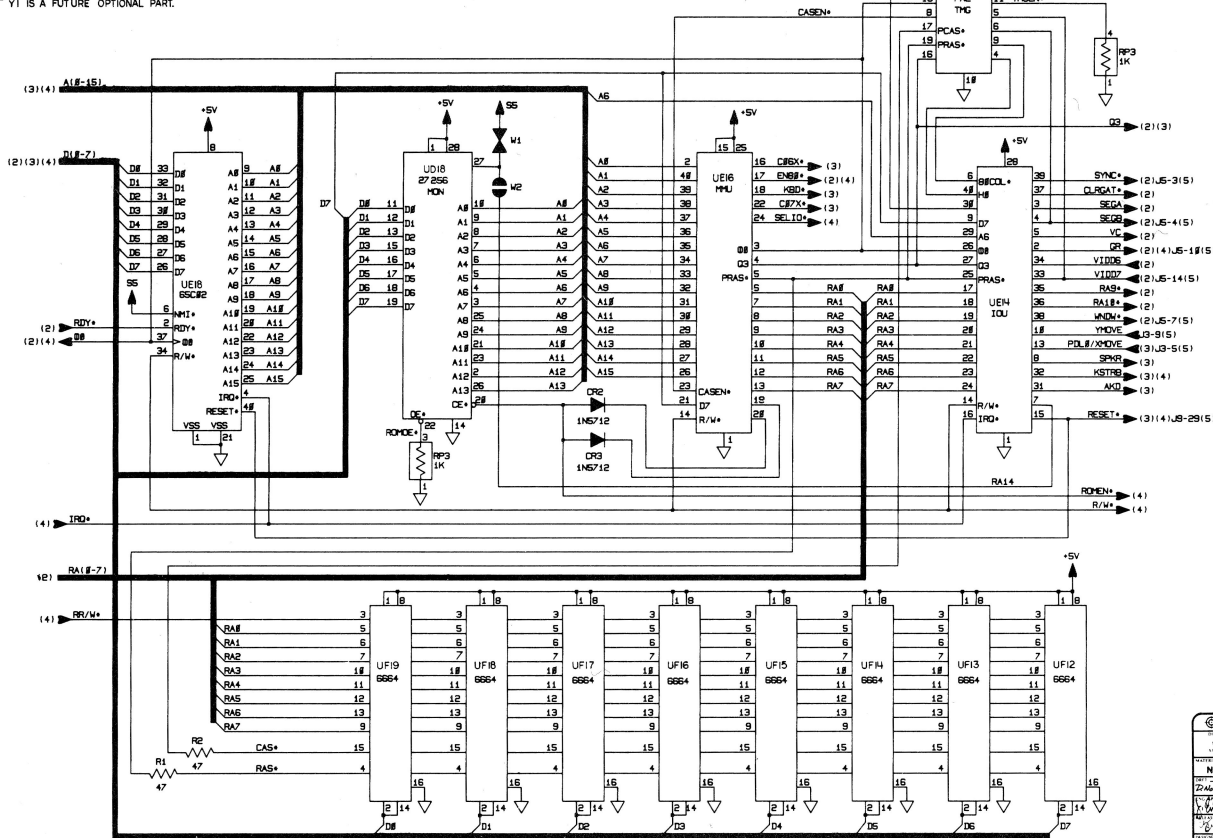


NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTANCE VALUES ARE IN OHMS, $\pm 5\%$, 1/4W.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

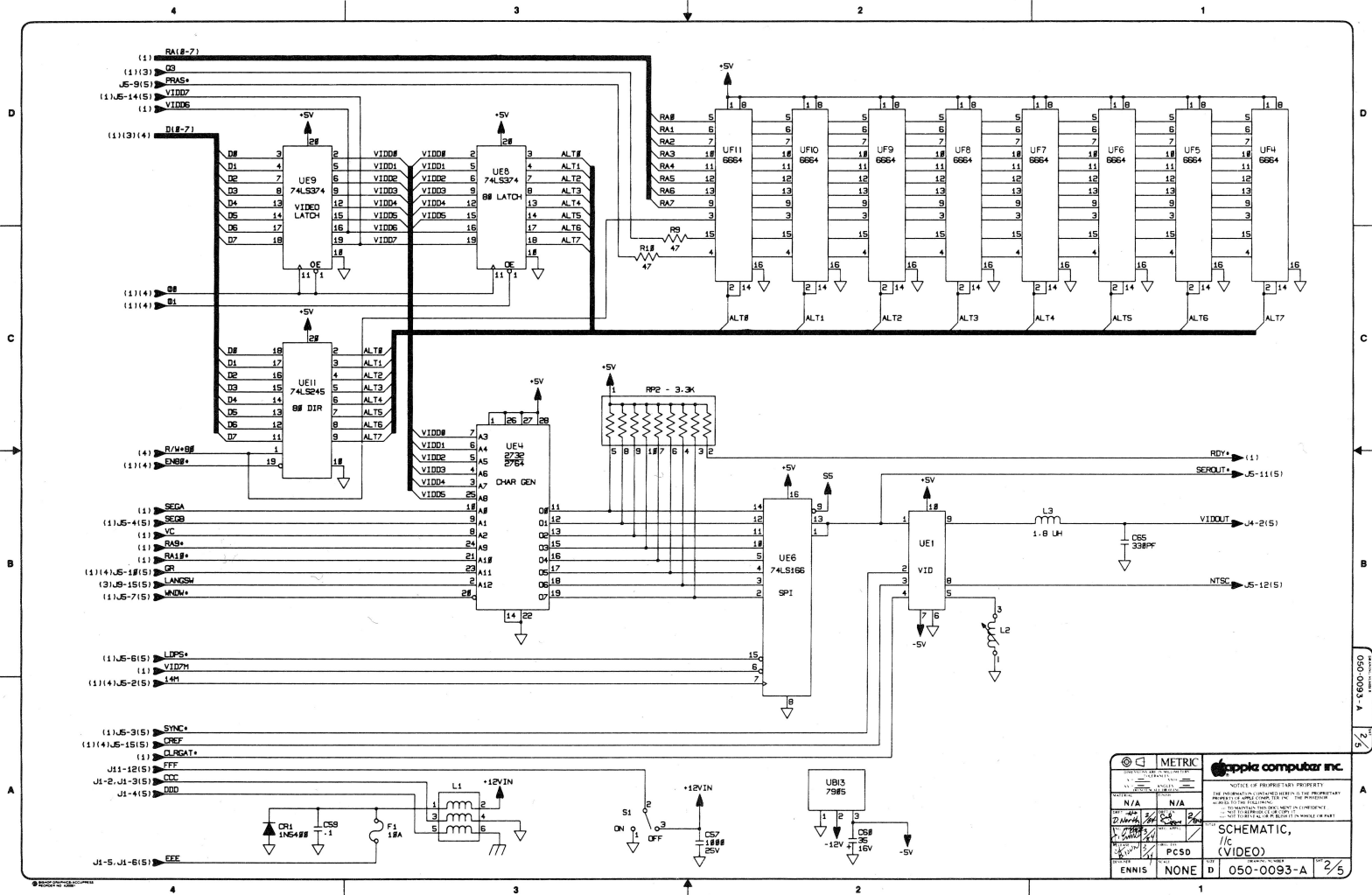
ON SHEET 4 OF 5, LOCATION B4, Y1 IS A FUTURE OPTIONAL PART.

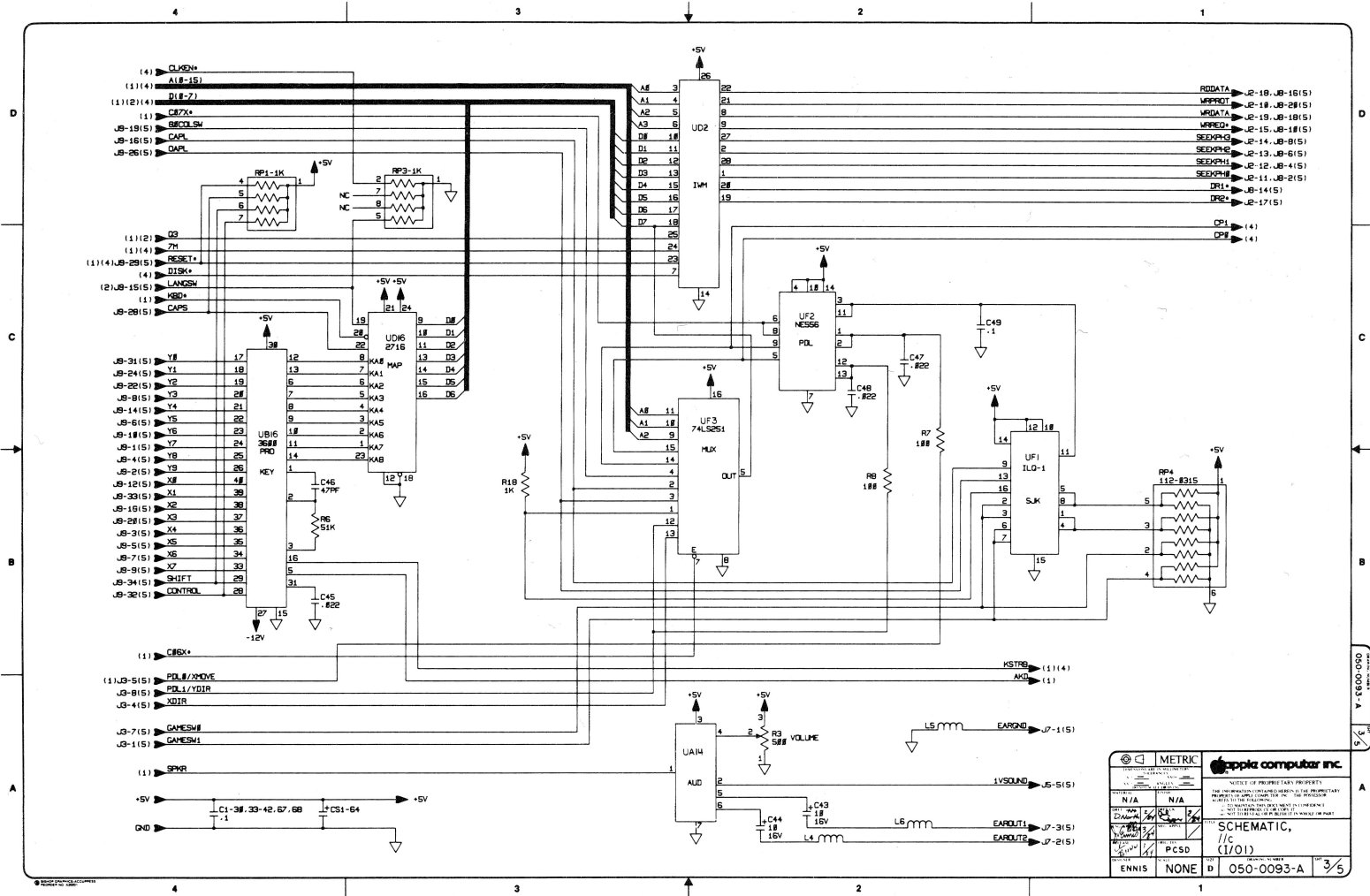
(2)(14), (5-2)(5) 14M
(3)(4) 7H
(4)(5-1)(5) TEXT
(4) TO GND, IO*

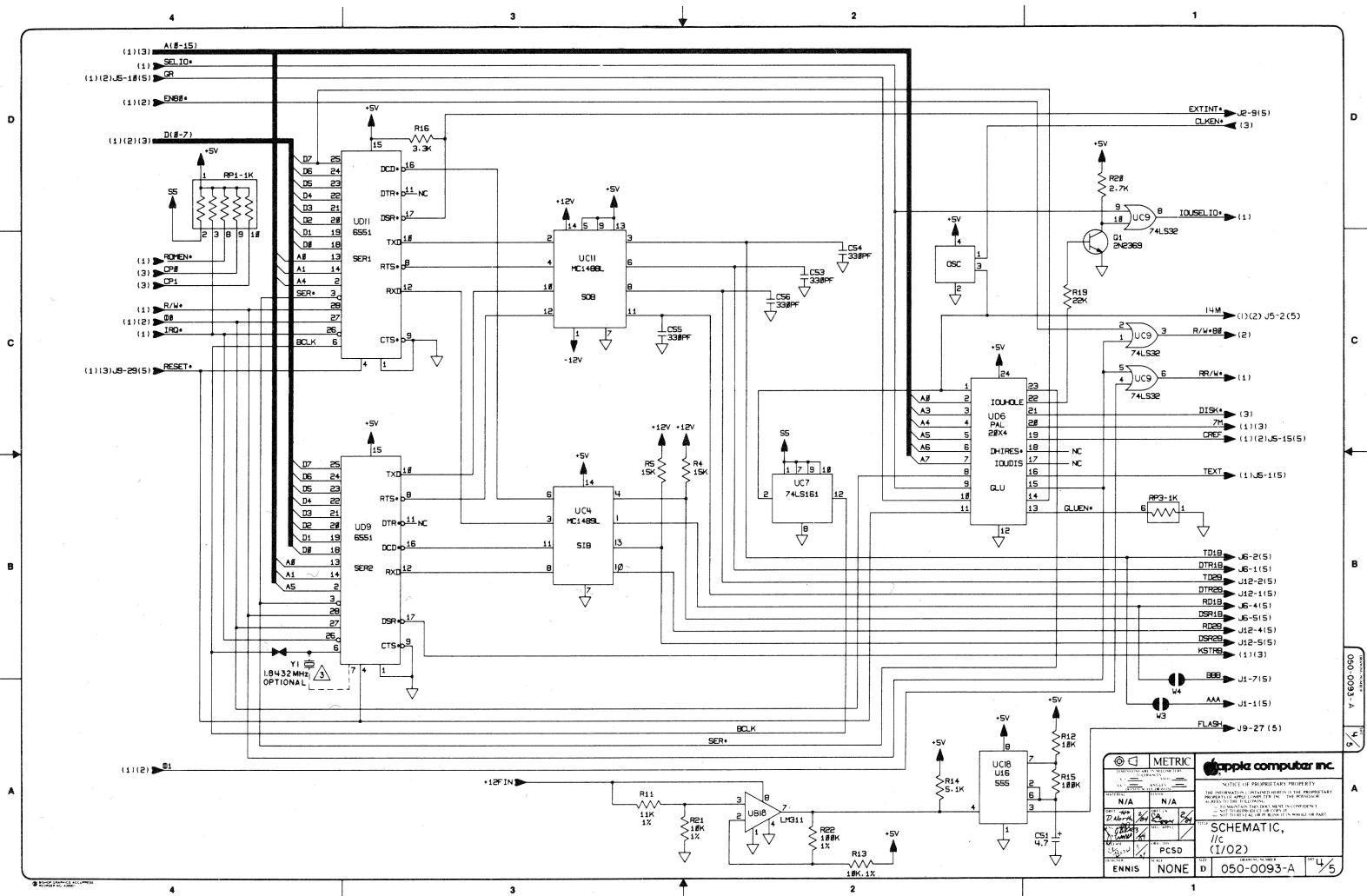


REV	DATE	REV	REVISION	APPRO	DATE
A		A067	INITIAL RELEASE		
B3		J22	CORRECTED IC REFERENCE DESIGNATIONS FROM NUMERICAL TO GRID SYSTEM		
		SHEET 4:	CORRECTED PIN CONFIGURATION OF UC4 TO MATCH FUNCTION OF IC		

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SCHEMATIC, I/c (CPU)			
ENNIS	NONE	D	050-0093-A 1/5



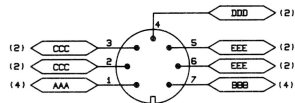
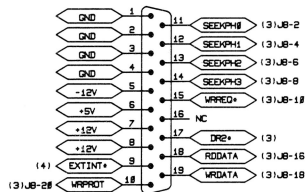
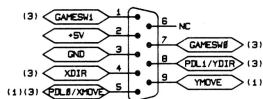
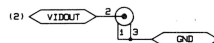
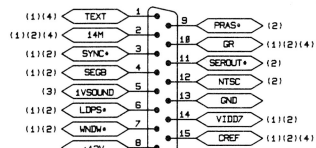
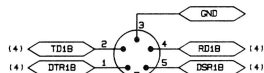
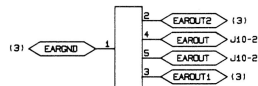
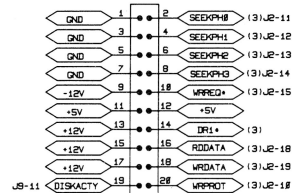
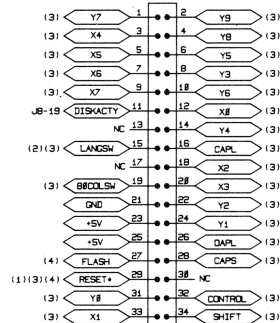
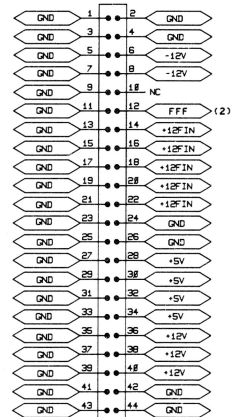
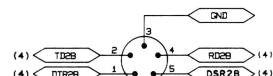




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SCHEMATIC, I/C (1/02)		PCSD	
ENNIS	NONE	D	050-0093-A

050-0093-A 5

D

J1
EXTERNAL POWER INJ2
EXTERNAL DISKJ3
GAMEJ4
VIDEOJ5
VIDEOJ6
SERIAL 1J7
EARPHONEJ8
INTERNAL DISKJ9
KEYBOARDJ10
INTERNAL SPEAKERJ11
POWER SUPPLYJ12
SERIAL 2

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PCSD		PCSD	
ENNIS		NONE	
050-0093-A		050-0093-A	
SCHEMATIC, l/c (CONNECTORS)		SCHEMATIC, l/c (CONNECTORS)	
9/5		9/5	

050-0093-A 9/5

A