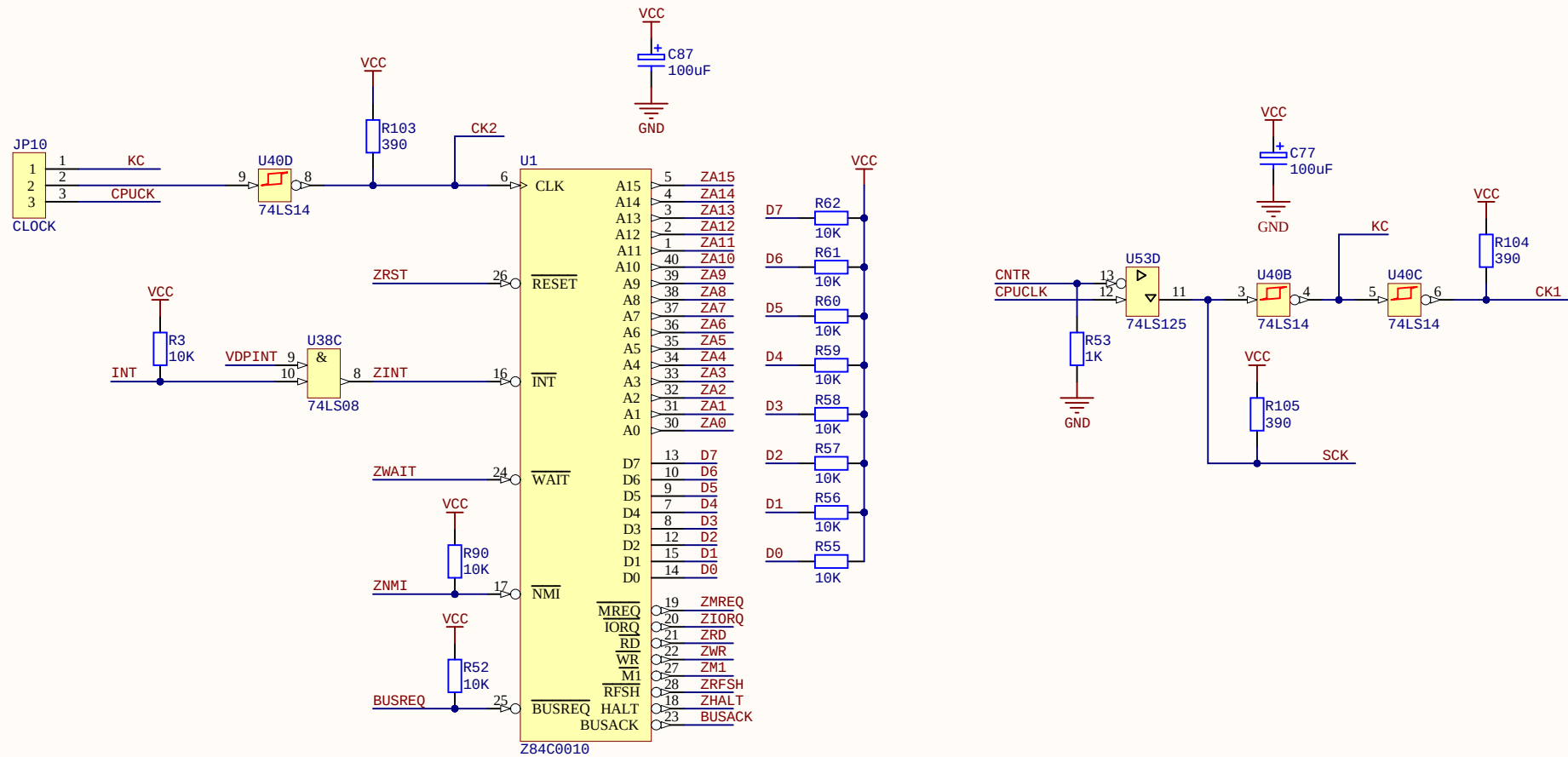
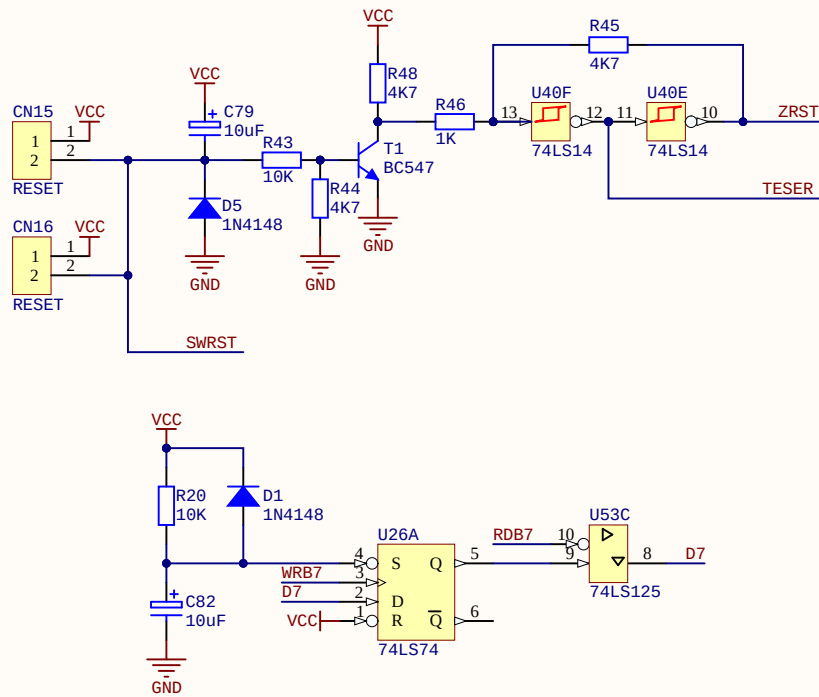
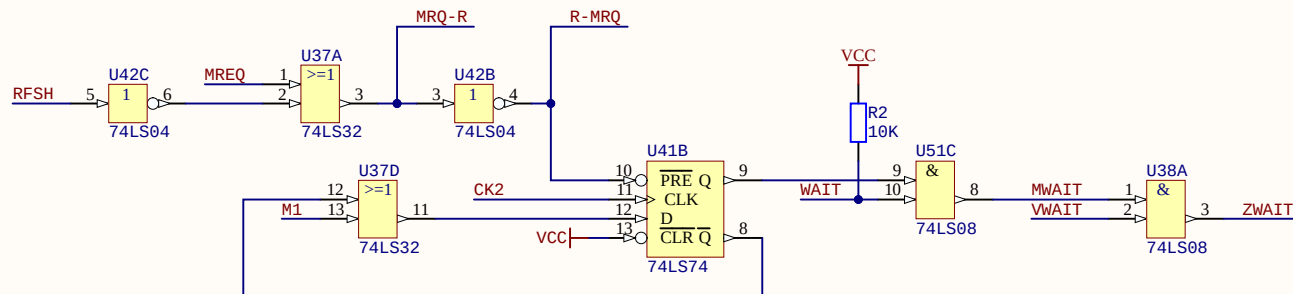




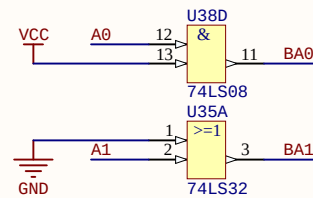
Title			CPU
Size	Number	Revision	
A4		AC21B24	
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\CPU.SchDoc	Drawn By:	ADEMIR CARCHANO



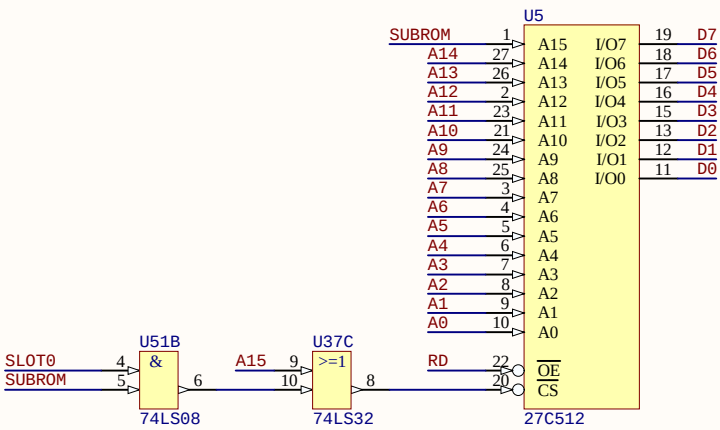
Title			RESET
Size	Number	Revision	
A4		AC21B24	
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\RESET.SchDoc	Drawn By:	ADEMIR CARCHANO



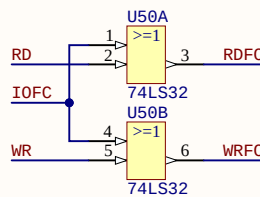
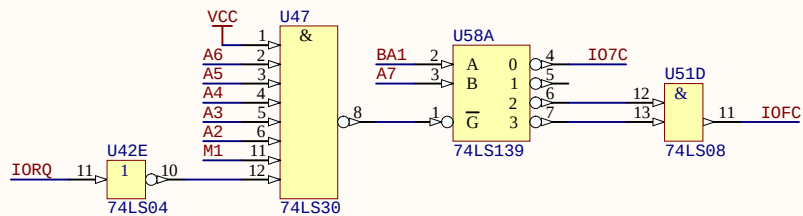
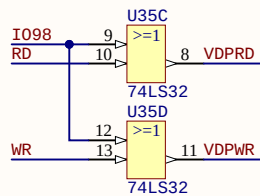
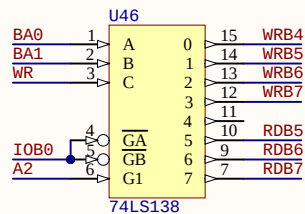
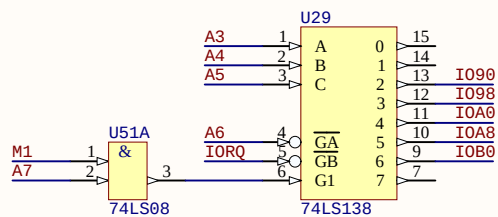
Title			WAIT/M1	
Size	Number	AC21B24		Revision
A4				
Date:	5/16/2022	Sheet	of	
File:	D:\SSD240\...\WAITM1.SchDoc	Drawn By:	ADEMIR CARCHANO	



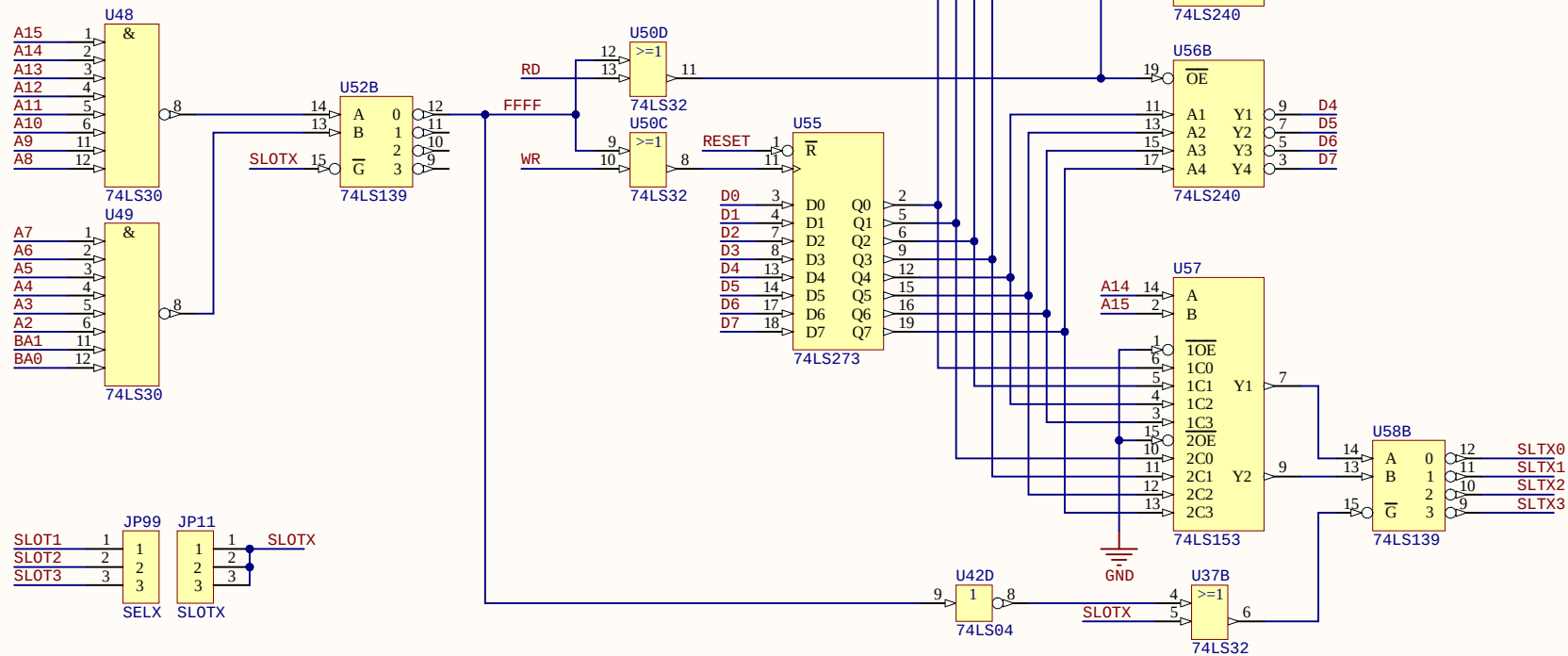
Title		BUFFER	
Size A4	Number	AC21B24	Revision
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\BUFFER.SchDoc	Drawn By:	ADEMIR CARCHANO



Title			EPROM	
Size	Number	Revision		
A4	AC21B24			
Date:	5/16/2022	Sheet of		
File:	D:\SSD240\...\EPROM.SchDoc	Drawn By:	ADEMIR CARCHANO	



Title			SELEÇÃO DE I/O
Size	Number	Revision	
A4		AC21B24	
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\IOSEL.SchDoc	Drawn By:	ADEMIR CARCHANO



Title		EXPANSOR SLOT	
Size A4	Number AC21B24	Revision	
Date: 5/16/2022	File: D:\SSD240\.\EXPLOT.SchDoc	Sheet of	Drawn By: ADEMIR CARCHANO

A

A

B

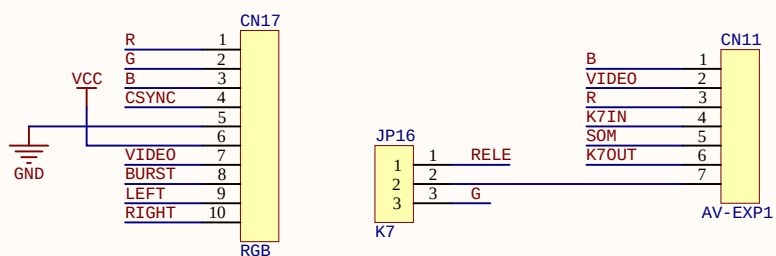
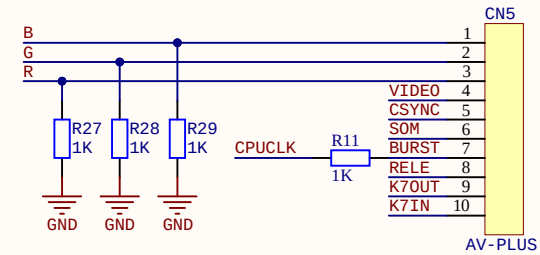
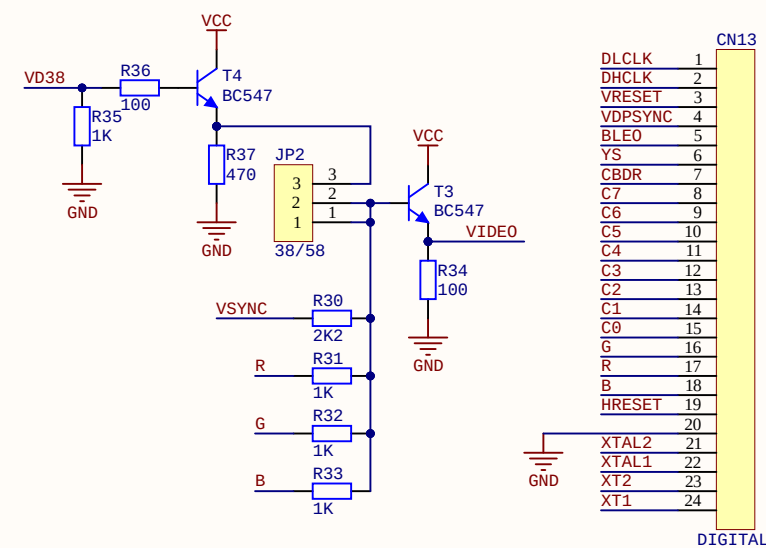
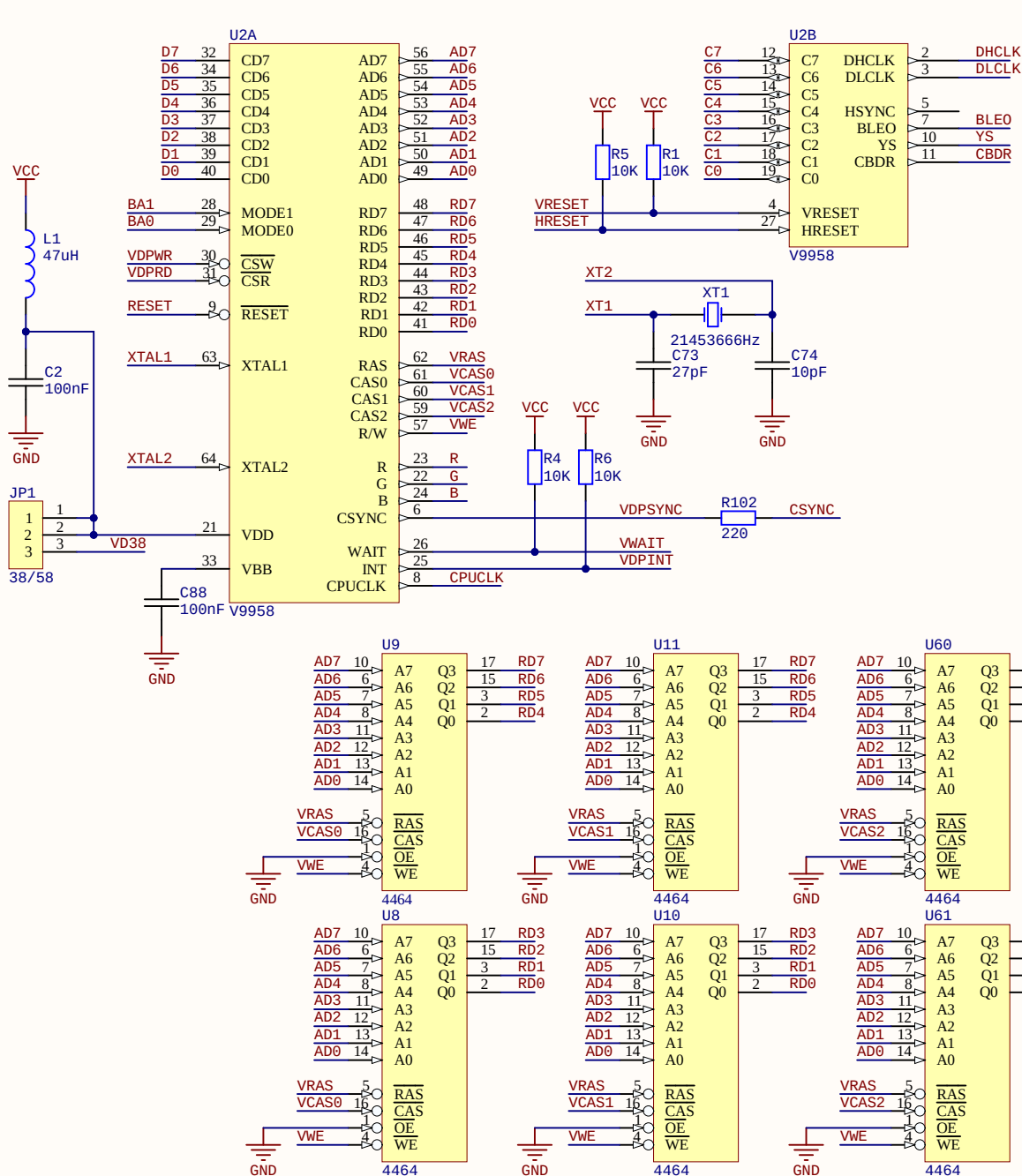
B

C

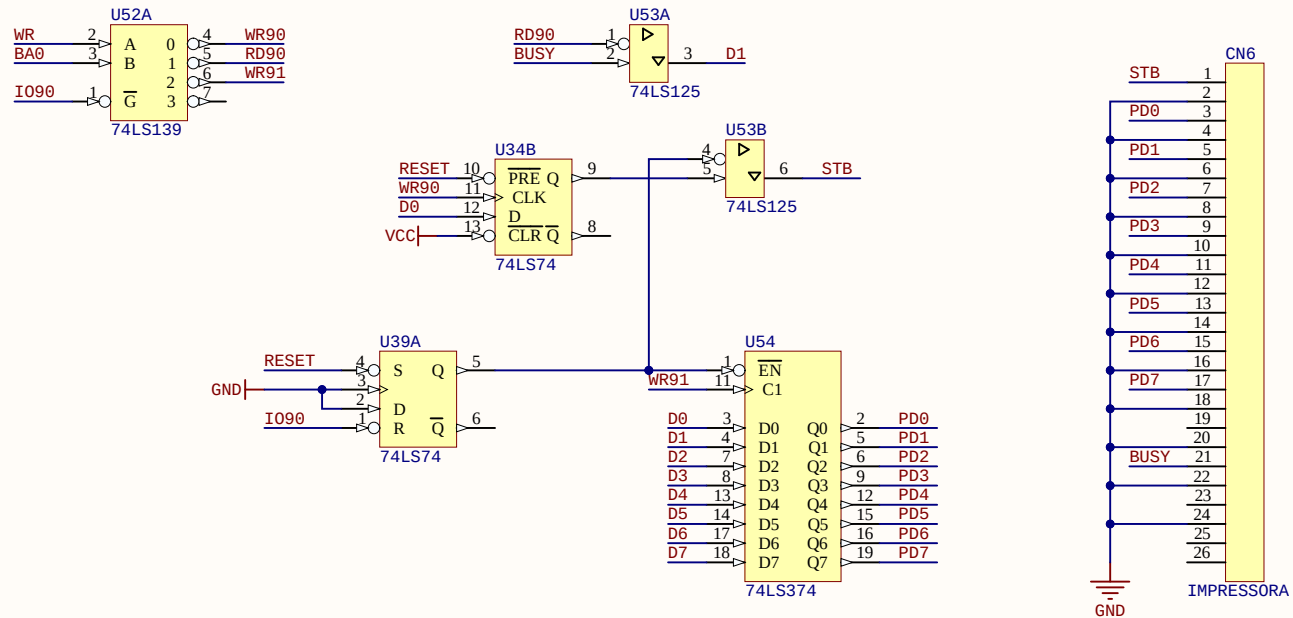
C

D

D



Title		
VDP		
Size	Number	Revision
A4	AC21B24	
Date:	5/16/2022	Sheet of
File:	D:\SSD240\...\VDP.SchDoc	Drawn By: ADEMIR CARCHANO



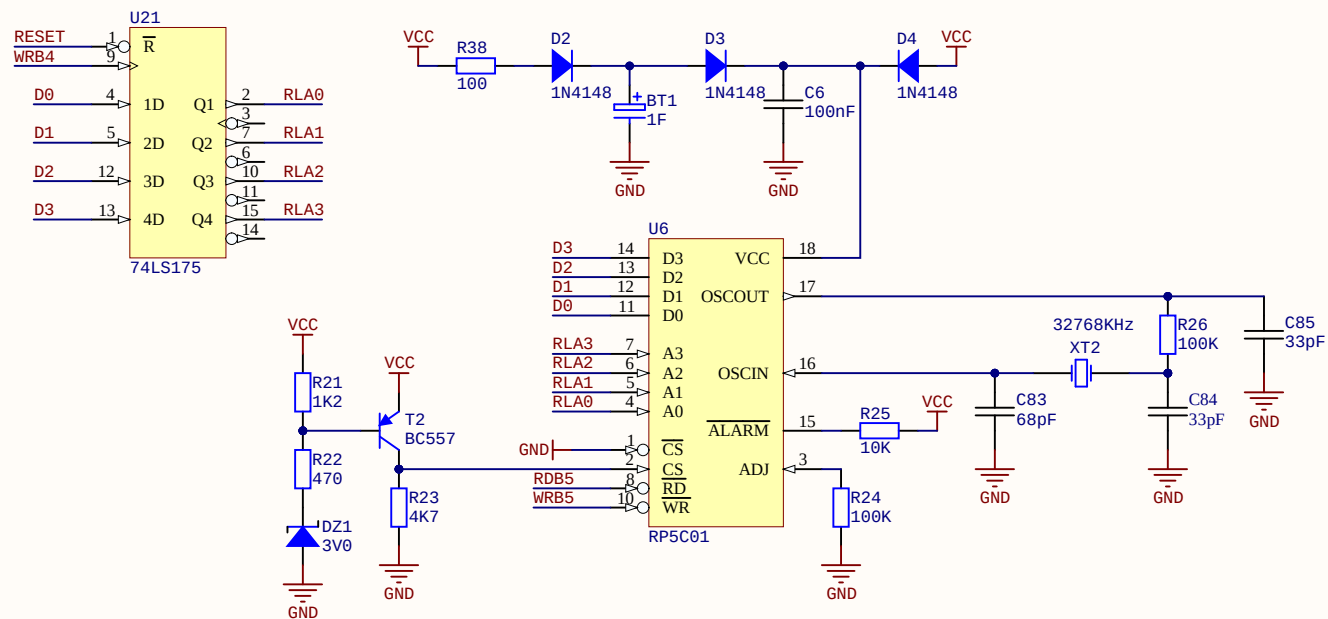
Title			IMPRESSORA		
Size		Number		Revision	
A4		AC21B24			
Date:		5/16/2022		Sheet of	
File:		D:\SSD240\...\PRT.SchDoc		Drawn By: ADEMIR CARCHANO	

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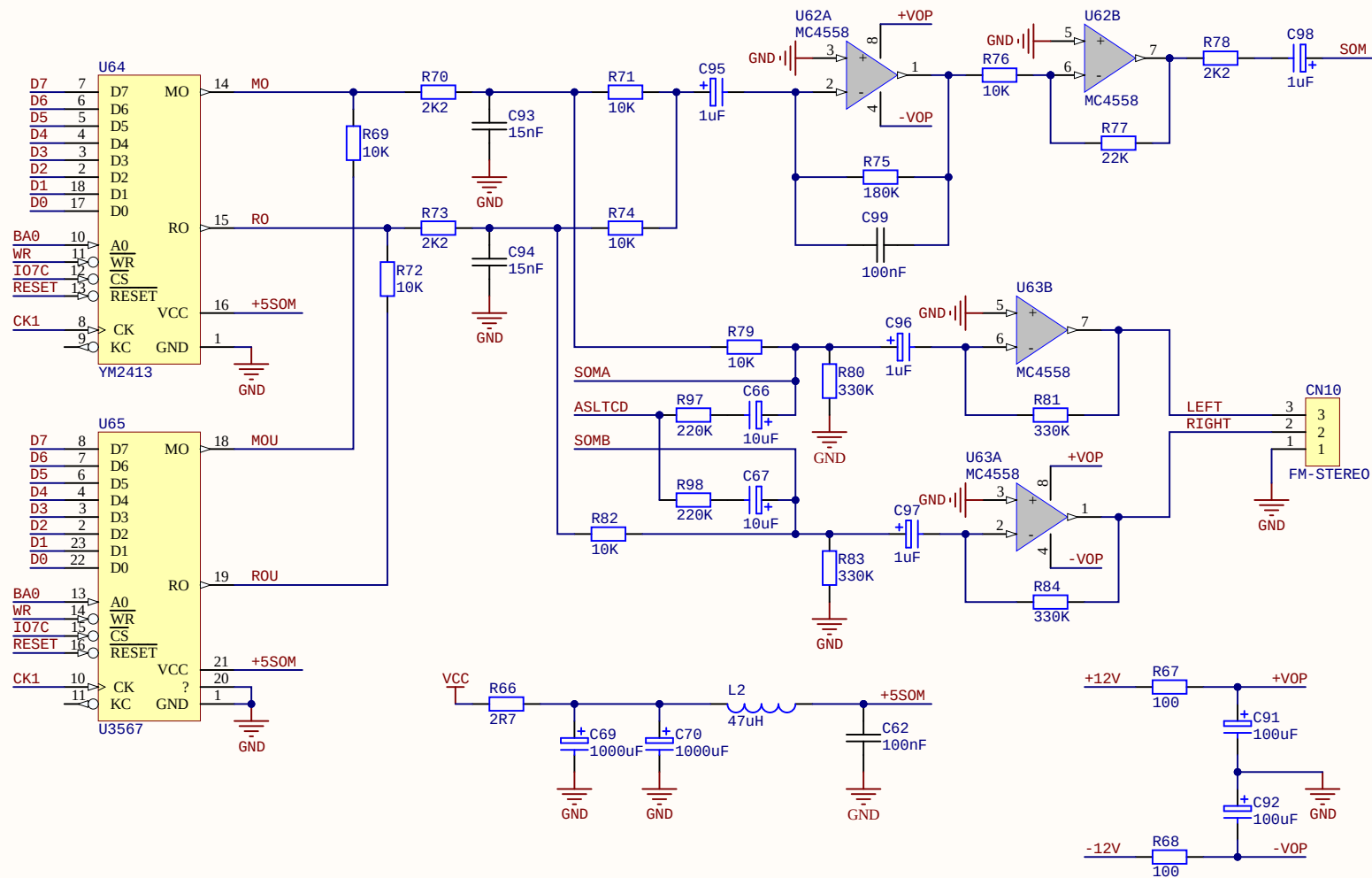
Title			RTC
Size	Number	Revision	
A4		AC21B24	
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...RTC.SchDoc	Drawn By:	ADEMIR CARCHANO

1

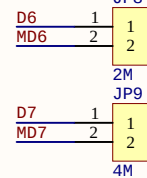
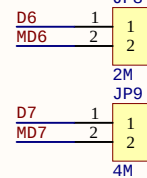
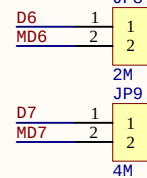
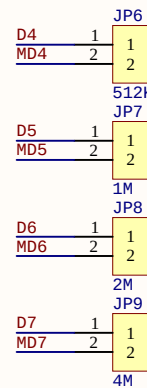
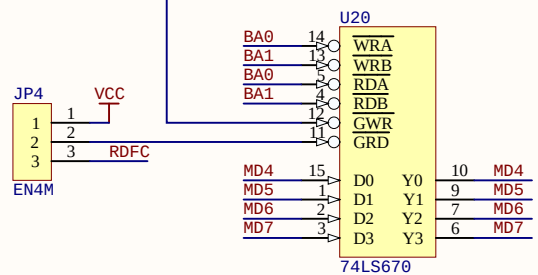
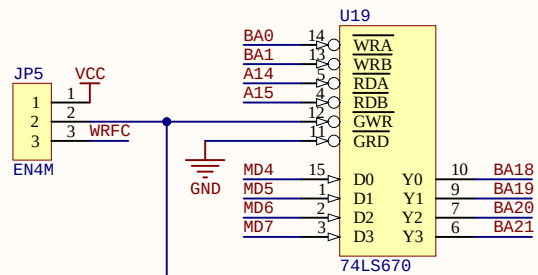
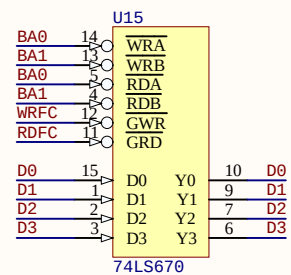
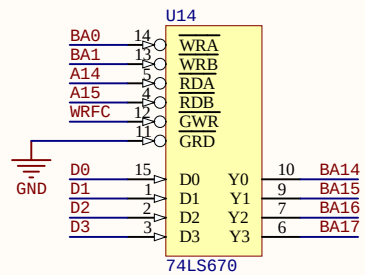
2

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Title			FM
Size	Number	Revision	
A4		AC21B24	
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...FM.SchDoc	Drawn By:	ADEMIR CARCHANO



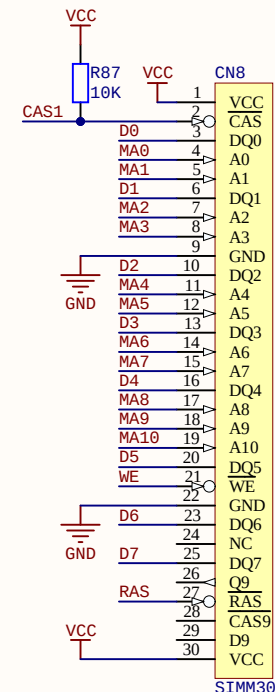
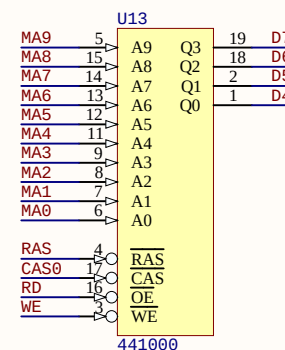
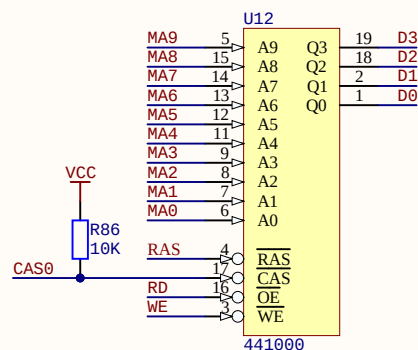
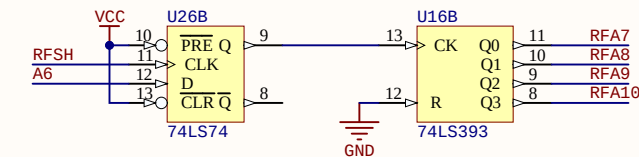
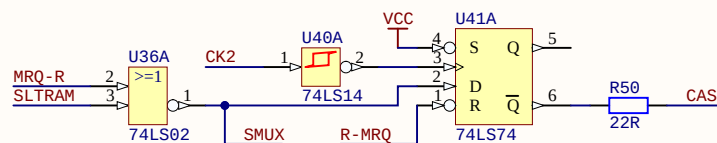
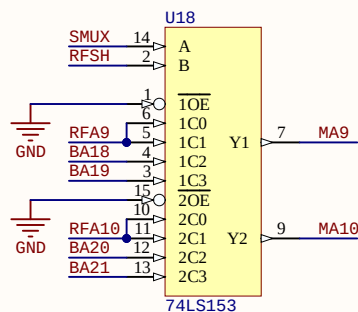
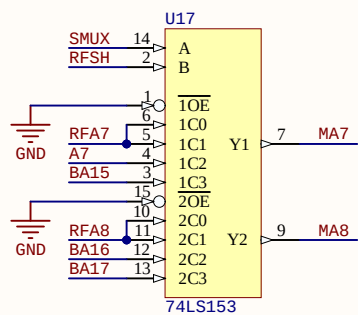
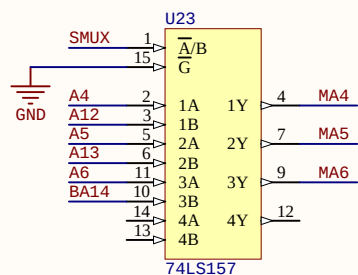
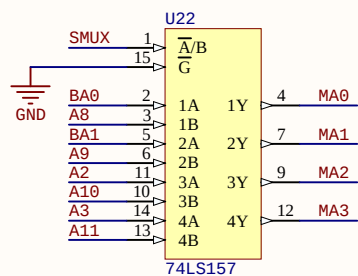
Title			MEMORY MAPPER		
Size	Number		Revision		
A4	AC21B24				
Date:	5/16/2022		Sheet of		
File:	D:\SSD240\...\MAPPER.SchDoc		Drawn By:		ADEMIR CARCHANO

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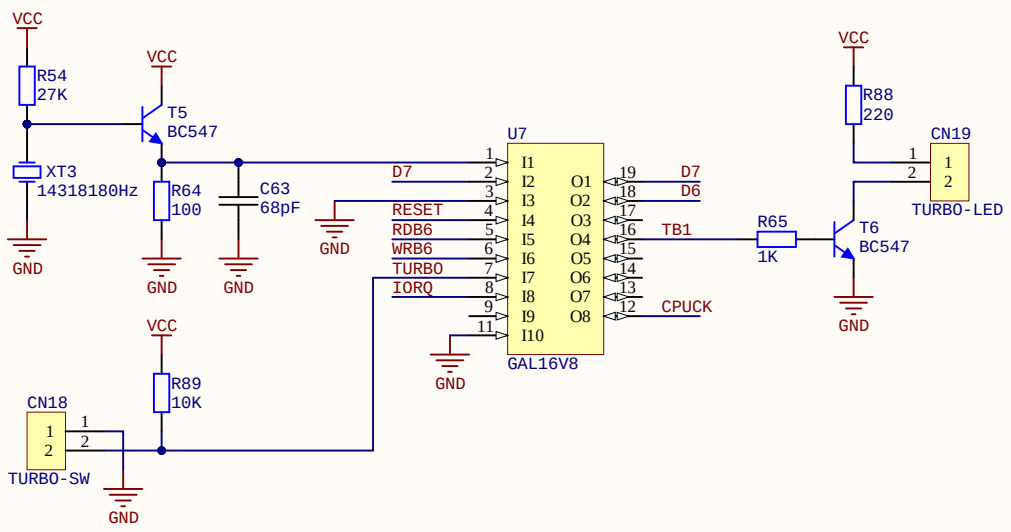
Title			DRAM
Size	Number	Revision	
A4	AC21B24		
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\DRAM.SchDoc	Drawn By: ADEMIR CARCHANO	

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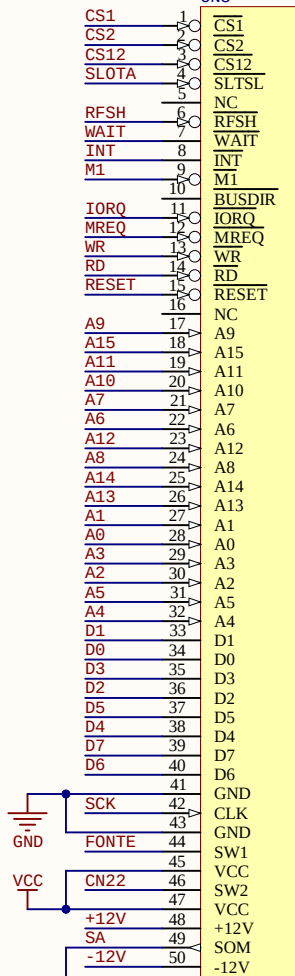


Title			TURBO	
Size	Number	AC21B24		Revision
A4				
Date:	5/16/2022	Sheet of		
File:	D:\SSD240\...\TURBO.SchDoc	Drawn By: ADEMIR CARCHANO		

CN3

CN4

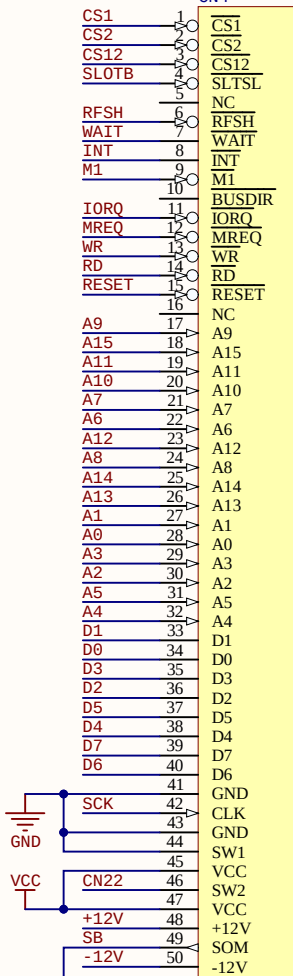
CN9



SLOT-A

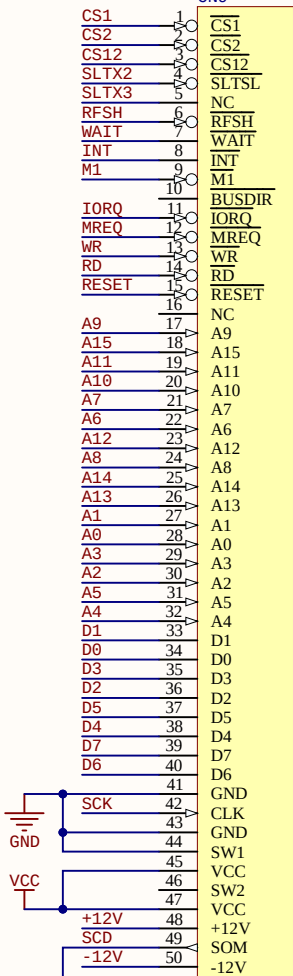
SLOT-B

SLOT-CD

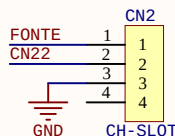
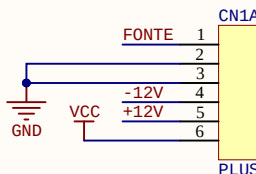
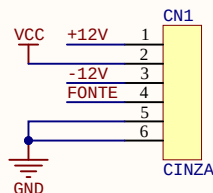
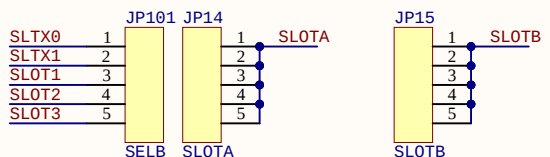
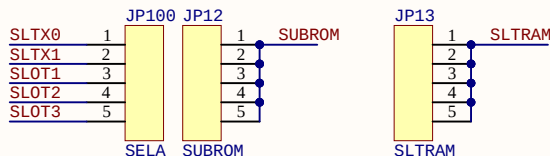


SLOT-B

SLOT-CD



SLOT-CD



Title			CONECTORES DE SLOT	
Size	Number	AC21B24		Revision
Date:	5/16/2022	Sheet of		
File:	D:\SSD240\...\SLOTS.SchDoc	Drawn By: ADEMIR CARCHANO		

1

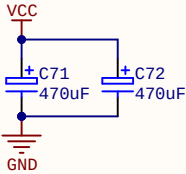
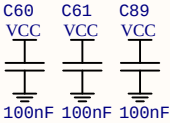
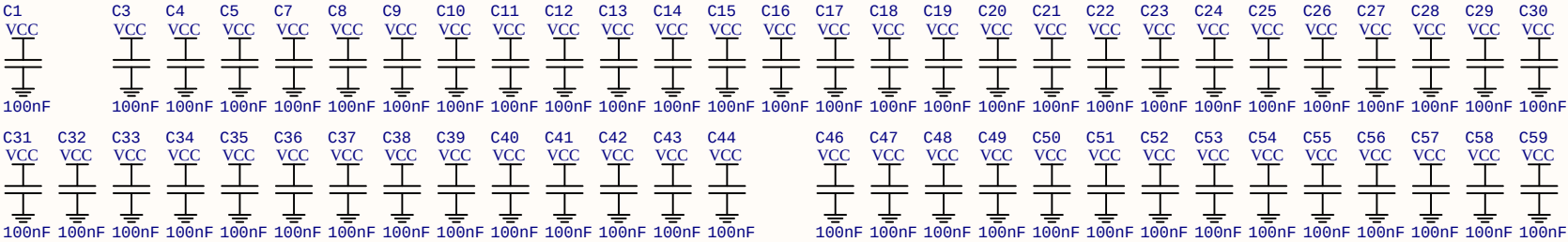
2

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A

A



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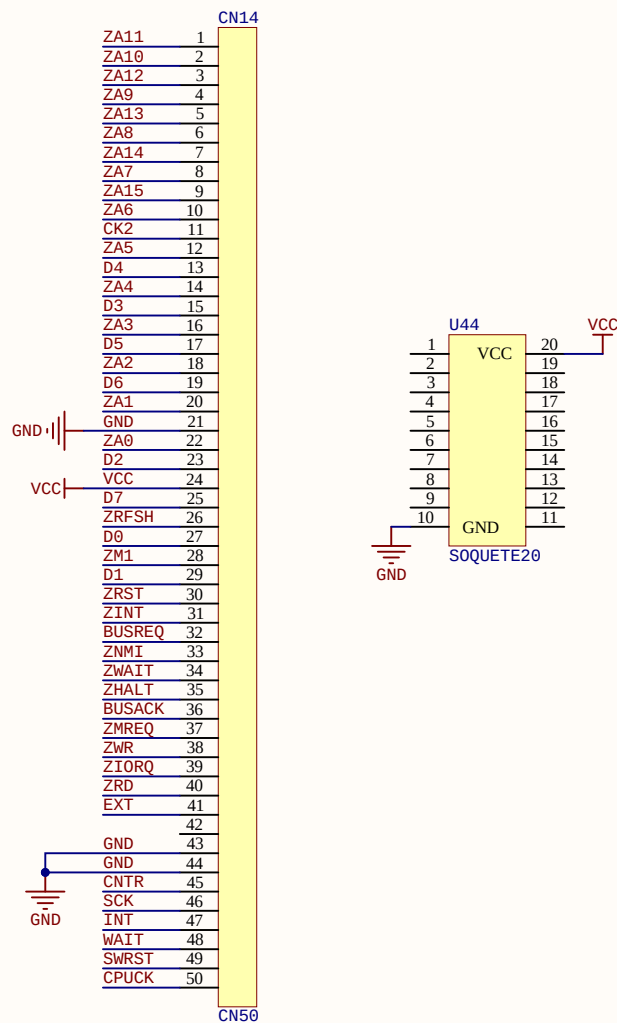
Title			CAPACITORES	
Size	Number	Revision		
A4		AC21B24		
Date:	5/16/2022	Sheet	of	
File:	D:\SSD240\...\CAPS.SchDoc	Drawn By:	ADEMIR CARCHANO	

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Title		CPU EXTERNA	
Size	Number	Revision	
A4	AC21B24		
Date:	5/16/2022	Sheet of	
File:	D:\SSD240\...\CPUEXT.SchDoc	Drawn By:	ADEMIR CARCHANO